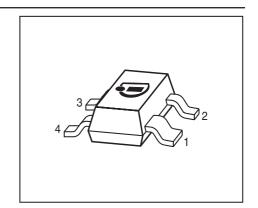


Low Noise Silicon Bipolar RF Transistor

- For low noise, high-gain broadband amplifiers at collector currents from 2 mA to 30 mA
- f_T = 8 GHz, NF_{min} = 0.9 dB at 900 MHz
- Pb-free (RoHS compliant) package
- Qualification report according to AEC-Q101 available





ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration					Package	
BFP183	RHs	1=C	2=E	3=B	4=E	-	-	SOT143

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I _C	65	mA
Base current	/ _B	5	
Total power dissipation ¹⁾	P _{tot}	250	mW
<i>T</i> _S ≤ 76 °C			
Junction temperature	TJ	150	°C
Storage temperature	T_{Stq}	-55 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	295	K/W

 $^{{}^{1}}T_{\rm S}$ is measured on the collector lead at the soldering point to the pcb

 $^{^2}$ For the definition of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)



Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics				•	
Collector-emitter breakdown voltage	V _{(BR)CEO}	12	-	_	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0	, ,				
Collector-emitter cutoff current	I _{CES}	-	-	100	μΑ
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 10 \text{ V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I _{EBO}	-	_	1	μΑ
$V_{\text{EB}} = 1 \text{ V}, I_{\text{C}} = 0$					
DC current gain	h _{FE}	70	100	140	_
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, pulse measured					

2



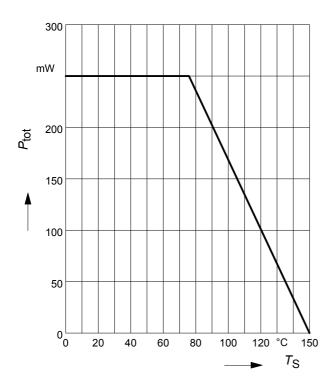
Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol		Unit		
		min.	typ.	max.	
AC Characteristics (verified by random sampling	ng)				
Transition frequency	f _T	6	8	-	GHz
$I_{\rm C}$ = 25 mA, $V_{\rm CE}$ = 8 V, f = 500 MHz					
Collector-base capacitance	C _{cb}	-	0.3	0.5	pF
$V_{\text{CB}} = 10 \text{ V}, f = 1 \text{ MHz}, V_{\text{BE}} = 0$,					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.27	-	
$V_{CE} = 10 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	1.1	-	
$V_{\text{EB}} = 0.5 \text{ V}, f = 1 \text{ MHz}, V_{\text{CB}} = 0$,					
collector grounded					
Minimum noise figure	NF _{min}				dB
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
f = 900 MHz		-	0.9	-	
f = 1.8 GHz		-	1.4	-	
Power gain, maximum stable ¹⁾	G _{ms}	-	22	-	dB
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$, $Z_{\rm L}$ = $Z_{\rm Lopt}$,					
<i>f</i> = 900 MHz					
Power gain, maximum available ¹⁾	G _{ma}	-	15.5	-	dB
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$, $Z_{\rm L}$ = $Z_{\rm Lopt}$,					
f = 1.8 GHz					
Transducer gain	S _{21e} ²				dB
$I_{\rm C}$ = 15 mA, $V_{\rm CE}$ = 8 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
f = 900 MHz		_	17.5	_	
f = 1.8 GHz		_	11.5	_	

 $^{^{1}}G_{\mathsf{ma}} = |S_{21e} \, / \, S_{12e}| \; (\mathsf{k}\text{-}(\mathsf{k}^{2}\text{-}1)^{1/2}), \; G_{\mathsf{ms}} = |S_{21} \, / \, S_{12}|$

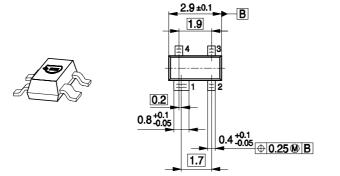


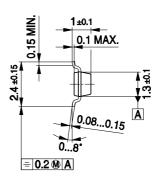
Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$





Package Outline

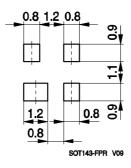




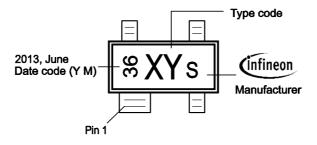
Note: Mold flash, protrusions or gate burrs of 0,2 mm max. per side are not included

SOT143-PO V09

Foot Print

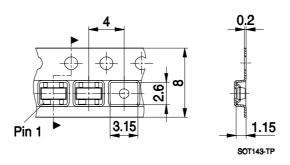


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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