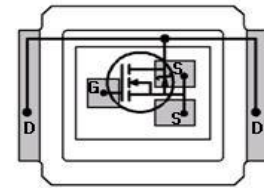


OptiMOS™3 Power-MOSFET
Features

- Optimized technology for DC/DC converters
- Excellent gate charge x $R_{DS(on)}$ product (FOM)
- Low profile (<0.7mm)
- Dual sided cooling
- Low parasitic inductance
- N-channel, normal level

Product Summary

V_{DS}	80	V
$R_{DS(on),max}$	10.4	m Ω
I_D	50	A

**CanPAK™ M
MG-WDSO-2**


Type	Package	Outline	Marking
BSB104N08NP3 G	MG-WDSO-2	MP	0308

Maximum ratings, at $T_j=25\text{ }^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I_D	$V_{GS}=10\text{ V}, T_C=25\text{ }^\circ\text{C}$	50	A
		$V_{GS}=10\text{ V}, T_C=100\text{ }^\circ\text{C}$	32	
		$V_{GS}=10\text{ V}, T_A=25\text{ }^\circ\text{C}, R_{thJA}=45\text{ K/W}^{1)}$	13	
Pulsed drain current ²⁾	$I_{D,pulse}$	$T_C=25\text{ }^\circ\text{C}$	200	
Avalanche energy, single pulse ³⁾	E_{AS}	$I_D=30\text{ A}, R_{GS}=25\text{ }\Omega$	110	mJ
Gate source voltage	V_{GS}		± 20	V

¹⁾ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical in still air.

²⁾ See figure 3 for more detailed information

³⁾ See figure 13 for more detailed information

Maximum ratings, at $T_j=25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Power dissipation	P_{tot}	$T_C=25\text{ °C}$	42	W
		$T_A=25\text{ °C}$, $R_{\text{thJA}}=45\text{ K/W}^{(1)}$	2.8	
Operating and storage temperature	T_j, T_{stg}		-40 ... 150	°C

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Thermal characteristics

Thermal resistance, junction - case	R_{thJC}	bottom	-	1.0	-	K/W
		top	-	-	3.0	
Device on PCB	R_{thJA}	6 cm ² cooling area ⁽¹⁾	-	-	45	

Electrical characteristics, at $T_j=25\text{ °C}$, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{ V}$, $I_{\text{D}}=1\text{ mA}$	80	-	-	V
Gate threshold voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=40\text{ }\mu\text{A}$	2.0	2.7	3.5	
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}}=80\text{ V}$, $V_{\text{GS}}=0\text{ V}$, $T_j=25\text{ °C}$	-	0.1	10	μA
		$V_{\text{DS}}=80\text{ V}$, $V_{\text{GS}}=0\text{ V}$, $T_j=125\text{ °C}$	-	10	100	
Gate-source leakage current	I_{GSS}	$V_{\text{GS}}=20\text{ V}$, $V_{\text{DS}}=0\text{ V}$	-	10	100	nA
Drain-source on-state resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10\text{ V}$, $I_{\text{D}}=10\text{ A}$	-	9.3	10.4	m Ω
Gate resistance	R_{G}		-	2.0	-	Ω
Transconductance	g_{fs}	$ V_{\text{DS}} >2 I_{\text{D}} R_{\text{DS(on)max}}$, $I_{\text{D}}=30\text{ A}$	23	46	-	S

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Dynamic characteristics

Input capacitance	C_{iss}	$V_{GS}=0\text{ V}, V_{DS}=40\text{ V}, f=1\text{ MHz}$	-	1600	2100	pF
Output capacitance	C_{oss}		-	430	570	
Reverse transfer capacitance	C_{rss}		-	18	27	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=40\text{ V}, V_{GS}=10\text{ V}, I_D=30\text{ A}, R_{G,ext}=1.6\ \Omega$	-	9	-	ns
Rise time	t_r		-	4	-	
Turn-off delay time	$t_{d(off)}$		-	19	-	
Fall time	t_f		-	4	-	

Gate Charge Characteristics⁴⁾

Gate to source charge	Q_{gs}	$V_{DD}=40\text{ V}, I_D=30\text{ A}, V_{GS}=0\text{ to }10\text{ V}$	-	8	11	nC
Gate to drain charge	Q_{gd}		-	5	8	
Switching charge	Q_{sw}		-	8	12	
Gate charge total	Q_g		-	23	31	
Gate plateau voltage	$V_{plateau}$		-	5.0	-	V
Output charge	Q_{oss}	$V_{DD}=40\text{ V}, V_{GS}=0\text{ V}$	-	31	41	nC

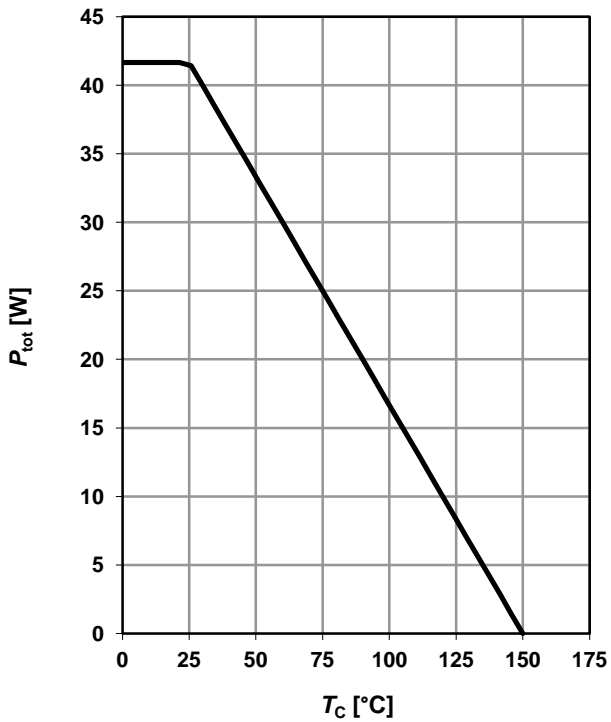
Reverse Diode

Diode continuous forward current	I_S	$T_C=25\text{ }^\circ\text{C}$	-	-	30	A
Diode pulse current	$I_{S,pulse}$		-	-	200	
Diode forward voltage	V_{SD}	$V_{GS}=0\text{ V}, I_F=30\text{ A}, T_J=25\text{ }^\circ\text{C}$	-	0.9	1.2	V
Reverse recovery time	t_{rr}	$V_R=40\text{ V}, I_F=30\text{ A}, di_F/dt=100\text{ A}/\mu\text{s}$	-	43	-	ns
Reverse recovery charge	Q_{rr}		-	55	-	

⁴⁾ See figure 16 for gate charge parameter definition

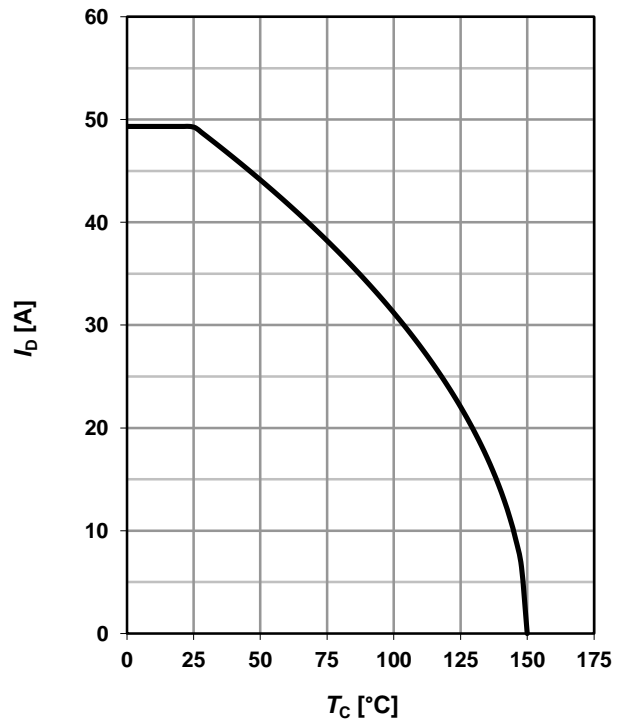
1 Power dissipation

$P_{tot}=f(T_C)$



2 Drain current

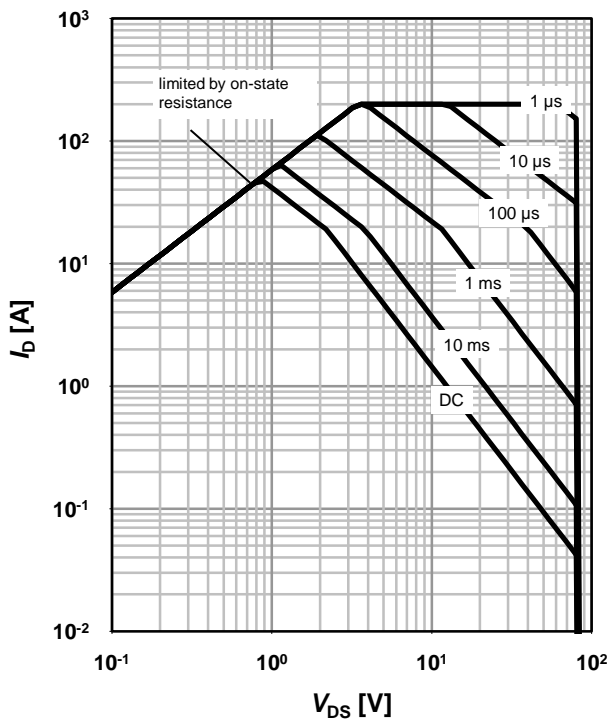
$I_D=f(T_C); V_{GS} \geq 10\text{ V}$



3 Safe operating area

$I_D=f(V_{DS}); T_C=25\text{ °C}; D=0$

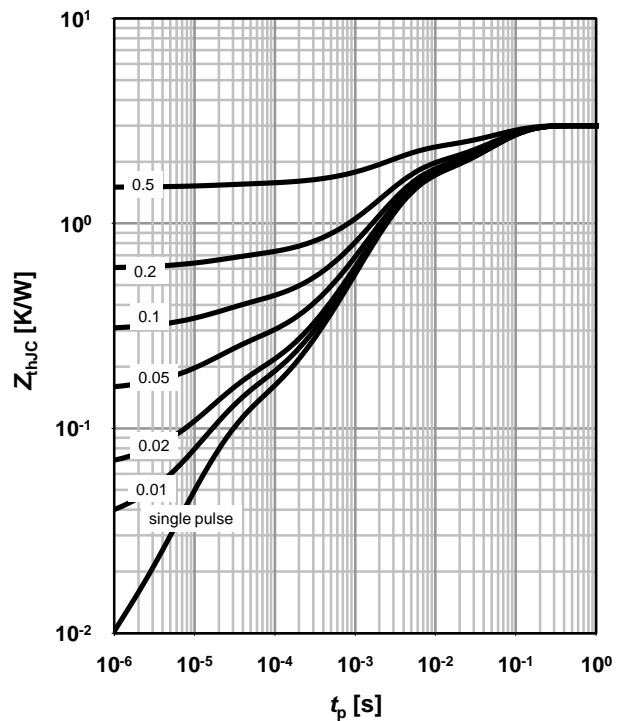
parameter: t_p



4 Max. transient thermal impedance

$Z_{thJC}=f(t_p)$

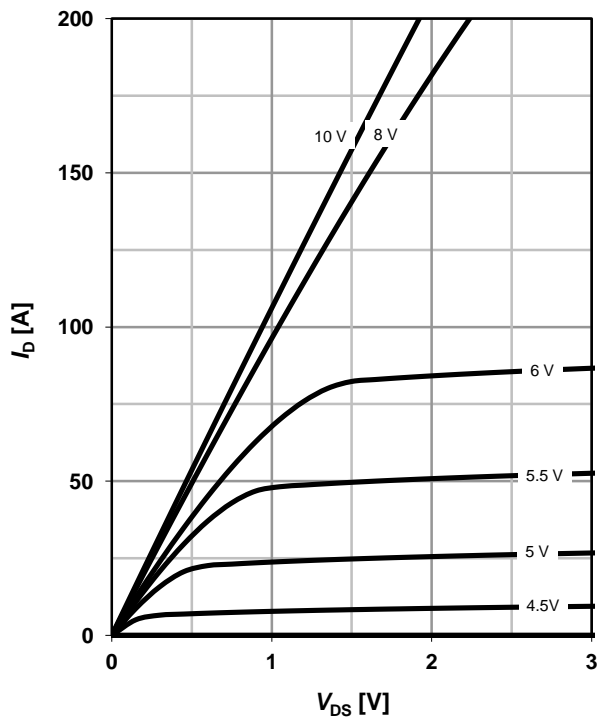
parameter: $D=t_p/T$



5 Typ. output characteristics

$I_D = f(V_{DS}); T_j = 25\text{ °C}$

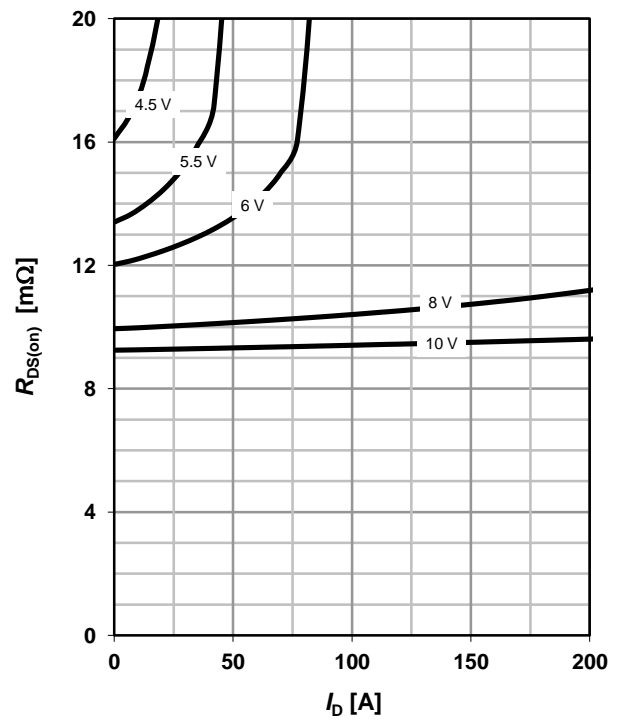
parameter: V_{GS}



6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D); T_j = 25\text{ °C}$

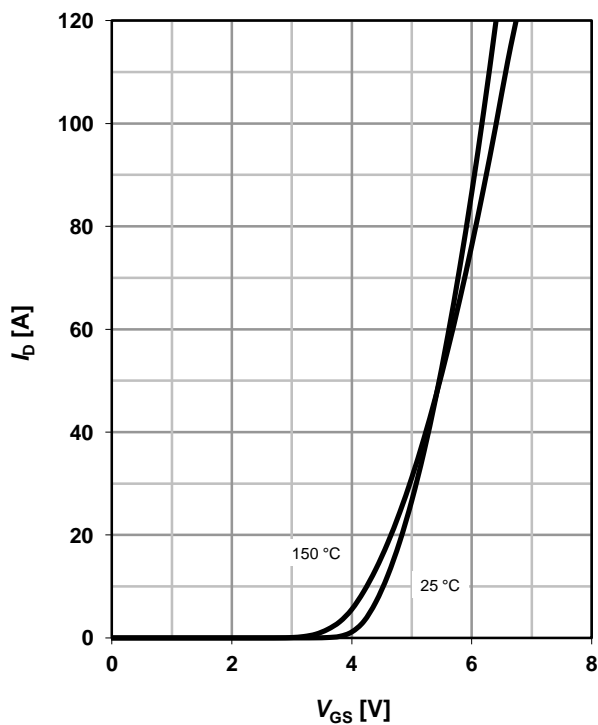
parameter: V_{GS}



7 Typ. transfer characteristics

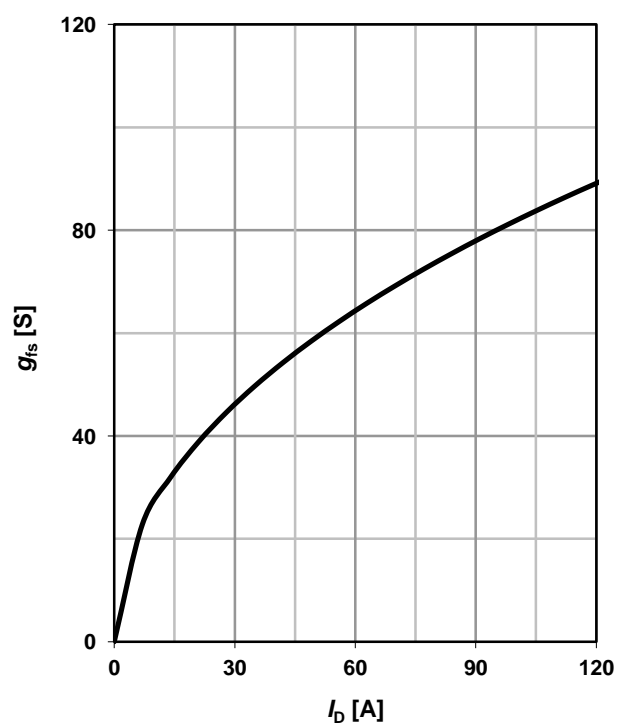
$I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$

parameter: T_j



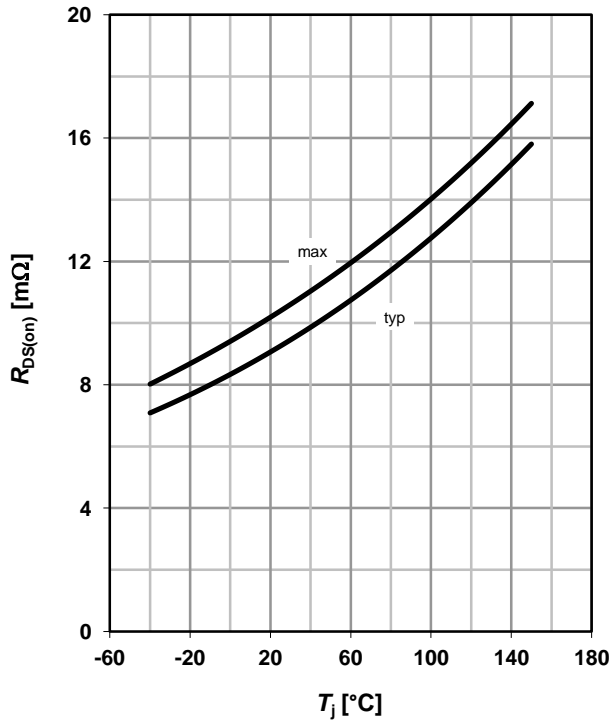
8 Typ. forward transconductance

$g_{fs} = f(I_D); T_j = 25\text{ °C}$



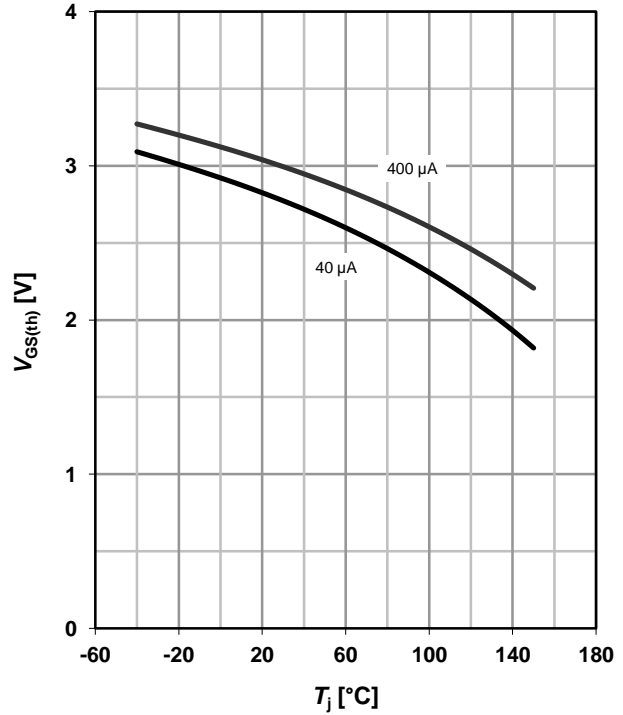
9 Drain-source on-state resistance

$R_{DS(on)}=f(T_j); I_D=10\text{ A}; V_{GS}=10\text{ V}$



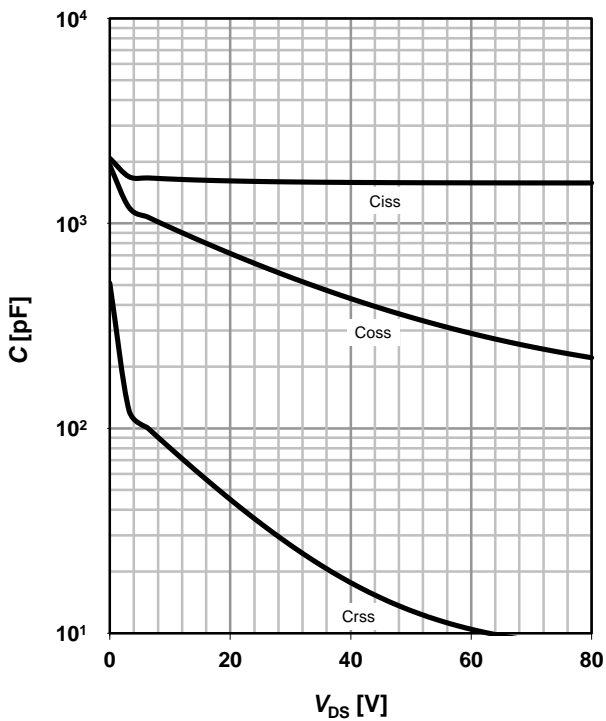
10 Typ. gate threshold voltage

$V_{GS(th)}=f(T_j); V_{GS}=V_{DS}$



11 Typ. capacitances

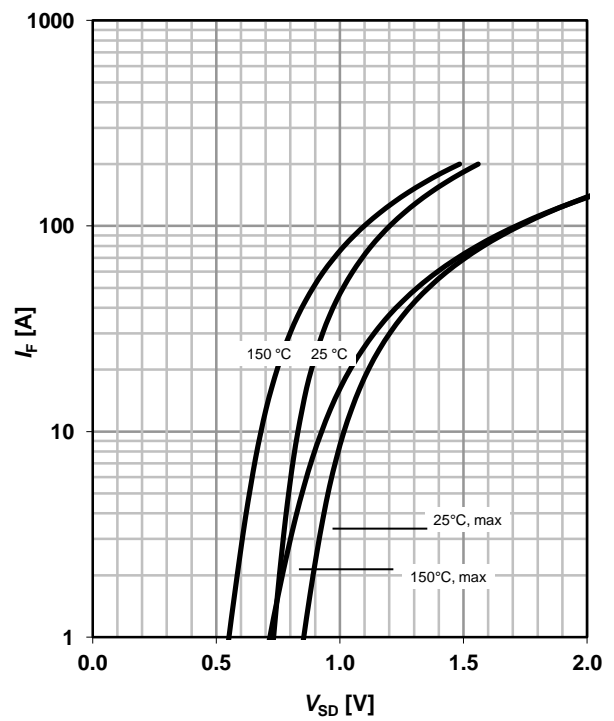
$C=f(V_{DS}); V_{GS}=0\text{ V}; f=1\text{ MHz}$



12 Forward characteristics of reverse diode

$I_F=f(V_{SD})$

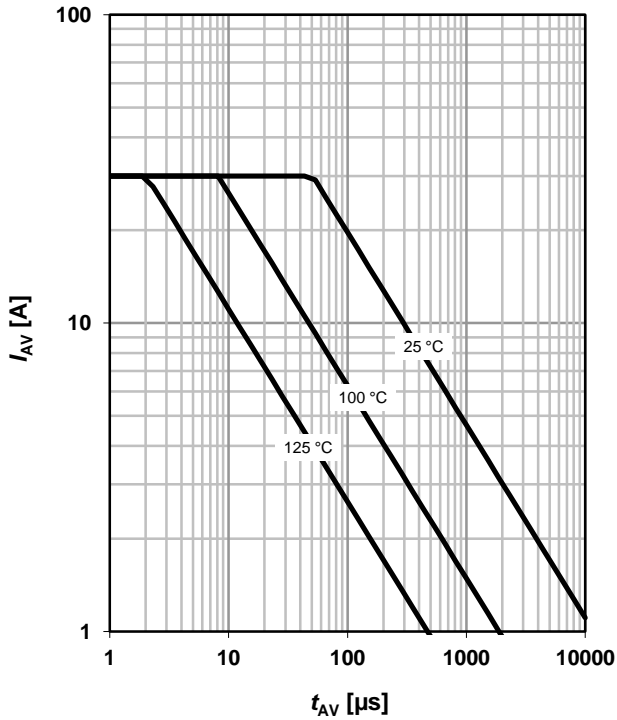
parameter: T_j



13 Avalanche characteristics

$I_{AS}=f(t_{AV}); R_{GS}=25 \Omega$

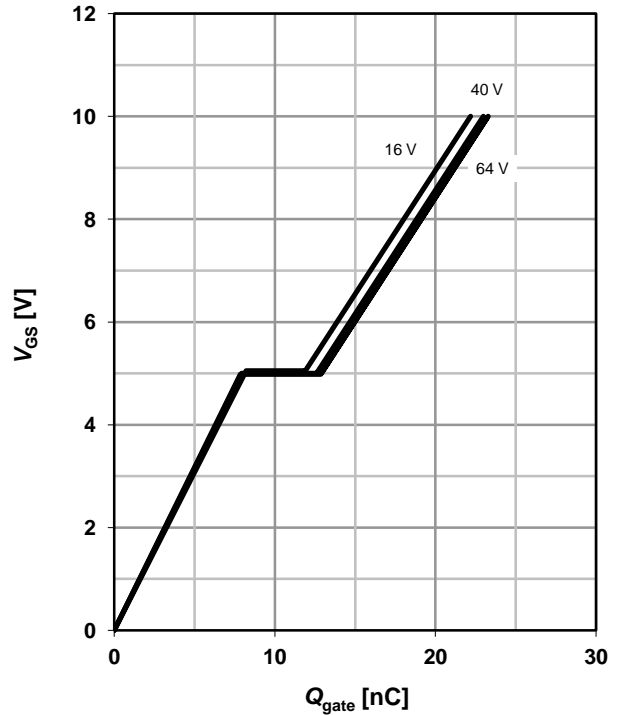
parameter: $T_{j(\text{start})}$



14 Typ. gate charge

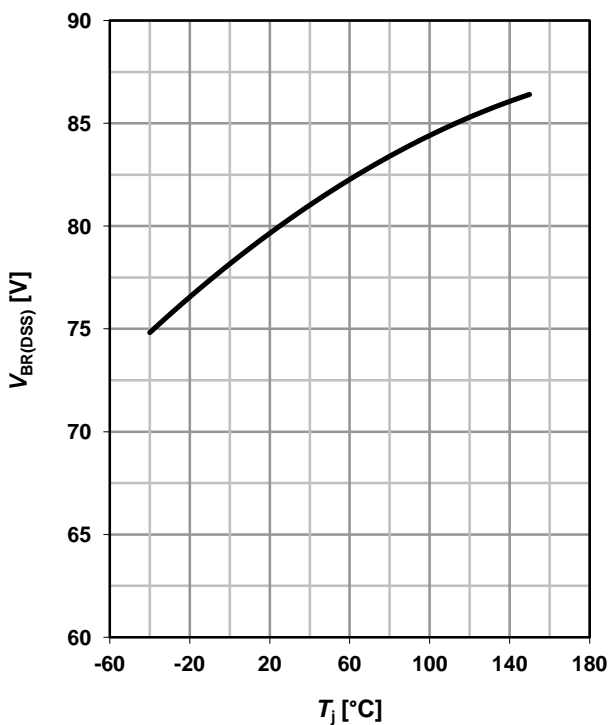
$V_{GS}=f(Q_{\text{gate}}); I_D=30 \text{ A pulsed}$

parameter: V_{DD}



15 Drain-source breakdown voltage

$V_{BR(DSS)}=f(T_j); I_D=1 \text{ mA}$

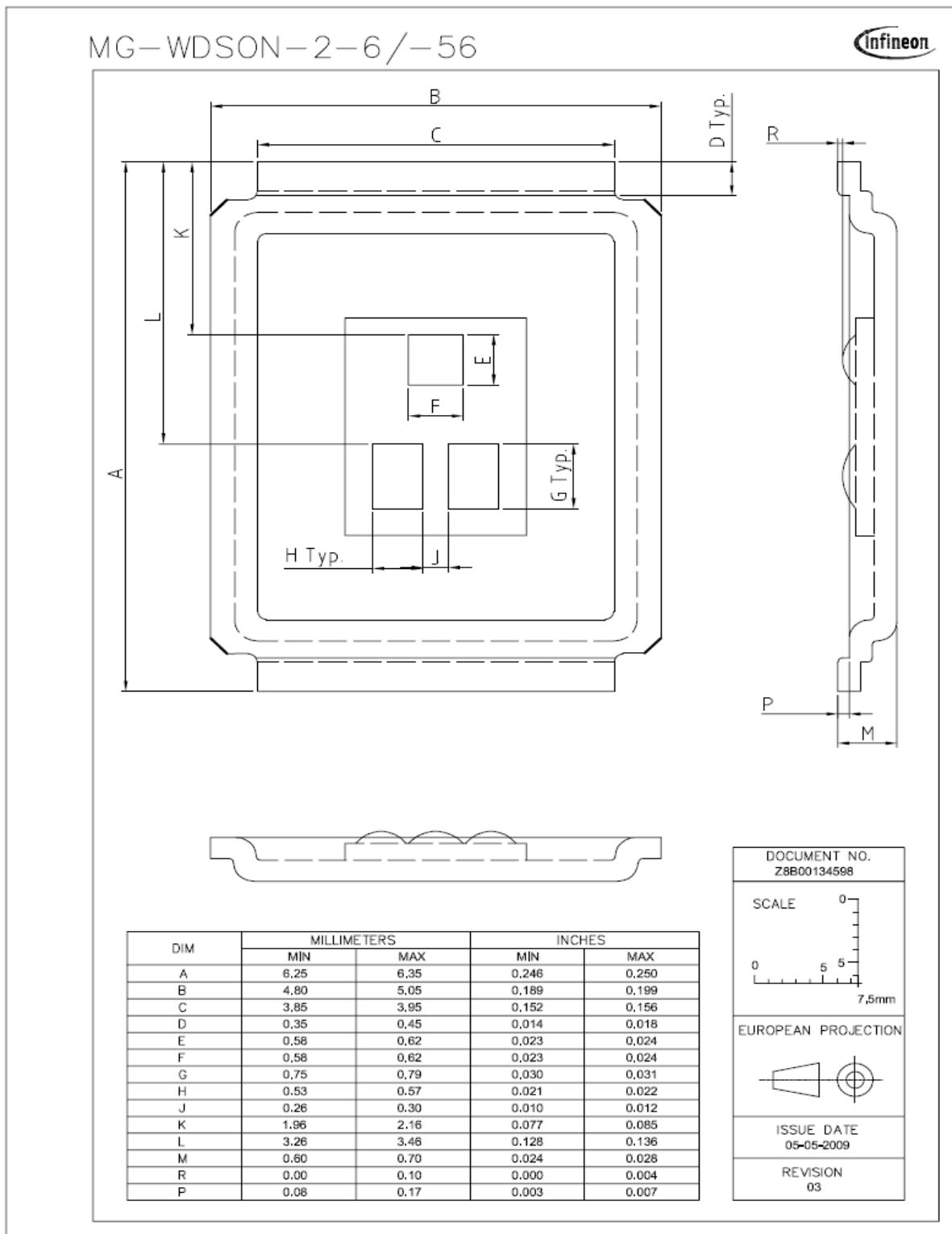


16 Gate charge waveforms

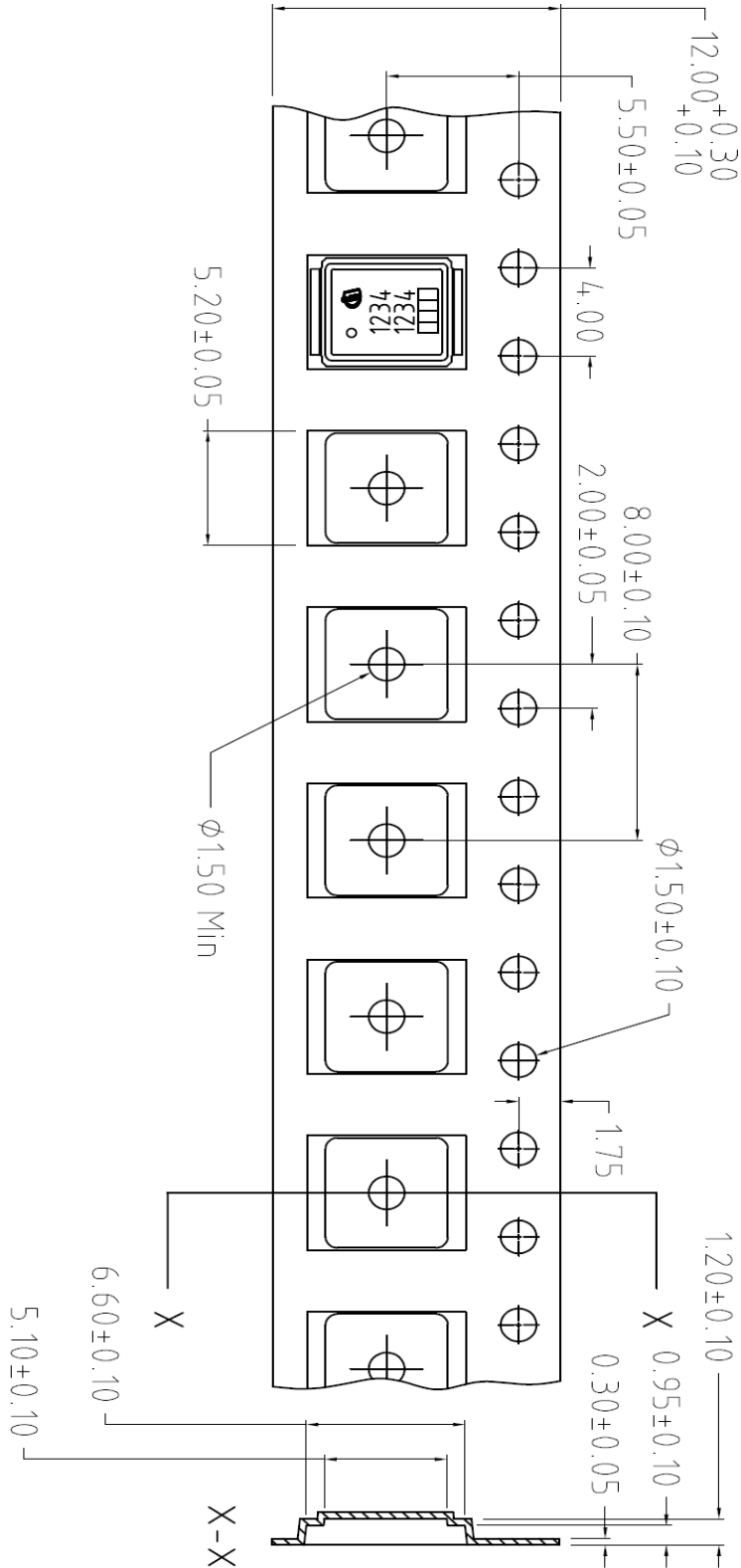


Package Outline

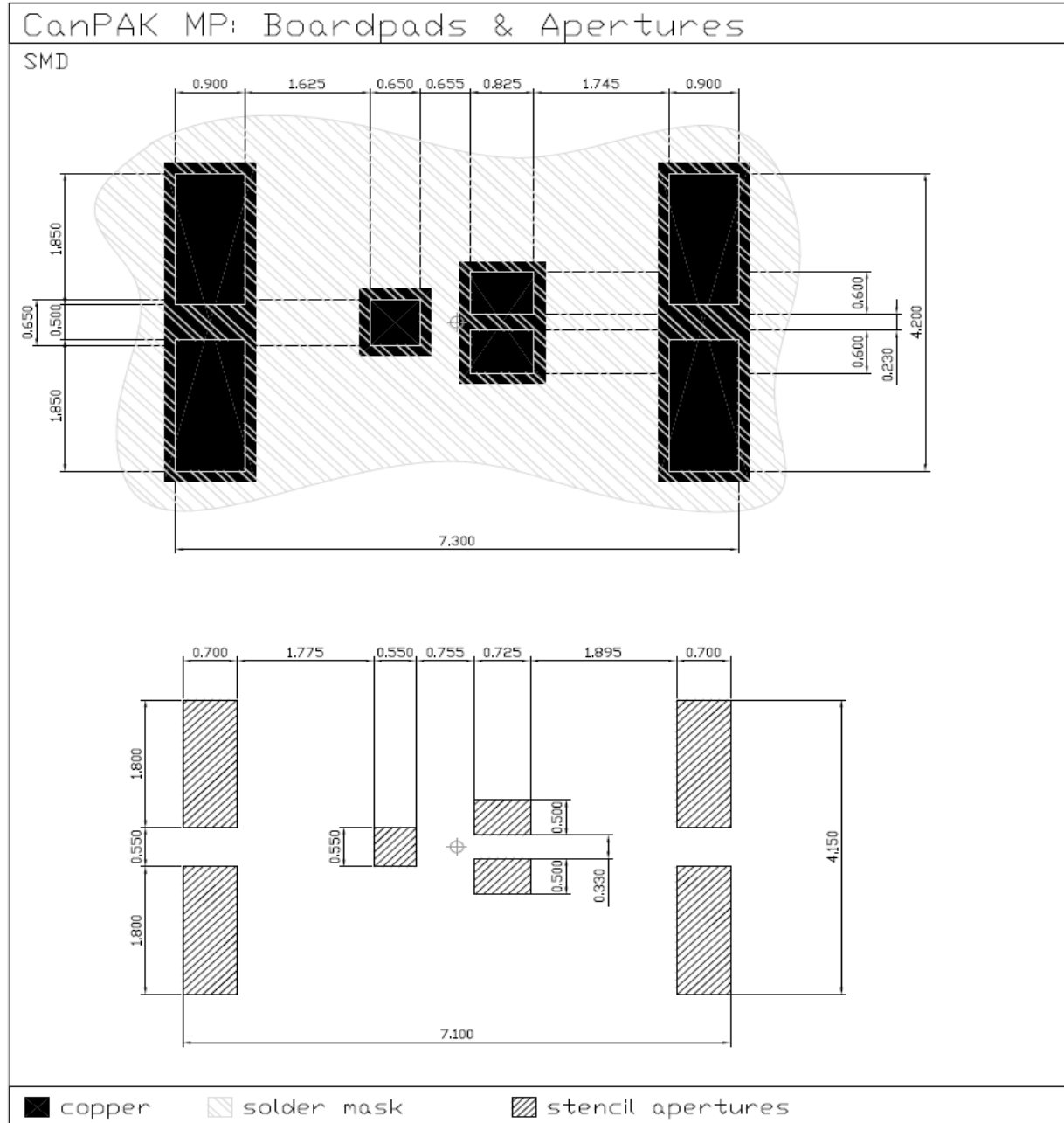
CanPAK™ M
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Dimensions in mm

Raccomended stencil thickness 150 µm

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