

Diode EMCON 4 Medium Power Chip

FEATURES:

- 1200V EMCON 4 technology
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

low / medium power modules



Applications:

low / medium power drives

Chip Type	V_R	I _F	Die Size	Package
IDC28D120T6M	1200V	50A	4.50 x 6.30 mm ²	sawn on foil

MECHANICAL PARAMETER:

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Raster size	4.50 x 6.30				
Area total / active	28.35 / 20.12	mm ²			
Anode pad size	3.546 x 5.346				
Thickness	110	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	518 pcs				
Passivation frontside	Photoimide				
Pad metall	3200 nm AlSiCu				
Backside metall	Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment store in original container, in dry nitrog < 6 month at an ambient temperature of					

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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current limited by T _{jmax}	I _F		1)	Α
Maximum repetitive forward current limited by T _{jmax}	I _{FRM}		100]^
Maximum junction and storage temperature	$T_{\rm vj,max}$, $T_{\rm stg}$		-40+175	°C
Reverse bias safe operating area ²⁾ (RBSOA)	$I_{F,max} = 100A$, $V_{R,max} = 1200V$, $T_{vj,op} \le 150$ °C, $P_{max} = $ tbd kW			

¹⁾ depending on thermal properties of assembly

Static Electrical Characteristics (tested on wafer), T_i =25 °C

Parameter	Symbol	Cond	Value			Unit	
Farailletei	Syllibol	Conditions		min.	Тур.	max.	Oiiit
Reverse leakage current	I_{R}	V _R =1200V	<i>T_j</i> =25°C			10	μΑ
Cathode-Anode breakdown Voltage	V_{Br}	I _R =0.25mA	<i>T_j</i> =25°C	1200			V
Forward voltage drop	V_{F}	I _F =50A	<i>T_j</i> =25°C	1.35	1.7	2.05	V

Dynamic Electrical Characteristics inductive load (not subject to production test - verified by design/characterization)

Parameter	Symbol	Conditions			Value 2)		Unit
raiailletei	Syllibol			min.	Тур.	max.	
Peak reverse recovery current	I _{RM}	$I_F=A$ di/dt=A/ms $V_R=V$ $V_{GE}=-15V$	$T_j = 25 ^{\circ}\text{C}$ $T_j = 125 ^{\circ}\text{C}$ $T_j = 150 ^{\circ}\text{C}$		tbd		А
Reverse recovery charge	Q _r	$I_F=A$ di/dt=A/ms $V_R=V$ $V_{GE}=-15V$	$T_j = 25$ °C $T_j = 125$ °C $T_j = 150$ °C		tbd		μC
Reverse recovery energy	E _{rec}	$I_F=A$ di/dt=A/ms $V_R=V$ $V_{GE}=-15V$	$T_j = 25 ^{\circ}\text{C}$ $T_j = 125 ^{\circ}\text{C}$ $T_j = 150 ^{\circ}\text{C}$		tbd		mJ

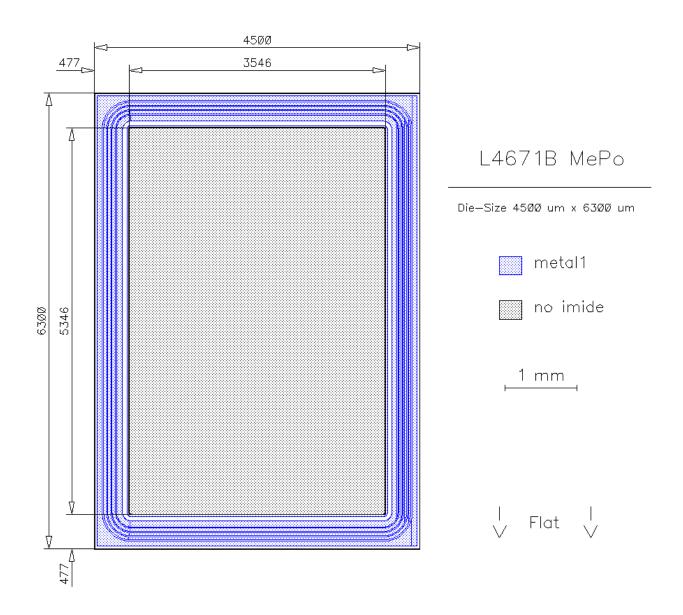
²⁾ values also influenced by parasitic L- and C- in measurement and package.

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²⁾ not subject to production test - verified by design/characterisation



CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:		
This chip data sheet refers to the device data sheet	tbd	
Description:		
AQL 0,65 for visual inspection according to failure	catalog	
Electrostatic Discharge Sensitive Device according	g to MIL-STD 883	

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