

Fast IGBT in NPT-technology

- lower *E*_{off} compared to previous generation
- \bullet Short circuit withstand time 10 μs
- Designed for:
 - Motor controls
 - Inverter
 - SMPS
- NPT-Technology offers:
 - very tight parameter distribution
 - high ruggedness, temperature stable behaviour
 - parallel switching capability



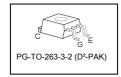
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : http://www.infineon.com/igbt/

Туре	V _{CE}	I _C	$oldsymbol{\mathcal{E}}_{off}$	T _j	Marking	Package
SGB07N120	1200V	8A	0.7mJ	150°C	GB07N120	PG-TO-263-3-2

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CE}	1200	V
DC collector current	I _C		Α
<i>T</i> _C = 25°C		16.5	
$T_{\rm C}$ = 100°C		7.9	
Pulsed collector current, t_p limited by T_{jmax}	I _{Cpuls}	27	
Turn off safe operating area	-	27	
$V_{CE} \le 1200 \text{V}, \ T_j \le 150^{\circ} \text{C}$			
Gate-emitter voltage	V _{GE}	±20	V
Avalanche energy, single pulse	E _{AS}	40	mJ
$I_{\rm C}$ = 8A, $V_{\rm CC}$ = 50V, $R_{\rm GE}$ = 25 Ω , start at $T_{\rm j}$ = 25 $^{\circ}$ C			
Short circuit withstand time ²	tsc	10	μs
$V_{\rm GE}$ = 15V, 100V $\leq V_{\rm CC} \leq$ 1200V, $T_{\rm j} \leq$ 150°C			
Power dissipation	P _{tot}	125	W
<i>T</i> _C = 25°C			
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$	-55+150	°C
Soldering temperature (reflow soldering, MSL1)	T _s	245	





¹ J-STD-020 and JESD-022

² Allowed number of short circuits: <1000; time between short circuits: >1s.



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Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic		,		1
IGBT thermal resistance,	R_{thJC}		1	K/W
junction – case				
Thermal resistance,	R_{thJA}		40	
junction – ambient ¹⁾				

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions		Unit		
Parameter	Symbol	Conditions	min.	typ.	max.	
Static Characteristic	•					
Collector-emitter breakdown voltage	V _{(BR)CES}	$V_{\rm GE}$ =0V, $I_{\rm C}$ =500 μ A	1200	-	-	V
Collector-emitter saturation voltage	V _{CE(sat)}	$V_{\rm GE} = 15 \rm V, I_{\rm C} = 8 \rm A$				
		<i>T</i> _j =25°C	2.5	3.1	3.6	
		T _j =150°C	-	3.7	4.3	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C} = 350 \mu A, V_{\rm CE} = V_{\rm GE}$	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =1200V,V _{GE} =0V				μА
		<i>T</i> _j =25°C	-	-	100	
		T _j =150°C	-	-	400	
Gate-emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =20V	-	-	100	nA
Transconductance	g_{fs}	$V_{\rm CE} = 20 \text{V}, I_{\rm C} = 8 \text{A}$		6	-	S
Dynamic Characteristic						
Input capacitance	Ciss	V _{CE} =25V,	ı	720	870	pF
Output capacitance	Coss	$V_{GE}=0V$,	1	60	75	
Reverse transfer capacitance	Crss	<i>f</i> =1MHz	1	40	50	
Gate charge	Q _{Gate}	$V_{\rm CC}$ =960V, $I_{\rm C}$ =8A	-	70	90	nC
		V _{GE} =15V				
Internal emitter inductance	LE		-	7	-	nH
measured 5mm (0.197 in.) from case						
Short circuit collector current ²⁾	$I_{C(SC)}$	$V_{\text{GE}} = 15 \text{V}, t_{\text{SC}} \le 10 \mu \text{s}$ $100 \text{V} \le V_{\text{CC}} \le 1200 \text{V},$ $T_{\text{j}} \le 150 ^{\circ} \text{C}$	-	75	-	A

 $^{^{1)}}$ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for collector connection. PCB is vertical without blown air. $^{2)}$ Allowed number of short circuits: <1000; time between short circuits: >1s.



Switching Characteristic, Inductive Load, at $\textit{T}_{j}\text{=-}25~^{\circ}\text{C}$

Parameter	Symbol	Conditions		I I mit		
			min.	typ.	max.	Unit
IGBT Characteristic	<u>.</u>					•
Turn-on delay time	t _{d(on)}	T _j =25°C,	-	27	35	ns
Rise time	t _r	$V_{CC} = 800 \text{V}, I_{C} = 8\text{A},$	-	29	38	
Turn-off delay time	t _{d(off)}	$V_{GE} = 15V/0V,$ $R_{G} = 47\Omega,$ $L_{\sigma}^{(1)} = 180nH,$ $C_{\sigma}^{(1)} = 40pF$	-	440	570	
Fall time	t _f		-	21	27	
Turn-on energy	Eon		-	0.6	8.0	mJ
Turn-off energy	E _{off}	Energy losses include	_	0.4	0.55	
Total switching energy	E _{ts}	"tail" and diode reverse recovery.	-	1.0	1.35	

Switching Characteristic, Inductive Load, at T_i =150 °C

Parameter	Symbol	Conditions	Value			I I m i 4
			min.	typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T _j =150°C	-	30	36	ns
Rise time	$t_{\rm r}$	$V_{\rm CC}$ =800V, $I_{\rm C}$ =8A, $V_{\rm GE}$ =15V/0V, $R_{\rm G}$ =47 Ω , $L_{\sigma}^{1)}$ =180nH, $C_{\sigma}^{1)}$ =40pF Energy losses include	-	26	31	
Turn-off delay time	$t_{d(off)}$		-	490	590	
Fall time	t_{f}		-	30	36	
Turn-on energy	Eon		-	1.0	1.2	mJ
Turn-off energy	E _{off}		-	0.7	0.9	
Total switching energy	Ets	"tail" and diode reverse recovery.	-	1.7	2.1	

 $^{^{1)}}$ Leakage inductance L_{σ} and stray capacity C_{σ} due to dynamic test circuit in figure E.



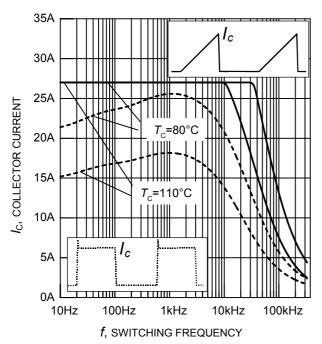
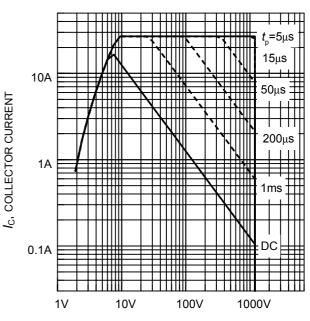


Figure 1. Collector current as a function of switching frequency

 $(T_{\rm j} \le 150^{\circ}\text{C}, D = 0.5, V_{\rm CE} = 800\text{V}, V_{\rm GE} = +15\text{V}/0\text{V}, R_{\rm G} = 47\Omega)$



 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area $(D = 0, T_C = 25^{\circ}C, T_i \le 150^{\circ}C)$

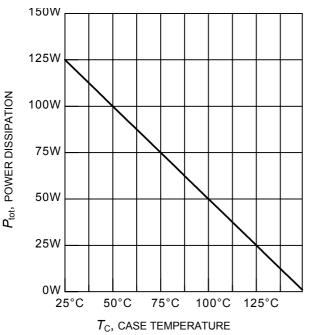


Figure 3. Power dissipation as a function of case temperature

 $(T_{\rm j} \leq 150^{\circ}{\rm C})$

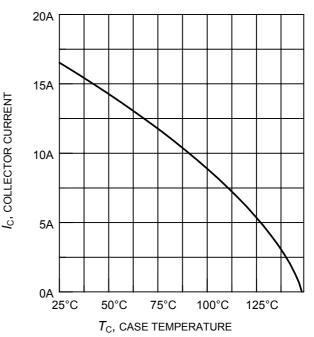


Figure 4. Collector current as a function of case temperature

 $(\textit{V}_{\text{GE}} \leq 15\text{V}, \; \textit{T}_{j} \leq 150^{\circ}\text{C})$



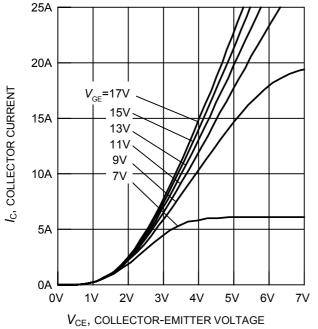


Figure 5. Typical output characteristics $(T_i = 25^{\circ}C)$

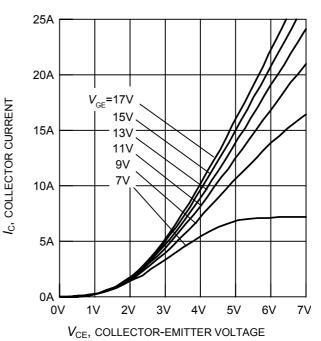


Figure 6. Typical output characteristics $(T_i = 150^{\circ}\text{C})$

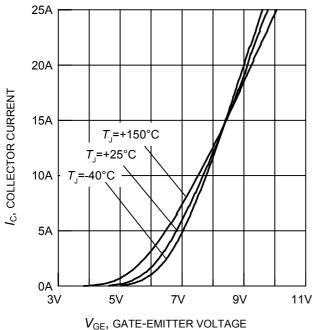


Figure 7. Typical transfer characteristics ($V_{CE} = 20V$)

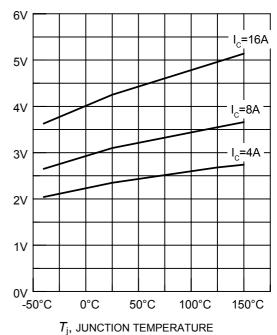


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature ($V_{\rm GE}$ = 15V)

 $V_{\text{CE(sat)}}$, COLLECTOR-EMITTER SATURATION VOLTAGE



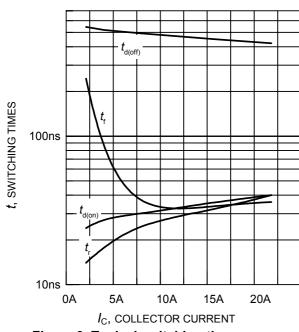


Figure 9. Typical switching times as a function of collector current (inductive load, $T_{\rm j}$ = 150°C, $V_{\rm CE}$ = 800V, $V_{\rm GE}$ = +15V/0V, $R_{\rm G}$ = 47 Ω , dynamic test circuit in Fig.E)

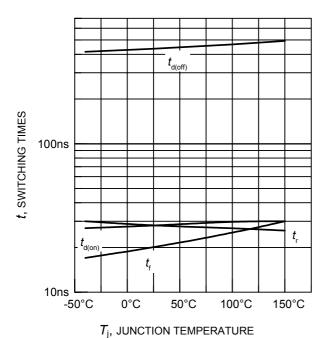


Figure 11. Typical switching times as a function of junction temperature (inductive load, V_{CE} = 800V, V_{GE} = +15V/0V, I_{C} = 8A, R_{G} = 47 Ω , dynamic test circuit in Fig.E)

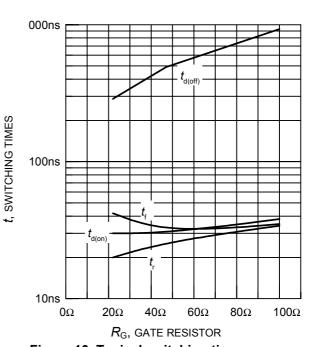


Figure 10. Typical switching times as a function of gate resistor (inductive load, T_j = 150°C, V_{CE} = 800V, V_{GE} = +15V/0V, I_C = 8A, dynamic test circuit in Fig.E)

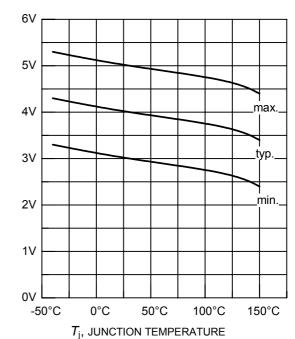


Figure 12. Gate-emitter threshold voltage as a function of junction temperature $(I_C = 0.3 \text{mA})$

 $V_{\text{GE(th)}}$, GATE-EMITTER THRESHOLD VOLTAGE



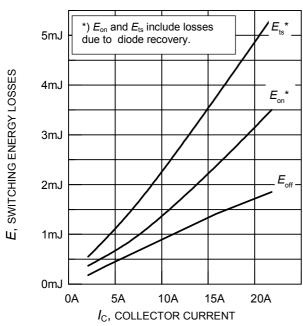


Figure 13. Typical switching energy losses as a function of collector current (inductive load, T_j = 150°C, V_{CE} = 800V, V_{GE} = +15V/0V, R_G = 47 Ω , dynamic test circuit in Fig.E)

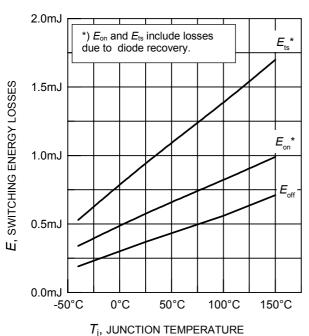


Figure 15. Typical switching energy losses as a function of junction temperature (inductive load, $V_{\text{CE}} = 800\text{V}$, $V_{\text{GE}} = +15\text{V}/0\text{V}$, $I_{\text{C}} = 8\text{A}$, $R_{\text{G}} = 47\Omega$, dynamic test circuit in Fig.E)

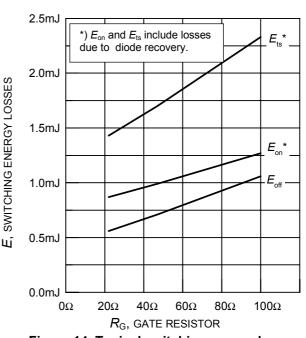


Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, T_j = 150°C, V_{CE} = 800V, V_{GE} = +15V/0V, I_C = 8A, dynamic test circuit in Fig.E)

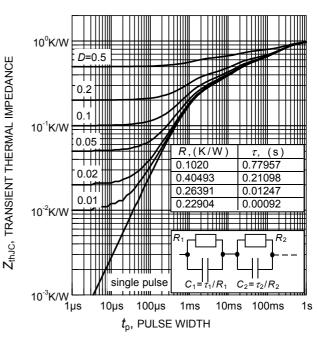


Figure 16. IGBT transient thermal impedance as a function of pulse width $(D = t_p / T)$



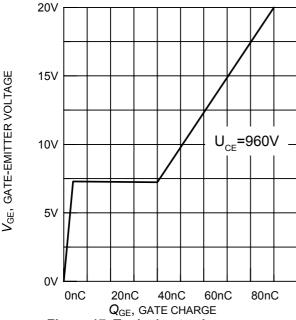


Figure 17. Typical gate charge $(I_C = 8A)$

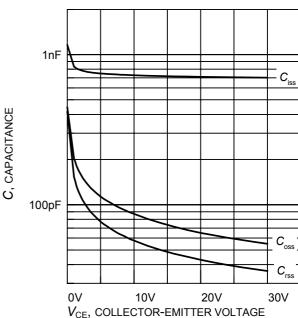


Figure 18. Typical capacitance as a function of collector-emitter voltage $(V_{GE} = 0V, f = 1MHz)$

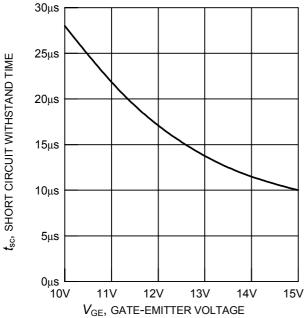


Figure 19. Short circuit withstand time as a function of gate-emitter voltage ($V_{CE} = 1200V$, start at $T_i = 25^{\circ}C$)

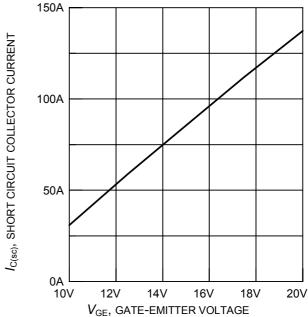
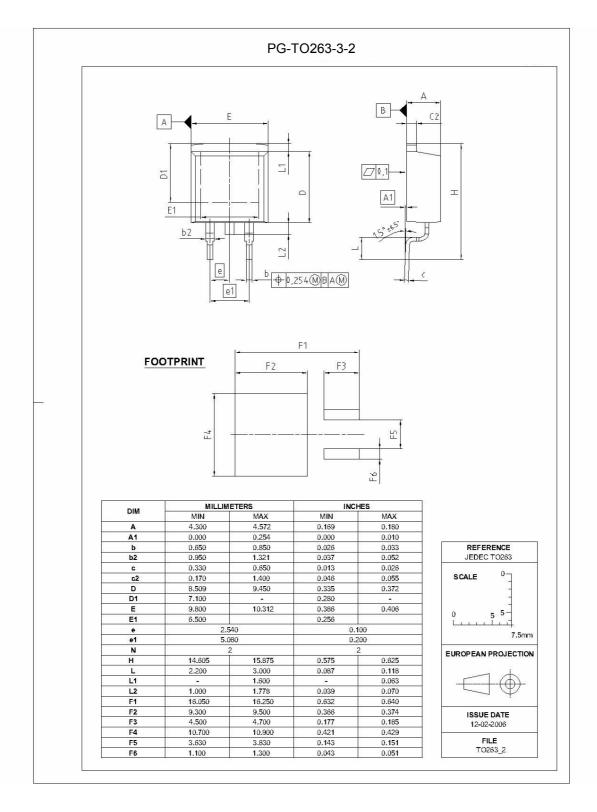
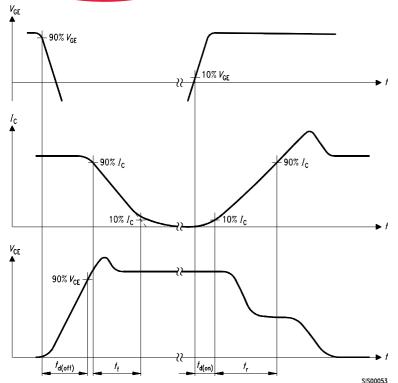


Figure 20. Typical short circuit collector current as a function of gate-emitter voltage $(100V \le V_{CE} \le 1200V, T_C = 25^{\circ}C, T_i \le 150^{\circ}C)$









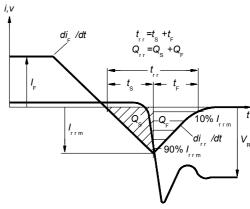


Figure C. Definition of diodes switching characteristics

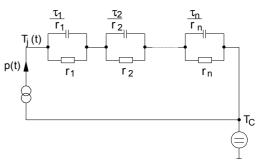


Figure A. Definition of switching times

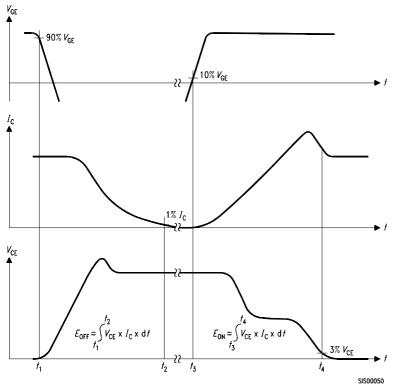


Figure D. Thermal equivalent circuit

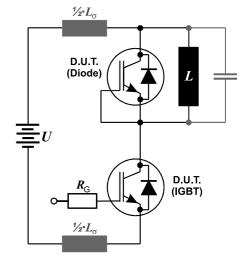


Figure B. Definition of switching losses

Figure E. Dynamic test circuit Leakage inductance L_{σ} =180nH, and stray capacity C_{σ} =40pF.



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