

### High speed 1200 V TRENCHSTOP™ IGBT 7 Technology co-packed with full rated current, soft-commutating, ultra-fast recovery and low Q<sub>rr</sub> emitter controlled 7 Rapid diode

#### Features

- V<sub>CE</sub> = 1200 V
- I<sub>C</sub> = 75 A
- Maximum junction temperature T<sub>vjmax</sub> = 175°C
- Best-in-class high speed IGBT co-packed with full rated current, low Q<sub>rr</sub> and soft-commutating high speed diode
- Low saturation voltage V<sub>CEsat</sub> = 1.7 V at T<sub>vj</sub> = 25°C
- Optimized for high efficiency in high speed hard switching topologies (2-L inverter, 3-L NPC T-type, ...)
- Easy paralleling capability due to positive temperature coefficient in V<sub>CEsat</sub>
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models: <http://www.infineon.com/igbt/>

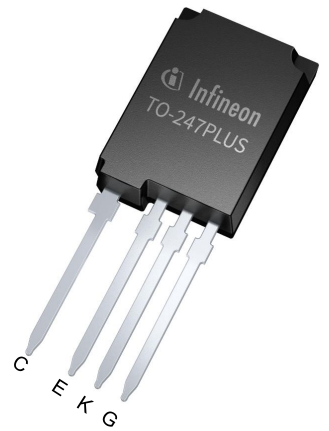
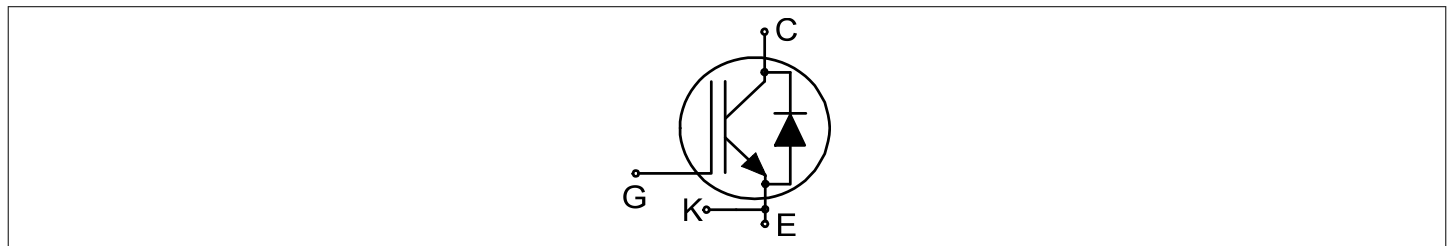
#### Potential applications

- Industrial UPS
- EV-Charging
- String inverter
- Welding

#### Product validation

- Qualified for industrial applications according to the relevant tests of JEDEC47/20/22

#### Description



- Lead-free
- Green
- Halogen-free
- RoHS

Type	Package	Marking
IKY75N120CH7	PG-TO247-4-PLUS-NN5.1	K75MCH7

## Table of contents

	<b>Description</b> .....	1
	<b>Features</b> .....	1
	<b>Potential applications</b> .....	1
	<b>Product validation</b> .....	1
	<b>Table of contents</b> .....	2
<b>1</b>	<b>Package</b> .....	3
<b>2</b>	<b>IGBT</b> .....	3
<b>3</b>	<b>Diode</b> .....	5
<b>4</b>	<b>Characteristics diagrams</b> .....	7
<b>5</b>	<b>Package outlines</b> .....	14
<b>6</b>	<b>Testing conditions</b> .....	15
	<b>Revision history</b> .....	16
	<b>Disclaimer</b> .....	17

## 1 Package

**Table 1** Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Internal emitter inductance measured 5 mm (0.197 in.) from case	$L_E$			13		nH
Storage temperature	$T_{stg}$		-55		150	°C
Soldering temperature	$T_{sold}$	wave soldering 1.6 mm (0.063 in.) from case for 10 s			260	°C
Thermal resistance, junction-ambient	$R_{th(j-a)}$				40	K/W
IGBT thermal resistance, junction-case	$R_{th(j-c)}$			0.21	0.27	K/W
Diode thermal resistance, junction-case	$R_{th(j-c)}$			0.37	0.48	K/W

## 2 IGBT

**Table 2** Maximum rated values

Parameter	Symbol	Note or test condition	Values	Unit	
Collector-emitter voltage	$V_{CE}$	$T_{vj} \geq 25 \text{ °C}$	1200	V	
DC collector current, limited by $T_{vjmax}$	$I_C$	limited by bondwire	$T_c = 25 \text{ °C}$	83	A
			$T_c = 100 \text{ °C}$	75	
Pulsed collector current, $t_p$ limited by $T_{vjmax}$	$I_{Cpulse}$		300	A	
Turn-off safe operating area		$V_{CC} \leq 800 \text{ V}$ , $V_{CE,peak} < 1200 \text{ V}$ , $V_{GE} = 0/15 \text{ V}$ , $R_{Goff} \geq 18 \text{ } \Omega$ , $T_{vj} \leq 175 \text{ °C}$	300	A	
Gate-emitter voltage	$V_{GE}$		$\pm 20$	V	
Transient gate-emitter voltage	$V_{GE}$	$t_p \leq 0.5 \text{ } \mu\text{s}$ , $D < 0.001$	$\pm 25$	V	
Power dissipation	$P_{tot}$		$T_c = 25 \text{ °C}$	549	W
			$T_c = 100 \text{ °C}$	275	

**Table 3** Characteristic values

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Collector-emitter saturation voltage	$V_{CEsat}$	$I_C = 75 \text{ A}$ , $V_{GE} = 15 \text{ V}$	$T_{vj} = 25 \text{ °C}$	1.7	2.15	V
			$T_{vj} = 175 \text{ °C}$	2		

(table continues...)

**Table 3 (continued) Characteristic values**

Parameter	Symbol	Note or test condition	Values			Unit
			Min.	Typ.	Max.	
Gate-emitter threshold voltage	$V_{GEth}$	$I_C = 1.2 \text{ mA}, V_{CE} = V_{GE}$	4.7	5.5	6.2	V
Zero gate-voltage collector current	$I_{CES}$	$V_{CE} = 1200 \text{ V}, V_{GE} = 0 \text{ V}$	$T_{vj} = 25 \text{ °C}$		40	$\mu\text{A}$
			$T_{vj} = 175 \text{ °C}$		4600	
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0 \text{ V}, V_{GE} = 20 \text{ V}$			100	nA
Transconductance	$g_{fs}$	$I_C = 75 \text{ A}, V_{CE} = 20 \text{ V}$		192		S
Input capacitance	$C_{ies}$	$V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}$		9.6		nF
Output capacitance	$C_{oes}$	$V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}$		184		pF
Reverse transfer capacitance	$C_{res}$	$V_{CE} = 25 \text{ V}, V_{GE} = 0 \text{ V}, f = 100 \text{ kHz}$		54		pF
Gate charge	$Q_G$	$I_C = 75 \text{ A}, V_{GE} = 15 \text{ V}, V_{CC} = 960 \text{ V}$		550		nC
Turn-on delay time	$t_{d(on)}$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		42	ns
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		40	
Rise time (inductive load)	$t_r$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		14	ns
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		18	
Turn-off delay time	$t_{d(off)}$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		367	ns
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		446	
Fall time (inductive load)	$t_f$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		38	ns
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		111	
Turn-on energy	$E_{on}$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		1.81	mJ
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		3.1	
Turn-off energy	$E_{off}$	$V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_{G(on)} = 5.3 \text{ }\Omega, R_{G(off)} = 5.3 \text{ }\Omega$	$T_{vj} = 25 \text{ °C}, I_C = 75 \text{ A}$		2.02	mJ
			$T_{vj} = 175 \text{ °C}, I_C = 75 \text{ A}$		4.15	

(table continues...)

**Table 3 (continued) Characteristic values**

Parameter	Symbol	Note or test condition	Values			Unit	
			Min.	Typ.	Max.		
Total switching energy	$E_{ts}$	$V_{CC} = 600\text{ V}, V_{GE} = 0/15\text{ V},$ $R_{G(on)} = 5.3\ \Omega,$ $R_{G(off)} = 5.3\ \Omega$	$T_{vj} = 25\ ^\circ\text{C},$ $I_C = 75\text{ A}$		3.83		mJ
			$T_{vj} = 175\ ^\circ\text{C},$ $I_C = 75\text{ A}$		7.25		
Operating junction temperature	$T_{vj}$		-40		175	$^\circ\text{C}$	

Note: Electrical Characteristic, at  $T_{vj} = 25\ ^\circ\text{C}$ , unless otherwise specified.

### 3 Diode

**Table 4 Maximum rated values**

Parameter	Symbol	Note or test condition	Values	Unit	
Diode forward current, limited by $T_{vjmax}$	$I_F$	limited by bondwire	$T_c = 25\ ^\circ\text{C}$	78	A
			$T_c = 97\ ^\circ\text{C}$	75	
Diode pulsed current, $t_p$ limited by $T_{vjmax}$	$I_{Fpulse}$		300	A	
Power dissipation	$P_{tot}$		$T_c = 25\ ^\circ\text{C}$	312	W
			$T_c = 100\ ^\circ\text{C}$	156	

**Table 5 Characteristic values**

Parameter	Symbol	Note or test condition	Values			Unit	
			Min.	Typ.	Max.		
Diode forward voltage	$V_F$	$I_F = 75\text{ A}$	$T_{vj} = 25\ ^\circ\text{C}$		2.5	3	V
			$T_{vj} = 175\ ^\circ\text{C}$		2.3		
Diode reverse recovery time	$t_{rr}$	$V_R = 600\text{ V}, R_{G(on)} = 5.3\ \Omega$	$T_{vj} = 25\ ^\circ\text{C},$ $I_F = 75\text{ A}$		90		ns
			$T_{vj} = 175\ ^\circ\text{C},$ $I_F = 75\text{ A}$		169		
Diode reverse recovery charge	$Q_{rr}$	$V_R = 600\text{ V}, R_{G(on)} = 5.3\ \Omega$	$T_{vj} = 25\ ^\circ\text{C},$ $I_F = 75\text{ A}$		2.55		$\mu\text{C}$
			$T_{vj} = 175\ ^\circ\text{C},$ $I_F = 75\text{ A}$		7.29		
Diode peak reverse recovery current	$I_{rrm}$	$V_R = 600\text{ V}, R_{G(on)} = 5.3\ \Omega$	$T_{vj} = 25\ ^\circ\text{C},$ $I_F = 75\text{ A}$		84		A
			$T_{vj} = 175\ ^\circ\text{C},$ $I_F = 75\text{ A}$		130		

(table continues...)

**Table 5 (continued) Characteristic values**

Parameter	Symbol	Note or test condition	Values			Unit	
			Min.	Typ.	Max.		
Diode peak rate of fall of reverse recovery current	$di_{rr}/dt$	$V_R = 600 \text{ V}, R_{G(on)} = 5.3 \text{ } \Omega$	$T_{vj} = 25 \text{ } ^\circ\text{C},$ $I_F = 75 \text{ A}$		-3200		A/ $\mu\text{s}$
			$T_{vj} = 175 \text{ } ^\circ\text{C},$ $I_F = 75 \text{ A}$		-2640		
Reverse recovery energy	$E_{rec}$	$V_R = 600 \text{ V}, R_{G(on)} = 5.3 \text{ } \Omega$	$T_{vj} = 25 \text{ } ^\circ\text{C},$ $I_F = 75 \text{ A}$		0.91		mJ
			$T_{vj} = 175 \text{ } ^\circ\text{C},$ $I_F = 75 \text{ A}$		2.93		
Operating junction temperature	$T_{vj}$			-40		175	$^\circ\text{C}$

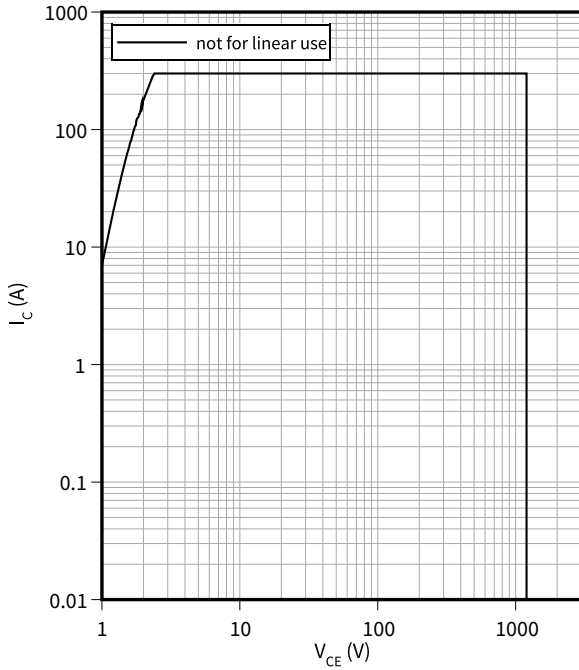
*Note: For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.*

*Dynamic test circuit, parasitic inductance  $L_\sigma = 30 \text{ nH}$ ,  $C_\sigma = 18 \text{ pF}$*

## 4 Characteristics diagrams

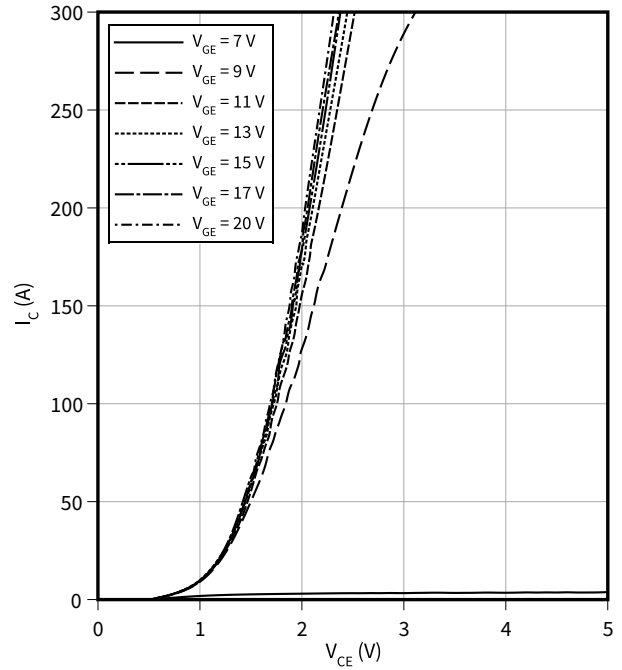
### Reverse bias safe operating area

$I_C = f(V_{CE}), I_C = f(V_{CE})$   
 $T_{vj} \leq 175\text{ °C}, V_{GE} = 0/15\text{ V}$



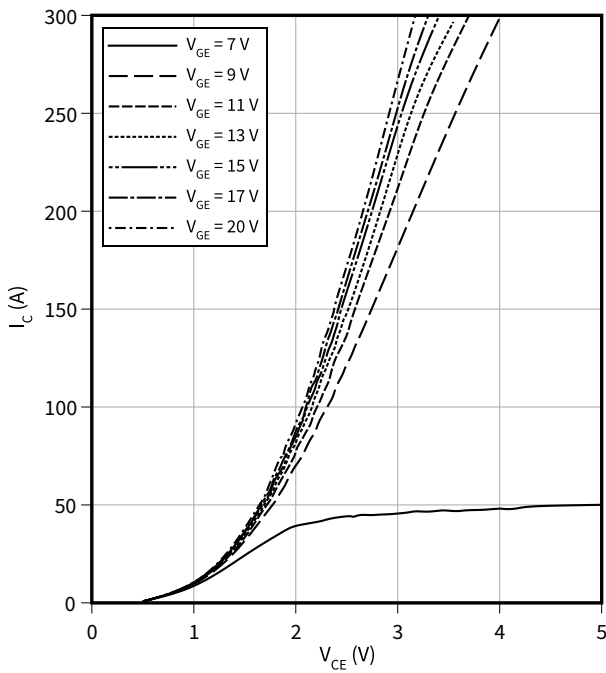
### Typical output characteristic

$I_C = f(V_{CE})$   
 $T_{vj} = 25\text{ °C}$



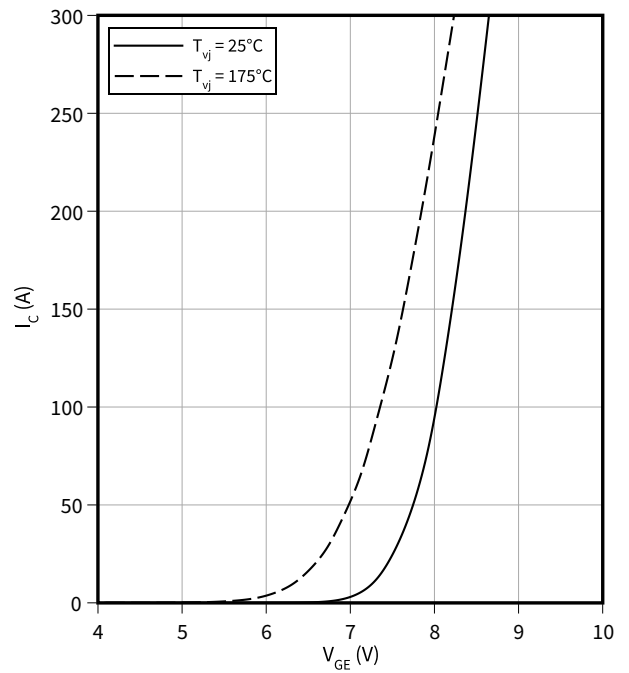
### Typical output characteristic

$I_C = f(V_{CE})$   
 $T_{vj} = 175\text{ °C}$



### Typical transfer characteristic

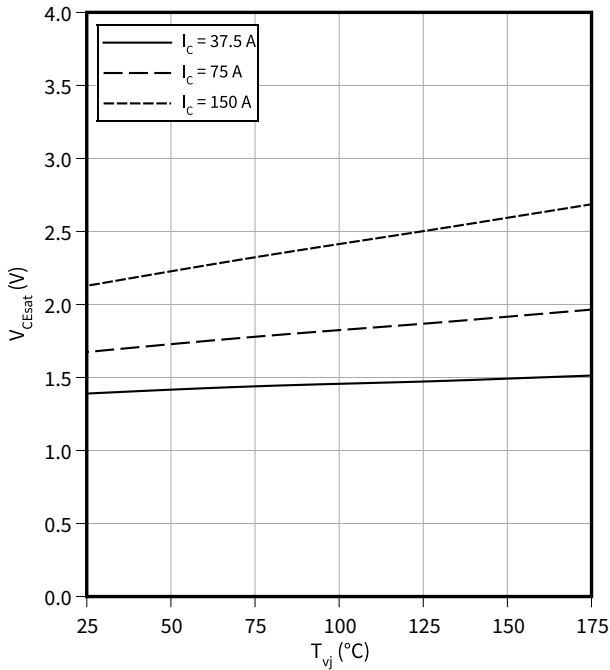
$I_C = f(V_{GE})$   
 $V_{CE} = 20\text{ V}$



4 Characteristics diagrams

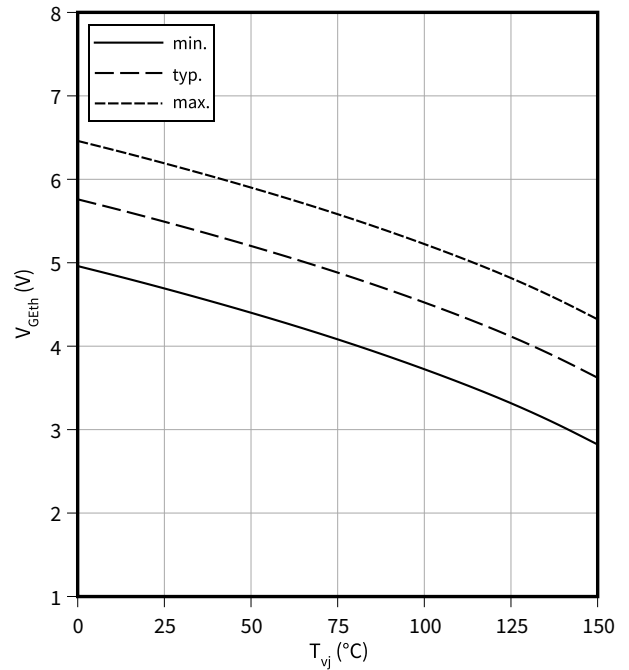
**Typical collector-emitter saturation voltage as a function of junction temperature**

$V_{CEsat} = f(T_{vj})$   
 $V_{GE} = 15 \text{ V}$



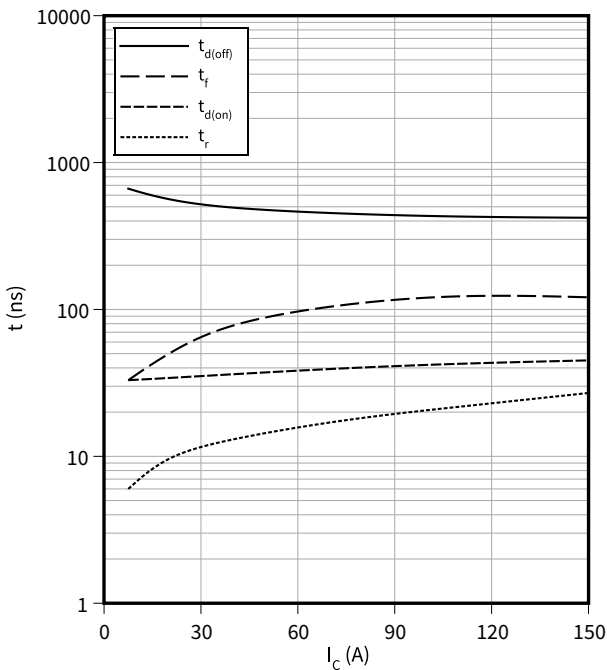
**Gate-emitter threshold voltage as a function of junction temperature**

$V_{GEth} = f(T_{vj})$   
 $I_c = 1.2 \text{ mA}$



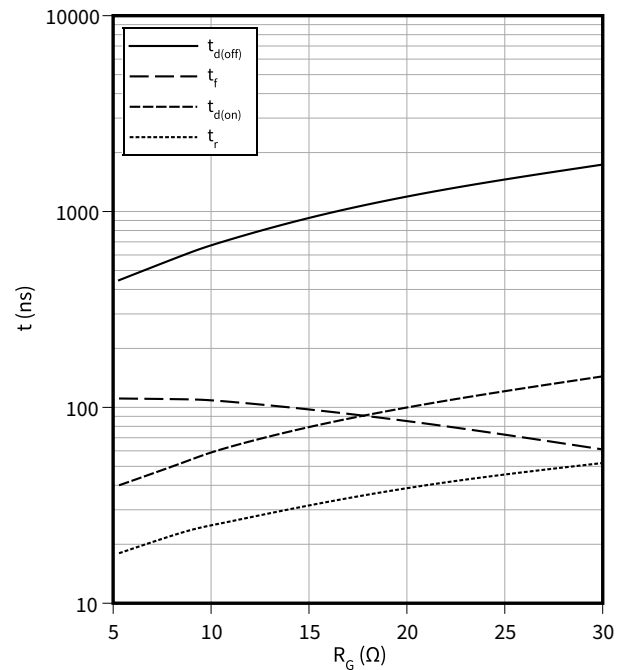
**Typical switching times as a function of collector current**

$t = f(I_c)$   
 $V_{CC} = 600 \text{ V}, T_{vj} = 175 \text{ °C}, V_{GE} = 0/15 \text{ V}, R_G = 5.3 \text{ } \Omega$



**Typical switching times as a function of gate resistor**

$t = f(R_G)$   
 $I_c = 75 \text{ A}, V_{CC} = 600 \text{ V}, T_{vj} = 175 \text{ °C}, V_{GE} = 0/15 \text{ V}$



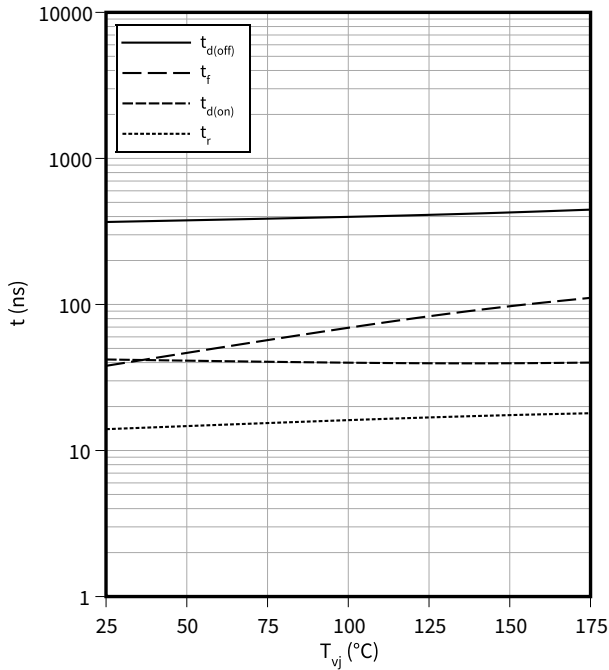


4 Characteristics diagrams

**Typical switching times as a function of junction temperature**

$t = f(T_{vj})$

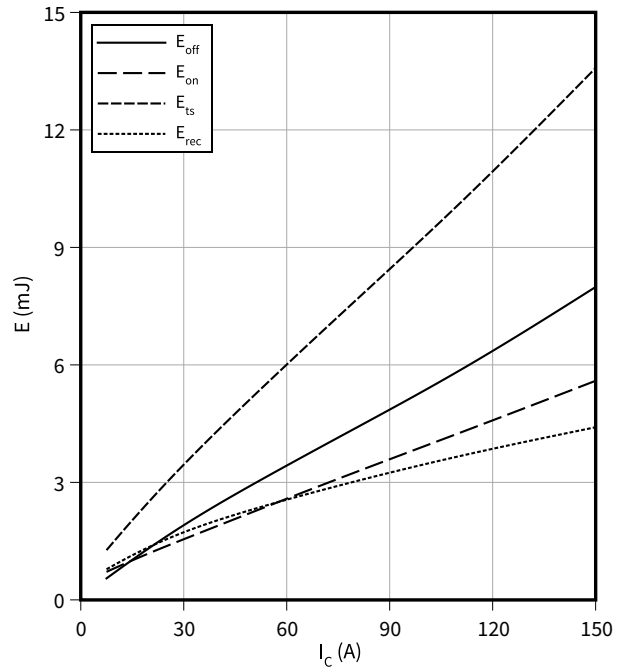
$I_C = 75 \text{ A}, V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_G = 5.3 \Omega$



**Typical switching energy losses as a function of collector current**

$E = f(I_C)$

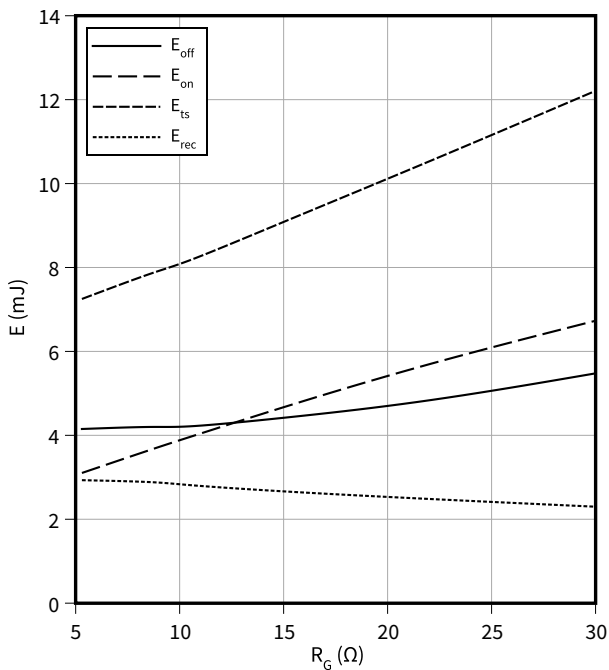
$V_{CC} = 600 \text{ V}, T_{vj} = 175 \text{ }^\circ\text{C}, V_{GE} = 0/15 \text{ V}, R_G = 5.3 \Omega$



**Typical switching energy losses as a function of gate resistor**

$E = f(R_G)$

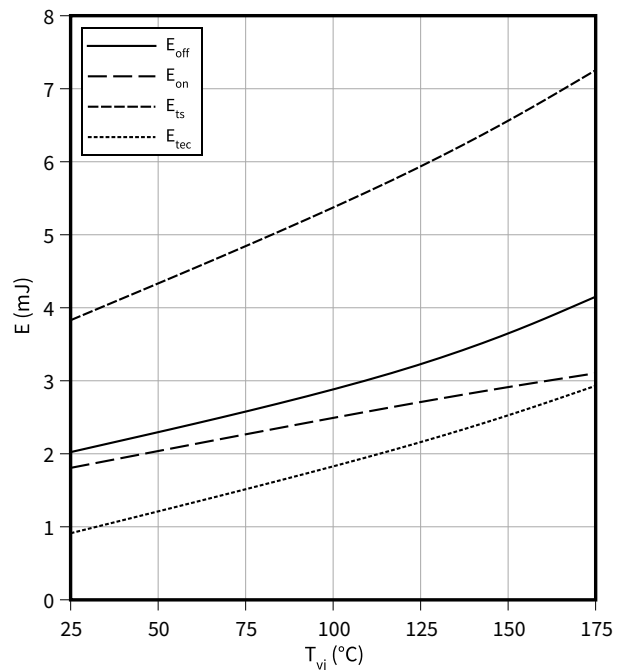
$I_C = 75 \text{ A}, V_{CC} = 600 \text{ V}, T_{vj} = 175 \text{ }^\circ\text{C}, V_{GE} = 0/15 \text{ V}$



**Typical switching energy losses as a function of junction temperature**

$E = f(T_{vj})$

$I_C = 75 \text{ A}, V_{CC} = 600 \text{ V}, V_{GE} = 0/15 \text{ V}, R_G = 5.3 \Omega$

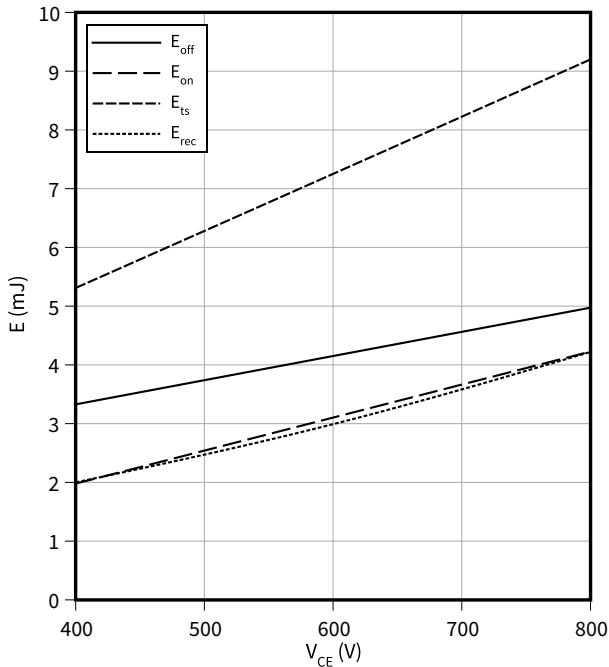


4 Characteristics diagrams

**Typical switching energy losses as a function of collector emitter voltage**

$E = f(V_{CE})$

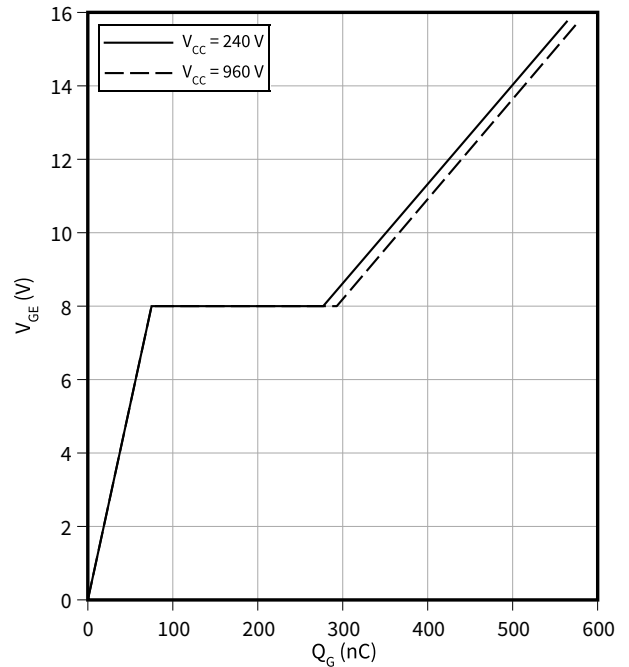
$I_C = 75 \text{ A}, V_{GE} = 0/15 \text{ V}, T_{vj} = 175 \text{ °C}, R_G = 5.3 \text{ } \Omega$



**Typical gate charge**

$V_{GE} = f(Q_G)$

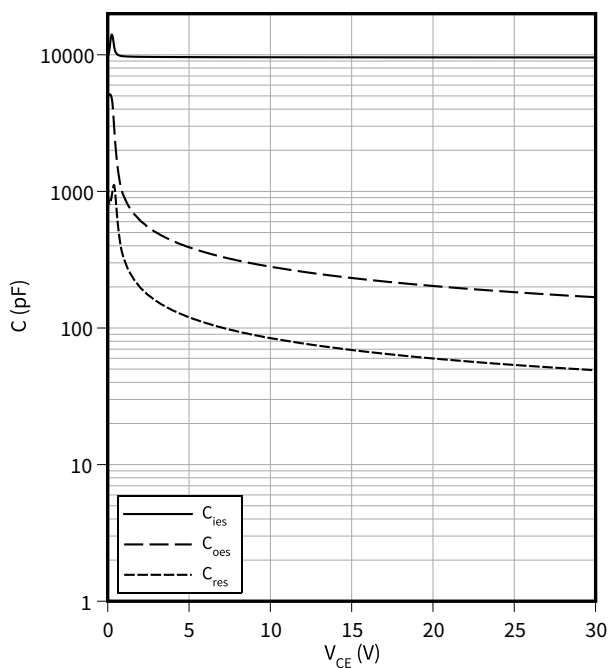
$I_C = 75 \text{ A}$



**Typical capacitance as a function of collector-emitter voltage**

$C = f(V_{CE})$

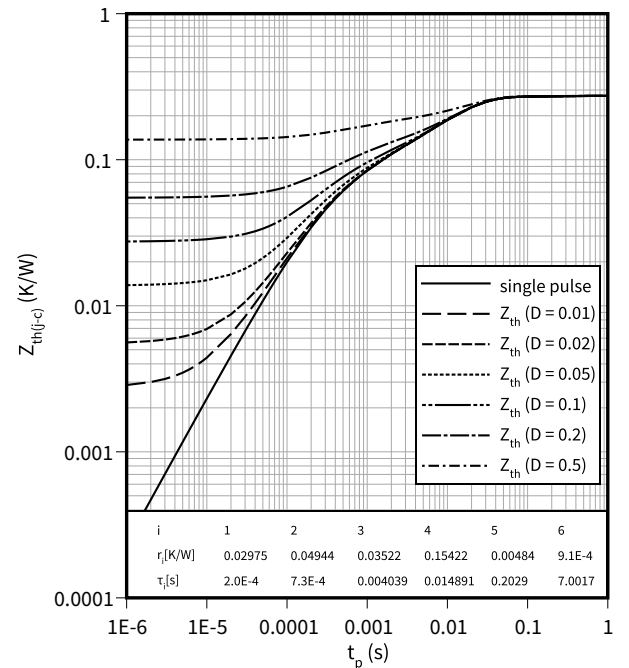
$f = 100 \text{ kHz}, V_{GE} = 0 \text{ V}$



**IGBT transient thermal impedance as a function of pulse width**

$Z_{th(j-c)} = f(t_p)$

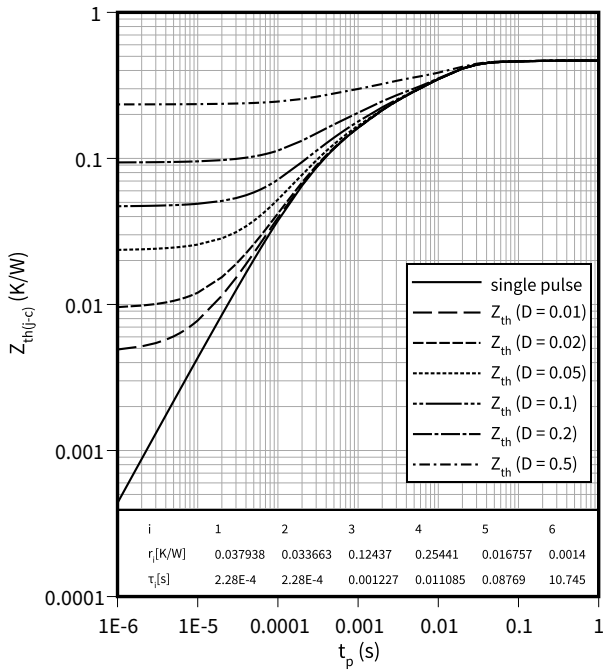
$D = t_p/T$



4 Characteristics diagrams

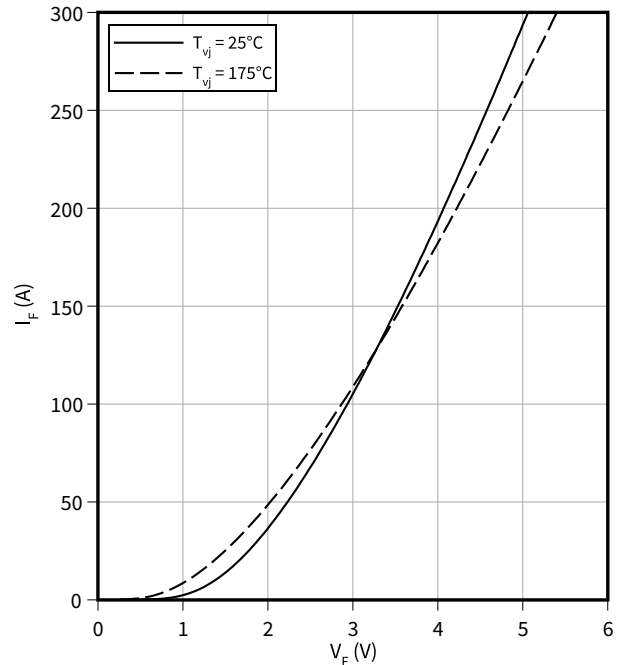
**Diode transient thermal impedance as a function of pulse width**

$Z_{th(j-c)} = f(t_p)$   
 $D = t_p/T$



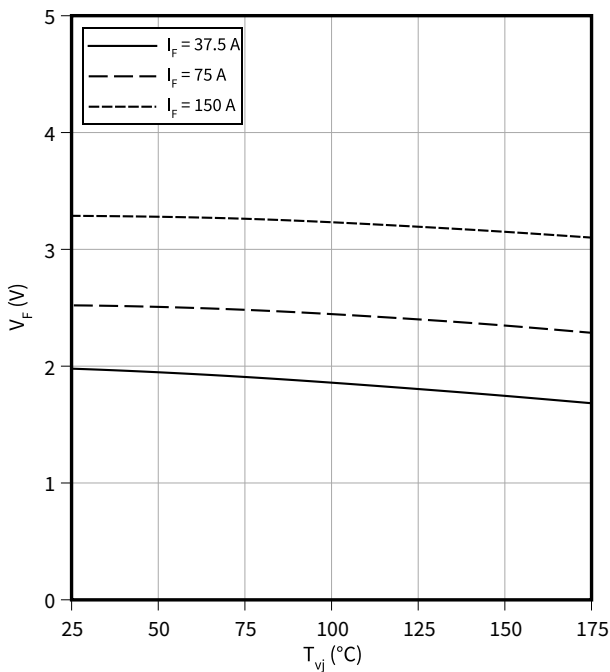
**Typical diode forward current as a function of forward voltage**

$I_F = f(V_F)$



**Typical diode forward voltage as a function of junction temperature**

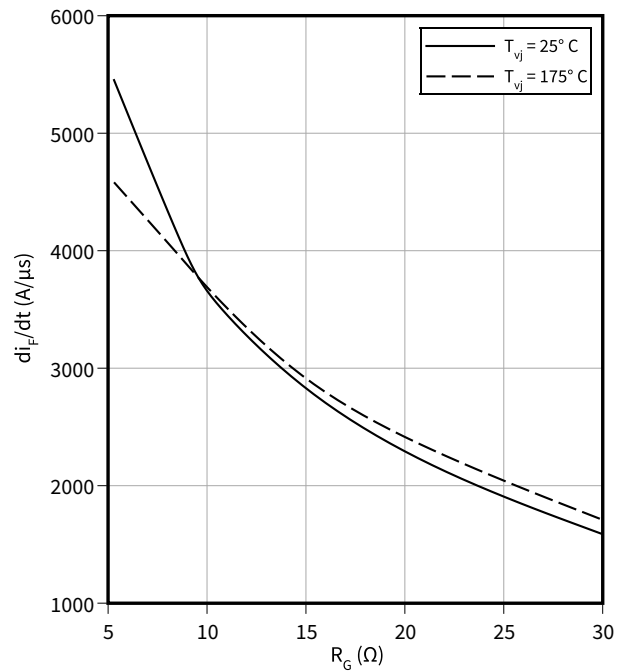
$V_F = f(T_{vj})$



**Typical diode current slope as a function of gate resistor**

$di_F/dt = f(R_G)$

$V_R = 600 \text{ V}, I_F = 75 \text{ A}$

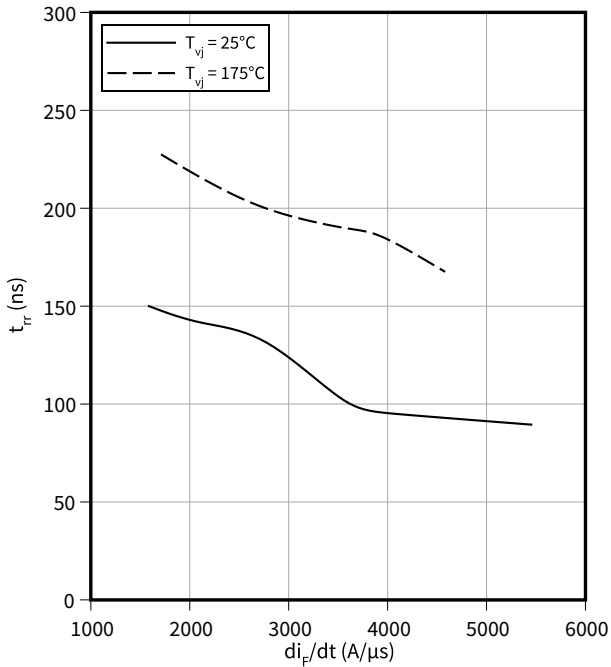


4 Characteristics diagrams

**Typical reverse recovery time as a function of diode current slope**

$t_{rr} = f(di_F/dt)$

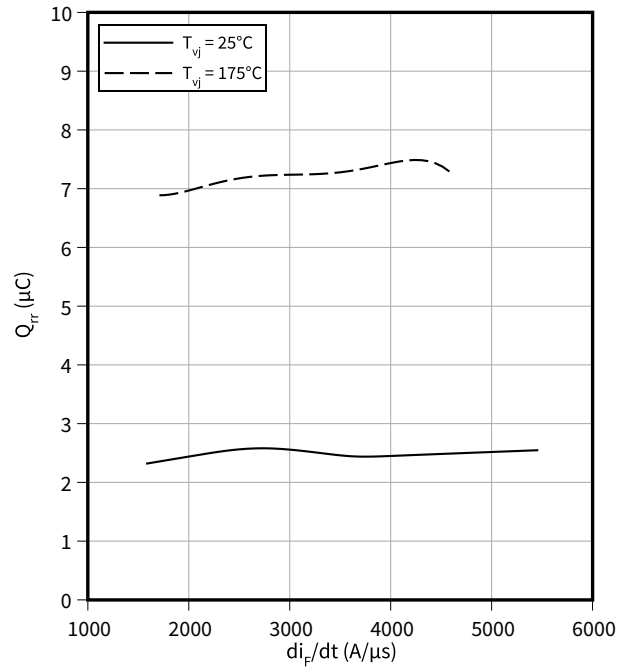
$V_R = 600\text{ V}, I_F = 75\text{ A}$



**Typical reverse recovery charge as a function of diode current slope**

$Q_{rr} = f(di_F/dt)$

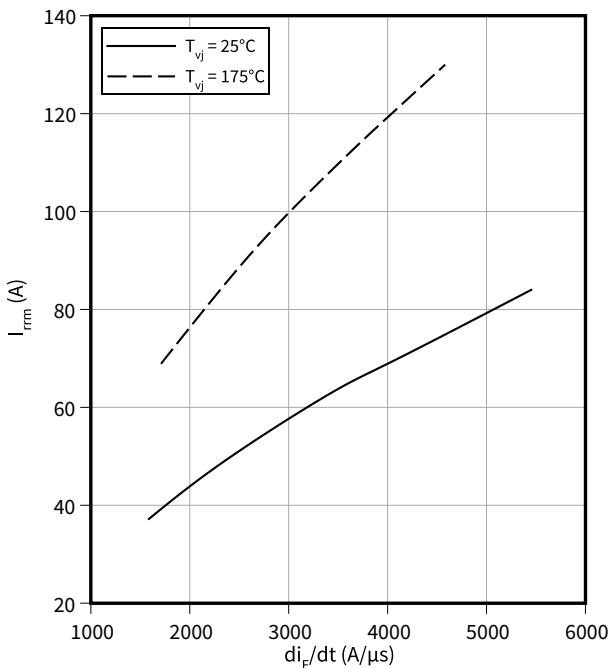
$V_R = 600\text{ V}, I_F = 75\text{ A}$



**Typical reverse recovery current as a function of diode current slope**

$I_{rrm} = f(di_F/dt)$

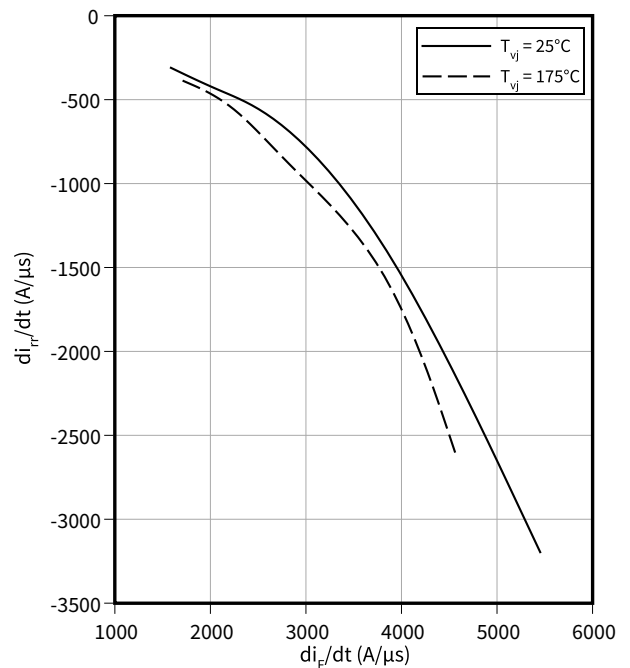
$V_R = 600\text{ V}, I_F = 75\text{ A}$



**Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**

$di_{rr}/dt = f(di_F/dt)$

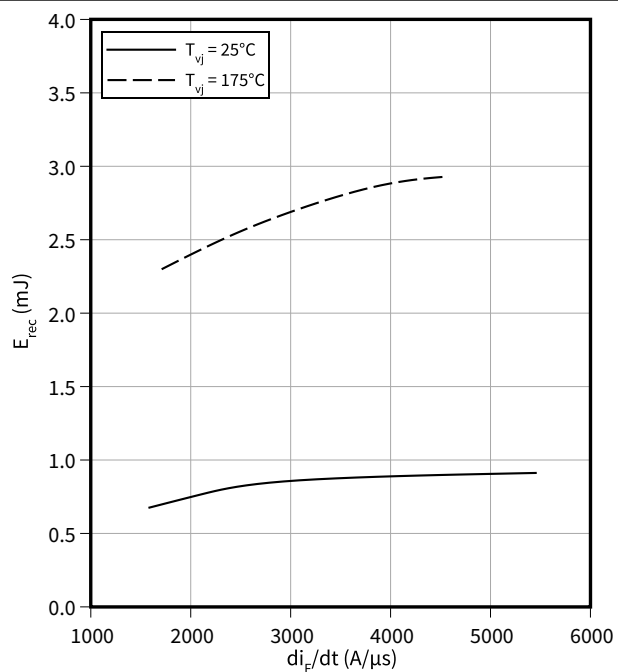
$V_R = 600\text{ V}, I_F = 75\text{ A}$



**Typical reverse energy losses as a function of diode current slope**

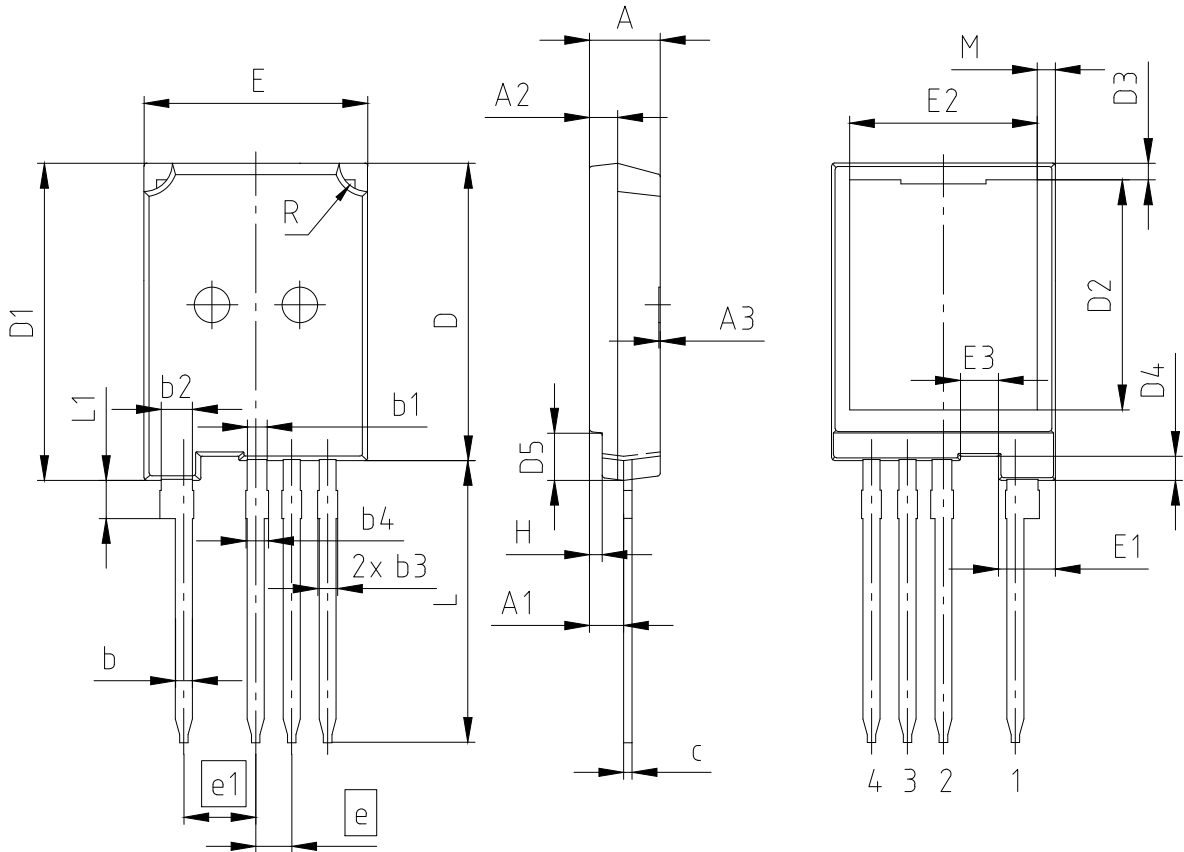
$$E_{\text{rec}} = f(di_F/dt)$$

$V_R = 600 \text{ V}$ ,  $I_F = 75 \text{ A}$



**5 Package outlines**

**PG-TO247-4-PLUS-NN5.1**



NOTES:  
 PACKAGE SURFACE ROUTE BETWEEN PIN 1 & PIN 2 WILL BE 5.1 mm MIN.  
 ALL b... AND c DIMENSIONS INCLUDING PLATING EXPECT AREA OF CUTTING

PACKAGE - GROUP NUMBER: <b>PG-TO247-4-U10</b>					
DIMENSIONS	MILLIMETERS		DIMENSIONS	MILLIMETERS	
	MIN.	MAX.		MIN.	MAX.
<b>A</b>	4.90	5.10	<b>E</b>	15.70	15.90
<b>A1</b>	2.31	2.51	<b>E1</b>	3.90	4.10
<b>A2</b>	1.90	2.10	<b>E2</b>	13.10	13.50
<b>b</b>	1.16	1.29	<b>E3</b>	2.58	2.78
<b>b1</b>	1.36	1.49	<b>e</b>	2.54	
<b>b2</b>	2.16	2.29	<b>e1</b>	5.08	
<b>b3</b>	1.16	1.45	<b>H</b>	0.80	1.00
<b>b4</b>	1.16	1.65	<b>N</b>	4	
<b>c</b>	0.59	0.66	<b>L</b>	19.80	20.10
<b>D</b>	20.90	21.10	<b>L1</b>	2.55	2.85
<b>D1</b>	22.30	22.50	<b>M</b>	0.97	1.57
<b>D2</b>	15.95	16.55	<b>R</b>	1.90	2.10
<b>D3</b>	1.00	1.35			
<b>D4</b>	1.60	1.80			
<b>D5</b>	3.24	3.44			

**Figure 1**

**6 Testing conditions**



**Figure 2**

## Revision history

Document revision	Date of release	Description of changes
0.10	2022-05-02	Target datasheet
0.20	2022-06-01	Editorial changes
1.00	2022-11-25	Final datasheet
1.10	2023-01-13	Change of product outline drawing on page 14



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