IGBT

High speed IGBT in Trench and Fieldstop technology

IGB30N60H3

600V high speed switching series third generation

Data sheet

Industrial Power Control

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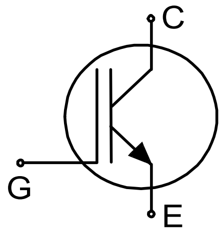

Features:

TRENCHSTOP^{TM} technology offering
- very low turn-off energy
- low V_{CEsat}
- low EMI
- maximum junction temperature 175°C
- qualified according to JEDEC for target applications
- Pb-free lead plating, halogen-free mould compound, RoHS compliant
- complete product spectrum and PSpice Models: http://www.infineon.com/igbt/

Applications:

- uninterruptible power supplies
- welding converters
- converters with high switching frequency

Green

Halogen-Free

RoHS

Key Performance and Package Parameters

Type	V_{CE}	I_C	V_{CEsat}, T_{vj}=25°C	T_{vjmax}	Marking	Package
IGB30N60H3	600V	30A	1.95V	175°C	G30H603	PG-TO263-3

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Maximum ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25°C$	V_{CE}	600	V
DC collector current, limited by T_{vjmax} $T_C = 25°C$ $T_C = 100°C$	I_C	60.0 30.0	A
Pulsed collector current, t_p limited by T_{vjmax}	I_{Cpuls}	120.0	A
Turn off safe operating area $V_{CE} \leq 600V$, $T_{vj} \leq 175°C$, $t_p = 1µs$	-	120.0	A
Gate-emitter voltage	V_{GE}	±20	V
Short circuit withstand time $V_{GE} = 15.0V$, $V_{CC} \leq 400V$ Allowed number of short circuits < 1000 Time between short circuits: ≥ 1.0s $T_{vj} = 150°C$	t_{SC}	5	µs
Power dissipation $T_C = 25°C$ Power dissipation $T_C = 100°C$	P_{tot}	187.0 94.0	W
Operating junction temperature	T_{vj}	-40...+175	°C
Storage temperature	T_{stg}	-55...+150	°C
Soldering temperature, reflow soldering (MSL1 according to JEDEC J-STA-020)		260	°C

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		0.80	K/W
Thermal resistance, min. footprint junction - ambient	$R_{th(j-a)}$		65	K/W
Thermal resistance, 6cm² Cu on PCB junction - ambient	$R_{th(j-a)}$		40	K/W

Electrical Characteristic, at T_{vj} = 25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value min.	Value typ.	Value max.	Unit
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0V$, $I_C = 2.00mA$	600	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	$V_{GE} = 15.0V$, $I_C = 30.0A$ $T_{vj} = 25°C$ $T_{vj} = 125°C$ $T_{vj} = 175°C$	- - -	1.95 2.30 2.50	2.40 - -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.43mA$, $V_{CE} = V_{GE}$	4.1	5.1	5.7	V
Zero gate voltage collector current	I_{CES}	$V_{CE} = 600V$, $V_{GE} = 0V$ $T_{vj} = 25°C$ $T_{vj} = 175°C$	- -	- -	40.0 2000.0	µA
Gate-emitter leakage current	I_{GES}	$V_{CE} = 0V$, $V_{GE} = 20V$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE} = 20V$, $I_C = 30.0A$	-	16.0	-	S

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Electrical Characteristic, at T_{vj} = 25°C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}	V_{CE} = 25V, V_{GE} = 0V, f = 1MHz	-	1630	-	pF
Output capacitance	C_{oes}		-	107	-	
Reverse transfer capacitance	C_{res}		-	50	-	
Gate charge	Q_G	V_{CC} = 480V, I_C = 30.0A, V_{GE} = 15V	-	165.0	-	nC
Short circuit collector current Max. 1000 short circuits Time between short circuits: ≥ 1.0s	$I_{C(SC)}$	V_{GE} = 15.0V, $V_{CC} \leq$ 400V, $t_{SC} \leq$ 5µs T_{vj} = 150°C	-	160	-	A

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 25°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 25°C, V_{CC} = 400V, I_C = 30.0A, V_{GE} = 0.0/15.0V, r_G = 10.5Ω, $L\sigma$ = 95nH, $C\sigma$ = 67pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode (IKW30N60H3) reverse recovery.	-	18	-	ns
Rise time	t_r		-	22	-	ns
Turn-off delay time	$t_{d(off)}$		-	207	-	ns
Fall time	t_f		-	22	-	ns
Turn-on energy	E_{on}		-	0.73	-	mJ
Turn-off energy	E_{off}		-	0.44	-	mJ
Total switching energy	E_{ts}		-	1.17	-	mJ

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at T_{vj} = 175°C						
Turn-on delay time	$t_{d(on)}$	T_{vj} = 175°C, V_{CC} = 400V, I_C = 30.0A, V_{GE} = 0.0/15.0V, r_G = 10.5Ω, $L\sigma$ = 95nH, $C\sigma$ = 67pF $L\sigma$, $C\sigma$ from Fig. E Energy losses include “tail” and diode (IKW30N60H3) reverse recovery.	-	18	-	ns
Rise time	t_r		-	22	-	ns
Turn-off delay time	$t_{d(off)}$		-	239	-	ns
Fall time	t_f		-	23	-	ns
Turn-on energy	E_{on}		-	0.95	-	mJ
Turn-off energy	E_{off}		-	0.60	-	mJ
Total switching energy	E_{ts}		-	1.55	-	mJ

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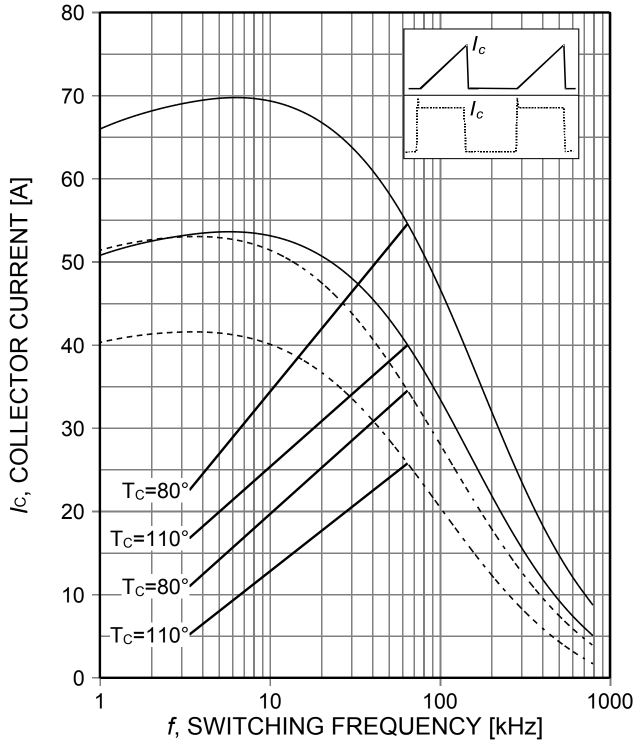

Figure 1. **Collector current as a function of switching frequency**
(T_j≤175°C, D=0.5, V_{CE}=400V, V_{GE}=15/0V, r_G=10,5Ω)

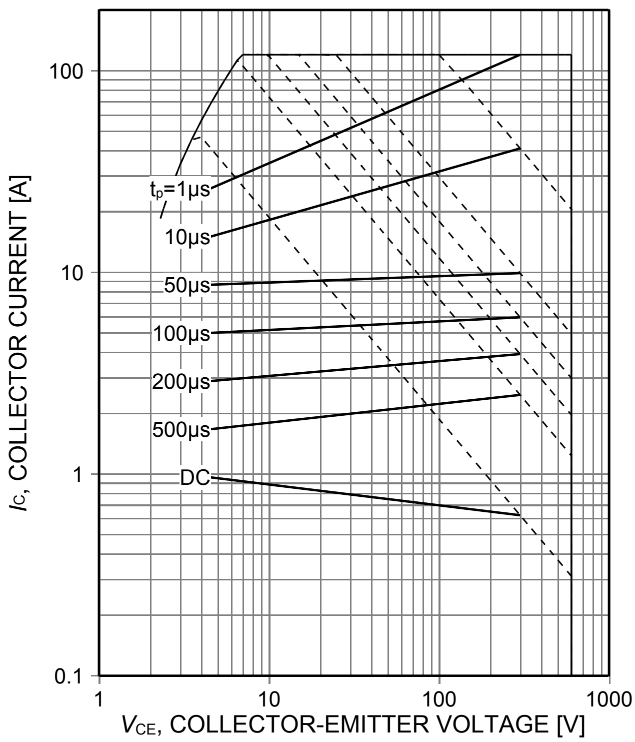

Figure 2. **Forward bias safe operating area**
(D=0, T_C=25°C, T_j≤175°C; V_{GE}=15V)

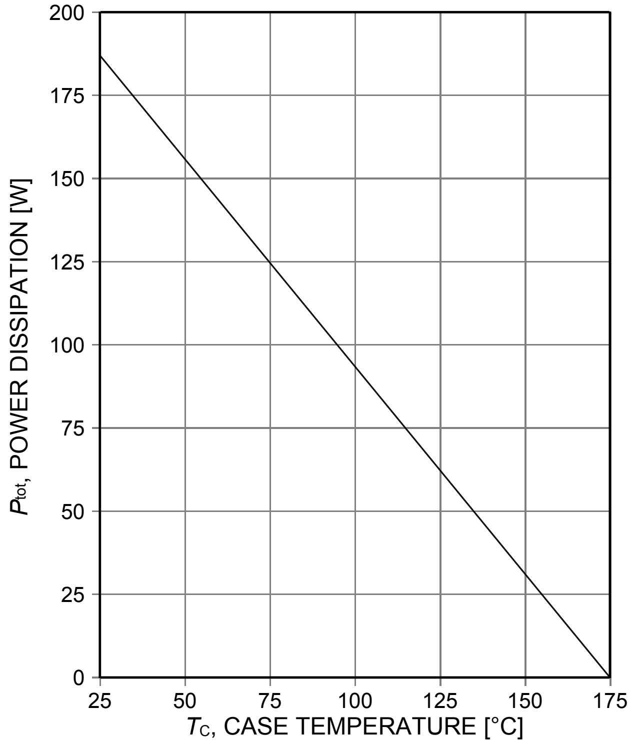

Figure 3. **Power dissipation as a function of case temperature**
(T_j≤175°C)

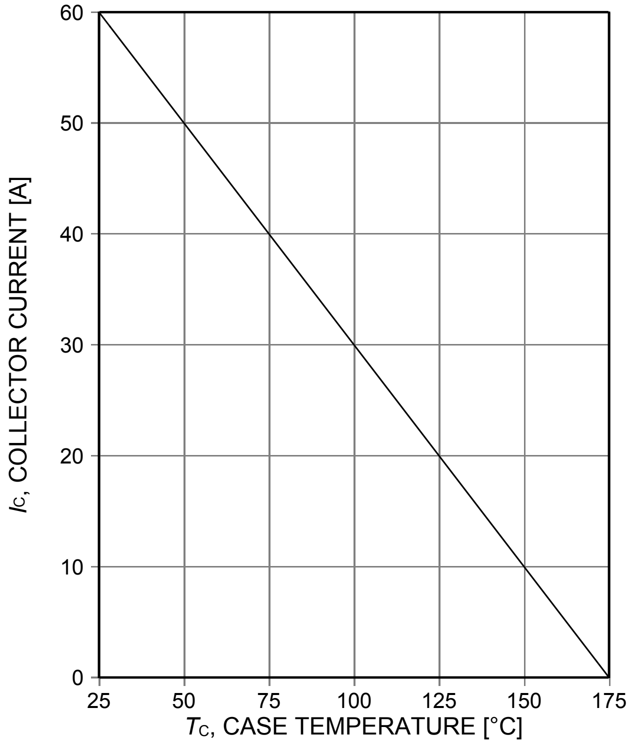

Figure 4. **Collector current as a function of case temperature**
(V_{GE}≥15V, T_j≤175°C)

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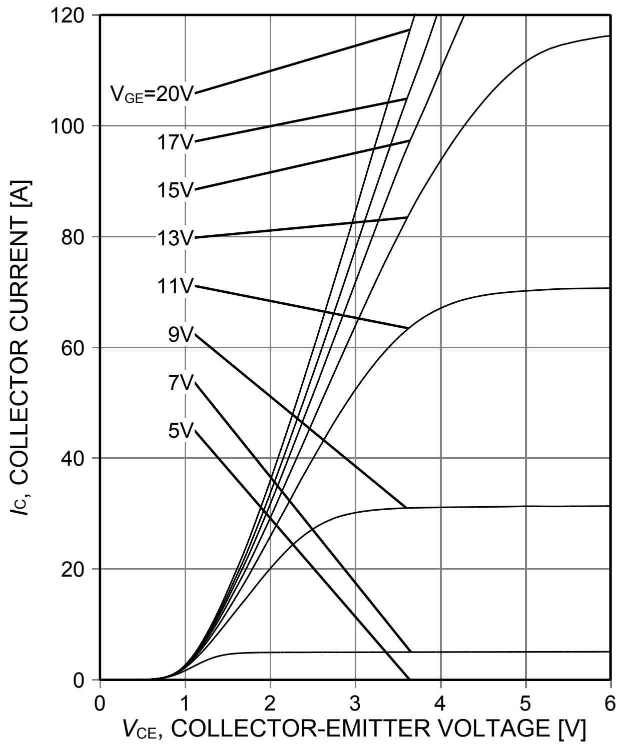

Figure 5. **Typical output characteristic**
(T_j=25°C)

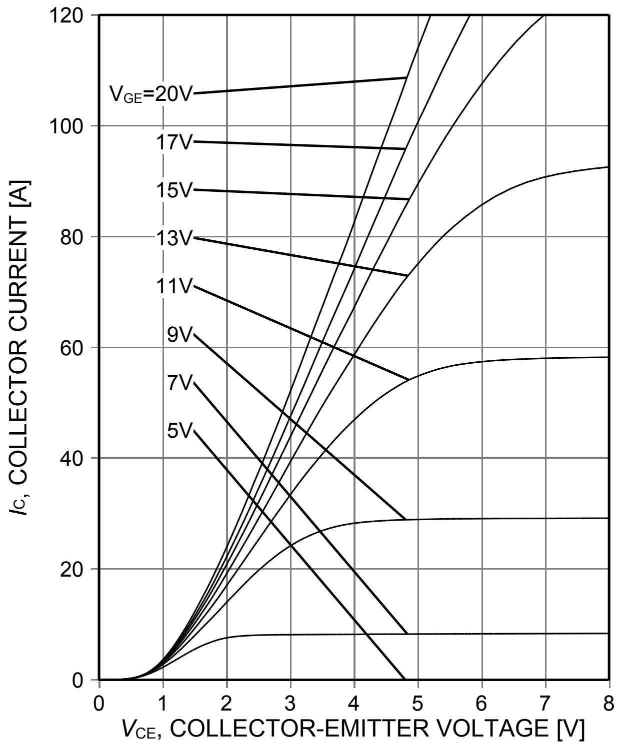

Figure 6. **Typical output characteristic**
(T_j=175°C)

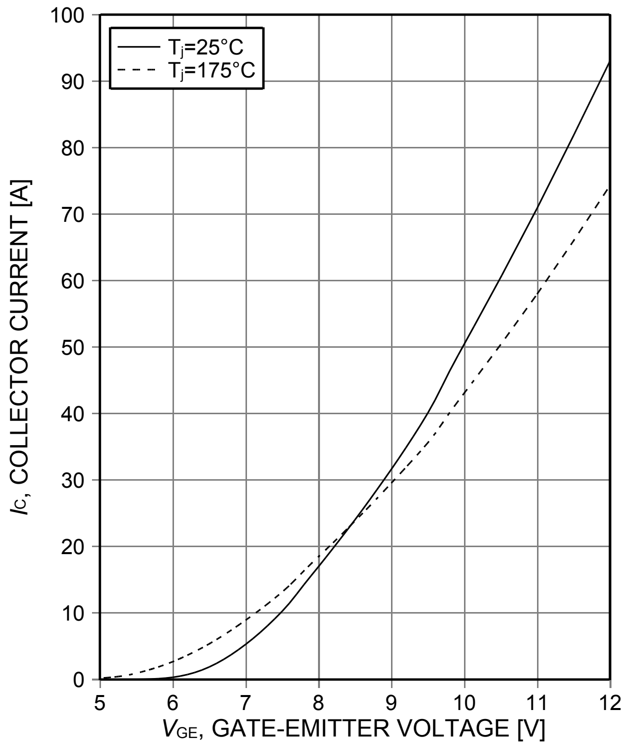

Figure 7. **Typical transfer characteristic**
(V_{CE}=20V)

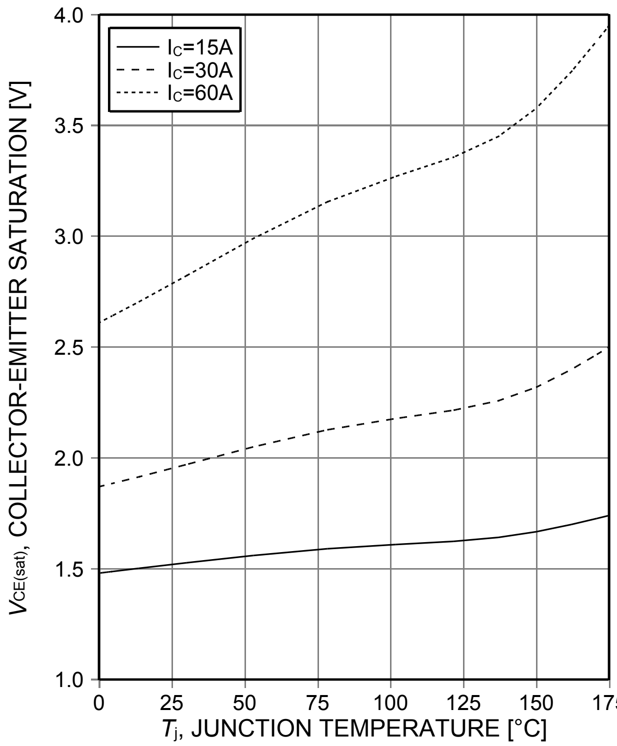

Figure 8. **Typical collector-emitter saturation voltage as a function of junction temperature**
(V_{GE}=15V)

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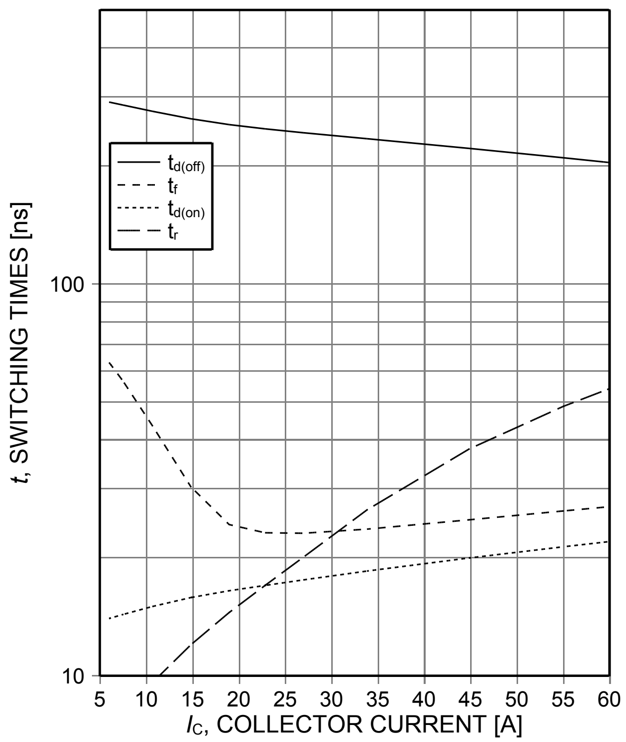

Figure 9. **Typical switching times as a function of collector current**
(ind. load, T_j=175°C, V_{CE}=400V, V_{GE}=15/0V, r_G=10,5Ω, test circuit in Fig. E)

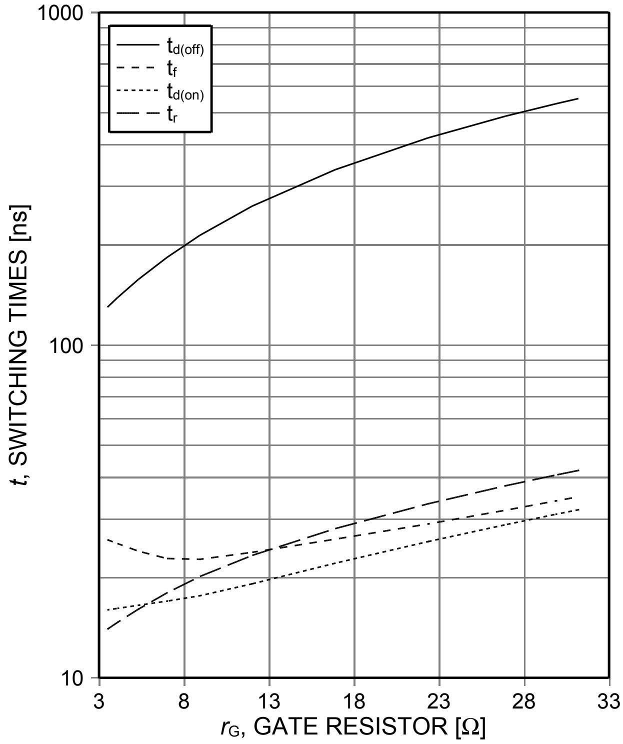

Figure 10. **Typical switching times as a function of gate resistor**
(ind. load, T_j=175°C, V_{CE}=400V, V_{GE}=15/0V, I_C=30A, test circuit in Fig. E)

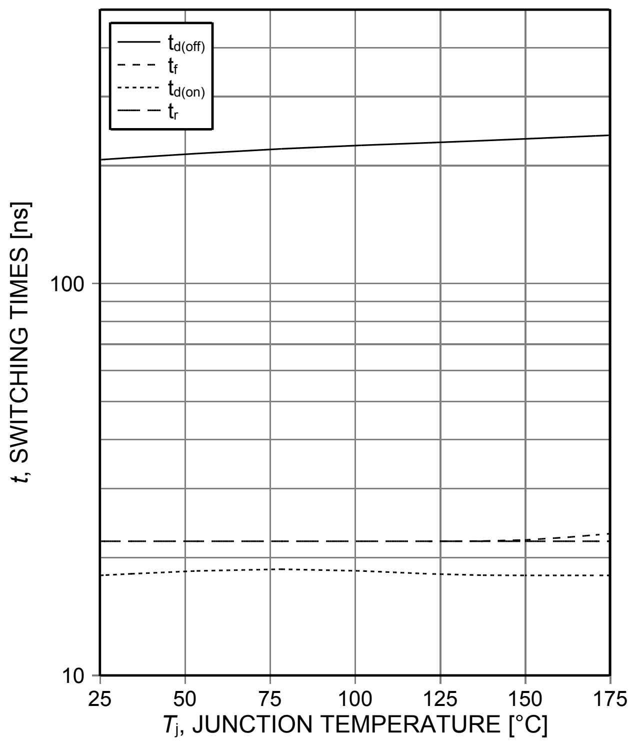

Figure 11. **Typical switching times as a function of junction temperature**
(ind. load, V_{CE}=400V, V_{GE}=15/0V, I_C=30A, r_G=10,5Ω, test circuit in Fig. E)

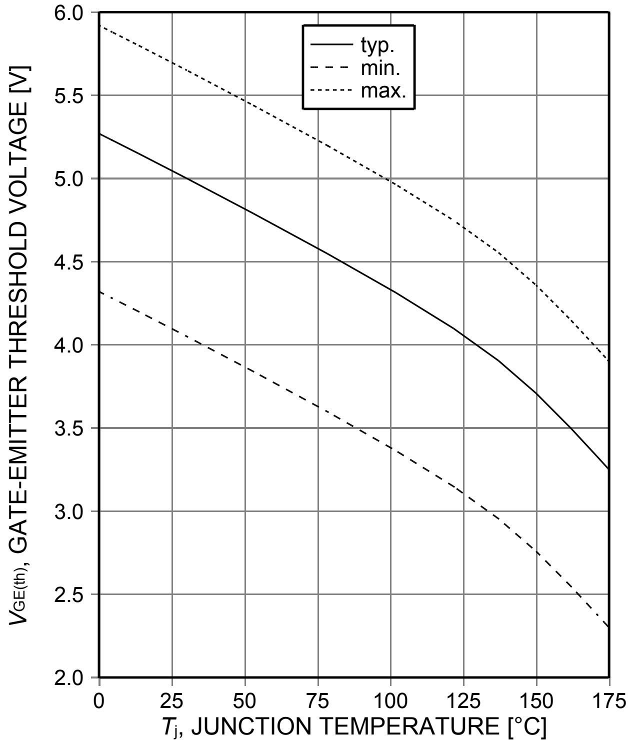

Figure 12. **Gate-emitter threshold voltage as a function of junction temperature**
(I_C=0.43mA)

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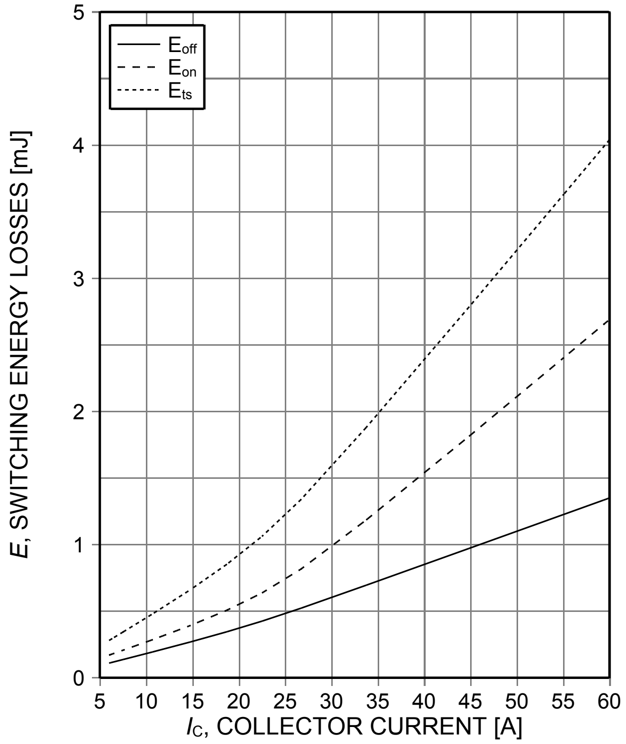

Figure 13. **Typical switching energy losses as a function of collector current**
(ind. load, T_j=175°C, V_{CE}=400V, V_{GE}=15/0V, r_G=10,5Ω, test circuit in Fig. E)

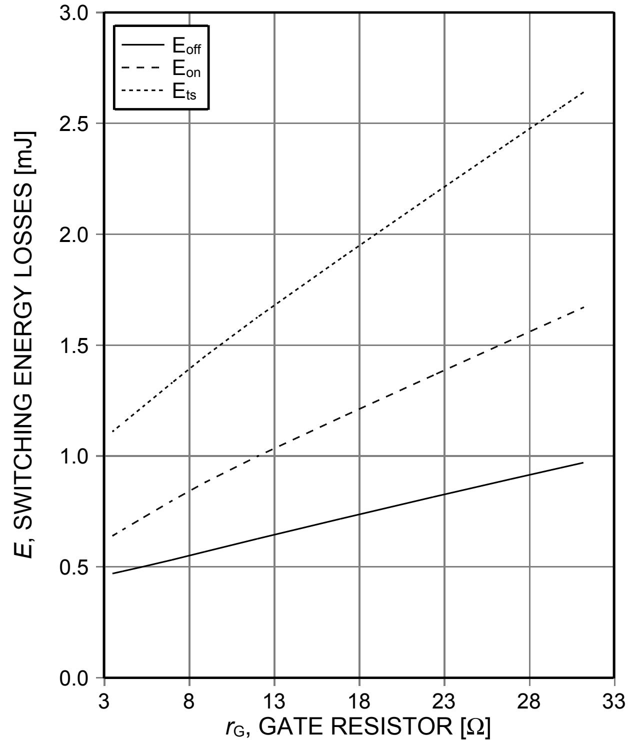

Figure 14. **Typical switching energy losses as a function of gate resistor**
(ind. load, T_j=175°C, V_{CE}=400V, V_{GE}=15/0V, I_C=30A, test circuit in Fig. E)

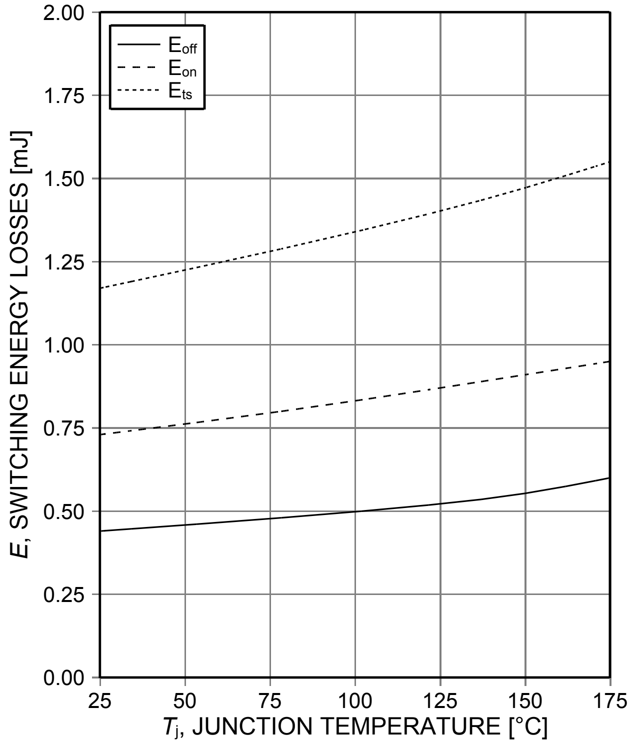

Figure 15. **Typical switching energy losses as a function of junction temperature**
(ind load, V_{CE}=400V, V_{GE}=15/0V, I_C=30A, r_G=10,5Ω, test circuit in Fig. E)

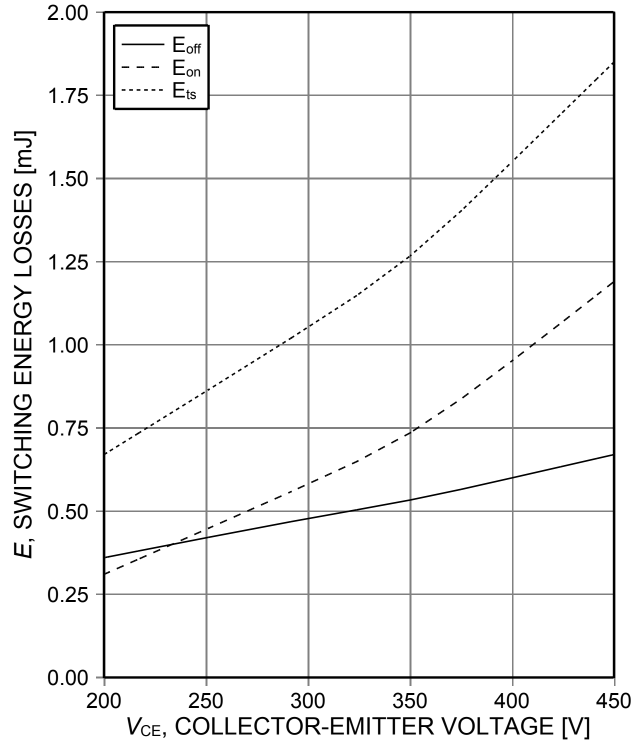

Figure 16. **Typical switching energy losses as a function of collector emitter voltage**
(ind. load, T_j=175°C, V_{GE}=15/0V, I_C=30A, r_G=10,5Ω, test circuit in Fig. E)

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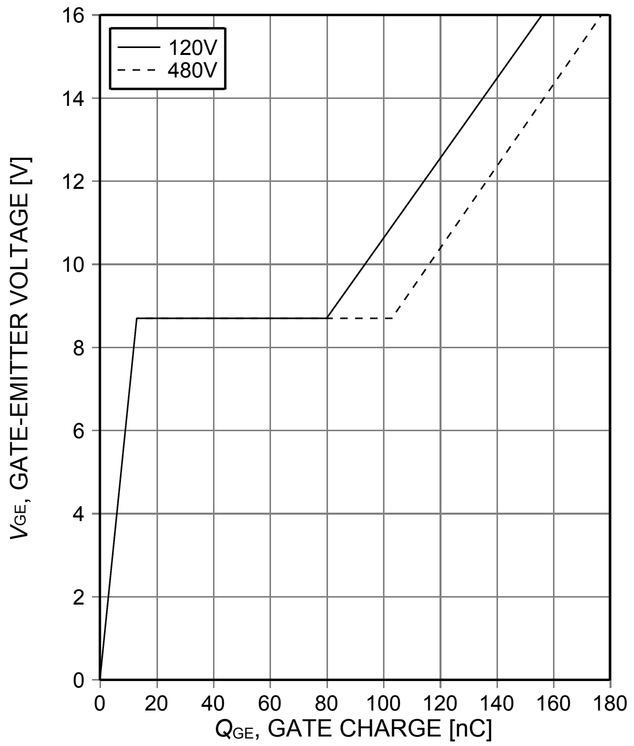

Figure 17. **Typical gate charge**
(I_C=30A)

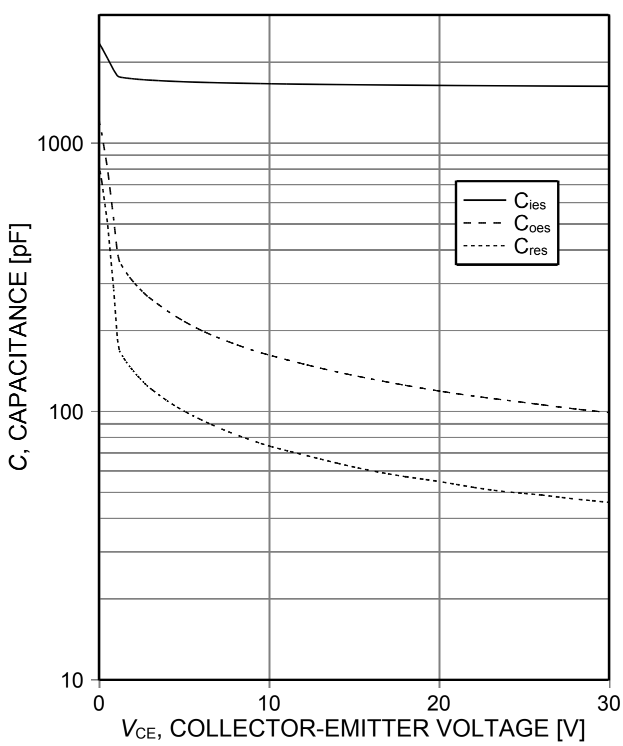

Figure 18. **Typical capacitance as a function of collector-emitter voltage**
(V_{GE}=0V, f=1MHz)

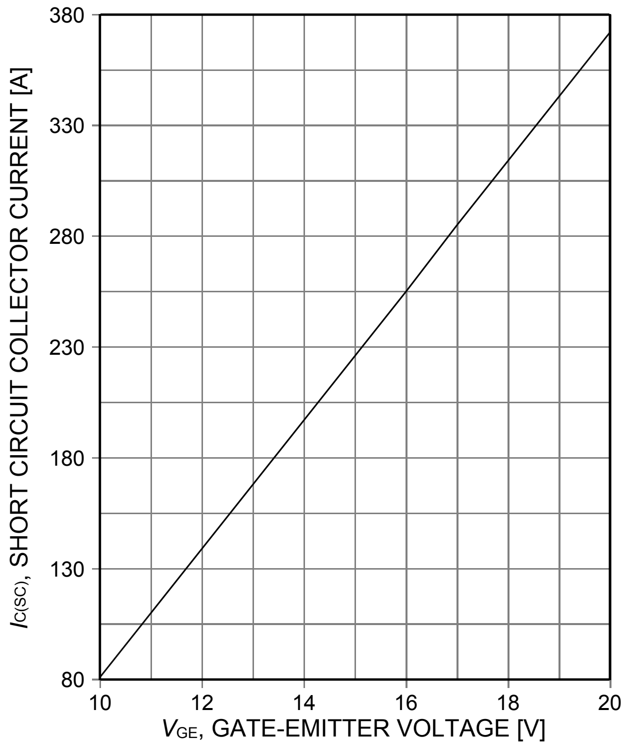

Figure 19. **Typical short circuit collector current as a function of gate-emitter voltage**
(V_{CE}≤400V, start at T_j=25°C)

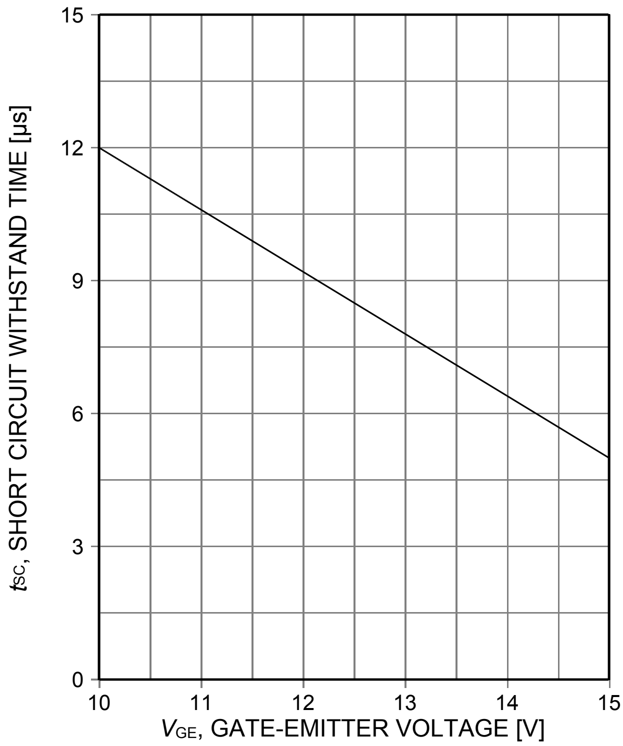

Figure 20. **Short circuit withstand time as a function of gate-emitter voltage**
(V_{CE}≤400V, start at T_j≤150°C)

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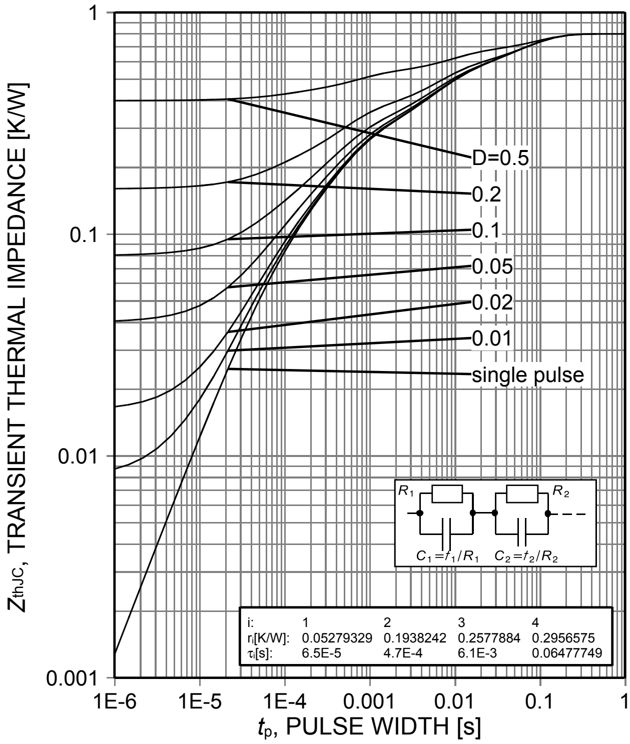

Figure 21. **IGBT transient thermal impedance**
($D=t_p/T$)

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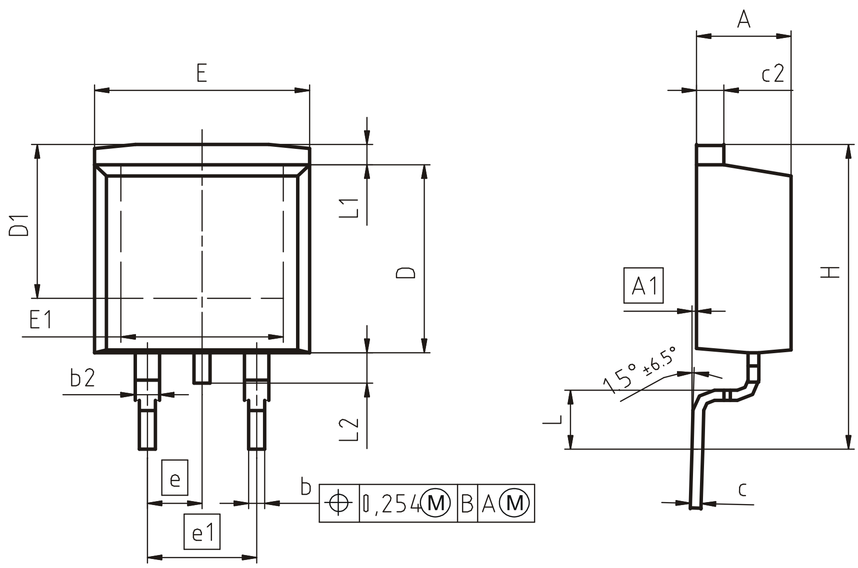
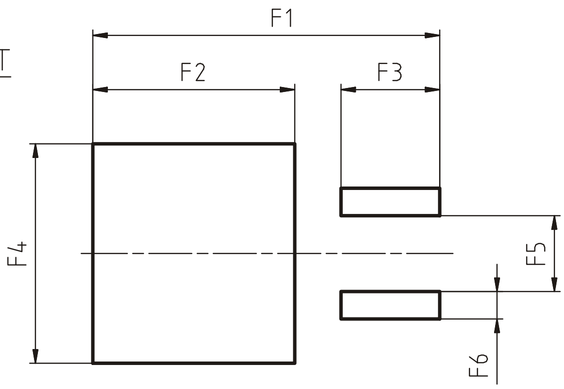


FOOTPRINT



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	2		2	
H	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057

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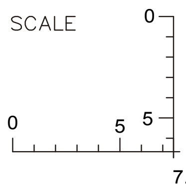
SCALE



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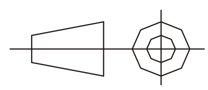

ISSUE DATE
30-08-2007

REVISION
01

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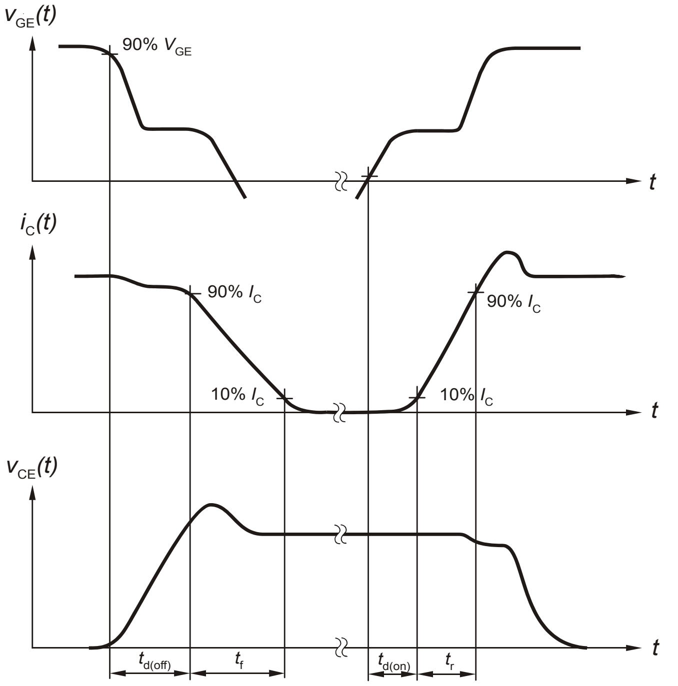

Figure A. Definition of switching times

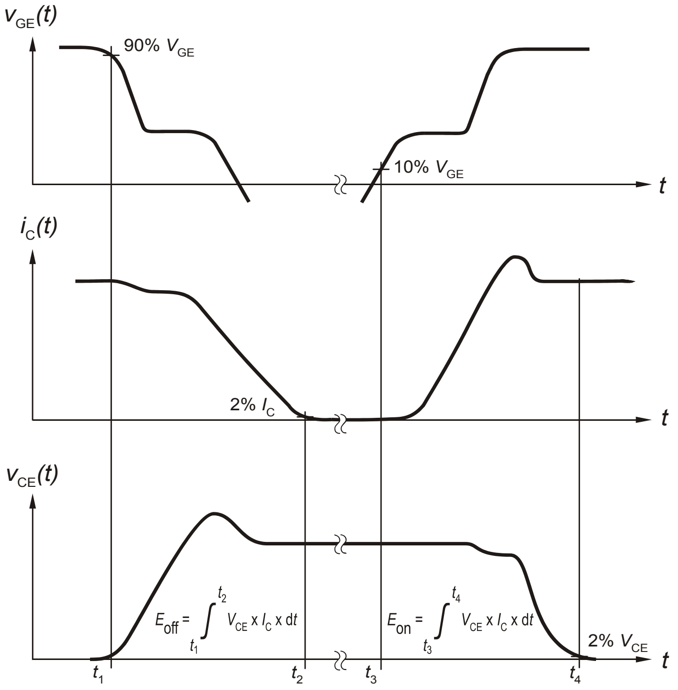

Figure B. Definition of switching losses

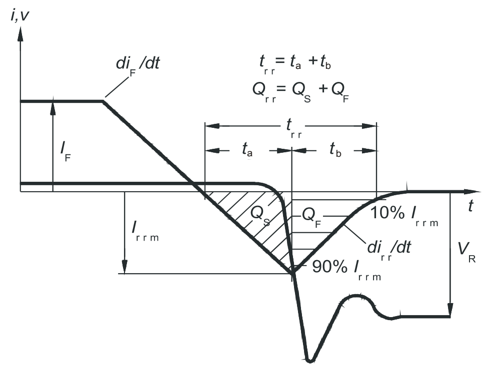

Figure C. Definition of diodes switching characteristics

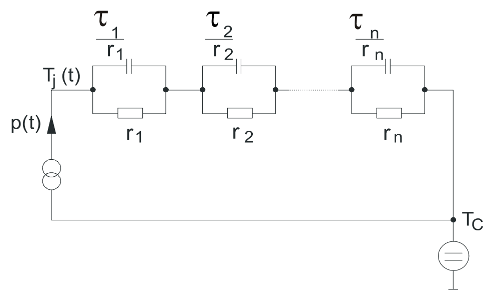

Figure D. Thermal equivalent circuit

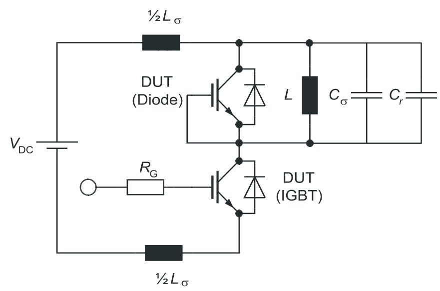

Figure E. Dynamic test circuit
Parasitic inductance L_{σ},
Parasitic capacitor C_{σ},
Relief capacitor C_r,
(only for ZVT switching)

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Revision History

IGB30N60H3

Revision: 2014-03-11, Rev. 2.3

Previous Revision

Revision	Date	Subjects (major changes since last revision)
1.1	2010-07-26	Preliminary datasheet
2.1	2013-12-09	New value Ices max limit at 175°C
2.2	2014-02-26	Without PB free logo
2.3	2014-03-11	Max ratings Vce, Tvj ≥ 25°C

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