

HighSpeed 2-Technology

- Designed for frequency inverters for washing machines, fans, pumps and vacuum cleaners
- 2nd generation HighSpeed-Technology for 1200V applications offers:
 - loss reduction in resonant circuits
 - temperature stable behavior
 - parallel switching capability
 - tight parameter distribution
 - E_{off} optimized for I_{C} =1A
- Qualified according to JEDEC² for target applications Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : http://www.infineon.com/igbt/





Туре	V _{CE}	I _C	E off	T j	Marking	Package
IGB01N120H2	1200V	1A	0.09mJ	150°C	G01H1202	PG-TO-263-3-2

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CE}	1200	V
Triangular collector current	I _C		Α
$T_{\rm C}$ = 25°C, f = 140kHz		3.2	
$T_{\rm C}$ = 100°C, f = 140kHz		1.3	
Pulsed collector current, t_p limited by T_{jmax}	I _{Cpuls}	3.5	
Turn off safe operating area	-	3.5	
$V_{CE} \le 1200 \text{V}, \ T_j \le 150^{\circ} \text{C}$			
Gate-emitter voltage	V _{GE}	±20	V
Power dissipation	P _{tot}	28	W
$T_{\rm C}$ = 25°C			
Operating junction and storage temperature	T _j , T _{stg}	-40+150	°C
Soldering temperature (reflow soldering, MSL1)	-	245	

² J-STD-020 and JESD-022



Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance,	R _{thJC}		4.5	K/W
junction – case				
Thermal resistance,	R_{thJA}	PG-TO-220-3-1	40	
junction – ambient ¹⁾				

Electrical Characteristic, at T_j = 25 °C, unless otherwise specified

Dovometer	Cumbal	Symbol Conditions		Value		
Parameter	Symbol Conditions		min.	Тур.	max.	Unit
Static Characteristic						
Collector-emitter breakdown voltage	V _{(BR)CES}	$V_{\rm GE} = 0 \text{V}, I_{\rm C} = 300 \mu \text{A}$	1200	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{\rm GE} = 15 \rm V, I_{\rm C} = 1 \rm A$				
		<i>T</i> _j =25°C	-	2.2	2.8	
		T _j =150°C	-	2.5	-	
		$V_{GE} = 10V, I_{C} = 1A,$				
		<i>T</i> _j =25°C	-	2.4	-	
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C}$ =30 μ A, $V_{\rm CE}$ = $V_{\rm GE}$	2.1	3	3.9	
Zero gate voltage collector current	I _{CES}	V _{CE} =1200V, V _{GE} =0V				μΑ
		<i>T</i> _j =25°C	-	-	20	
		T _j =150°C	-	-	80	
Gate-emitter leakage current	I _{GES}	$V_{CE}=0V, V_{GE}=20V$	-	-	40	nA
Transconductance	g fs	V _{CE} =20V, I _C =1A	-	0.75	-	S
Dynamic Characteristic						
Input capacitance	Ciss	V _{CE} =25V,	-	91.6	-	pF
Output capacitance	Coss	V _{GE} =0V,	-	9.8	-	
Reverse transfer capacitance	Crss	f=1MHz	-	3.4	-	
Gate charge	Q _{Gate}	V _{CC} =960V, I _C =1A	-	8.6	-	nC
		V _{GE} =15V				
Internal emitter inductance	LE		-	7	-	nH
measured 5mm (0.197 in.) from case						<u> </u>

 $^{^{1)}}$ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm 2 (one layer, 70 μm thick) copper area for collector connection. PCB is vertical without blown air.



Switching Characteristic, Inductive Load, at T_j =25 °C

Parameter	Symbol	Conditions	Value			Unit
raiailletei	Symbol Conditions		min.	Тур.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T _j =25°C,	-	13	-	ns
Rise time	t _r	V _{CC} =800V,	-	6.3	_	
Turn-off delay time	$t_{d(off)}$	$I_{\rm C}$ =1A,	-	370	_	
Fall time	t_{f}	V_{GE} =15V/0V,	-	28	-	
Turn-on energy	Eon	$R_{\rm G}$ =241 Ω , $L_{\rm G}^{2)}$ =180nH,	-	0.08	-	mJ
Turn-off energy	E _{off}	$C_{\sigma}^{2)}$ =40pF	-	0.06	_	
Total switching energy	E _{ts}	Energy losses include "tail" and diode ³⁾ reverse recovery.	-	0.14	-	

Switching Characteristic, Inductive Load, at T_i =150 °C

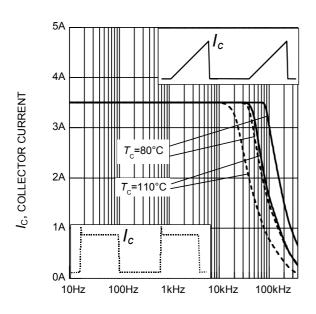
Davamatar	Cumbal	Conditions	Value			11
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic						•
Turn-on delay time	$t_{d(on)}$	T _j =150°C	-	12	-	ns
Rise time	t _r	V _{CC} =800V,	-	8.9	_	
Turn-off delay time	$t_{d(off)}$	$I_{\rm C}=1{\rm A}$	-	450	_	
Fall time	t _f	V_{GE} =15V/0V,	-	43	-	
Turn-on energy	Eon	$R_{\rm G}$ =241 Ω , $L_{\rm G}^{2)}$ =180nH,	-	0.11	_	mJ
Turn-off energy	E _{off}	$C_{\sigma}^{2)}$ =40pF	-	0.09	_	
Total switching energy	E _{ts}	Energy losses include "tail" and diode ⁴⁾ reverse recovery.	-	0.2	-	

Switching Energy ZVT, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
Farameter		Conditions	min.	Тур.	max.	Ullit
IGBT Characteristic						
Turn-off energy	E_{off}	V _{CC} =800V,				mJ
		$V_{CC} = 800V,$ $I_{C} = 1A,$ $V_{GE} = 15V/0V,$				
		$V_{GE} = 15 \text{V}/0 \text{V},$				
		$R_{\rm G}$ =241 Ω ,				
		$R_{\rm G}$ =241 Ω , $C_{\rm r}^{(2)}$ =1nF				
		T _j =25°C	-	0.02	-	
		T _j =150°C	-	0.044	-	

 $^{^2}$) Leakage inductance L_σ and stray capacity C_σ due to dynamic test circuit in figure E 4) Commutation diode from device IKP01N120H2

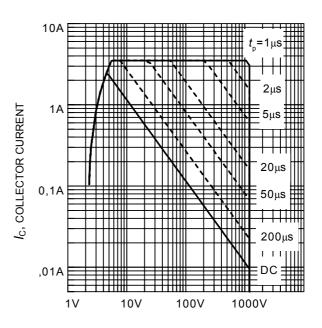




f, SWITCHING FREQUENCY

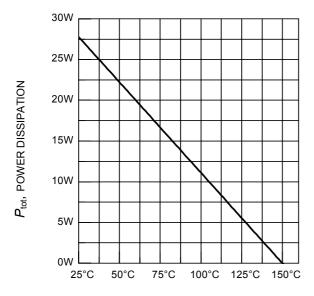
Figure 1. Collector current as a function of switching frequency

 $(T_{\rm j} \le 150^{\circ}{\rm C}, \, D = 0.5, \, V_{\rm CE} = 800{\rm V}, \ V_{\rm GE} = +15{\rm V/0V}, \, R_{\rm G} = 241\Omega)$



 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

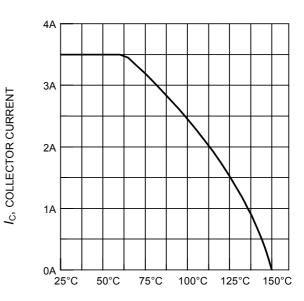
Figure 2. Safe operating area $(D = 0, T_C = 25^{\circ}C, T_i \le 150^{\circ}C)$



 $T_{\rm C}$, CASE TEMPERATURE

Figure 3. Power dissipation as a function of case temperature

 $(T_{\rm i} \le 150^{\circ}{\rm C})$

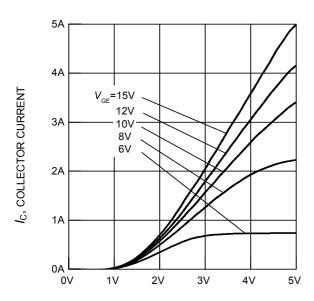


 $T_{\rm C}$, CASE TEMPERATURE

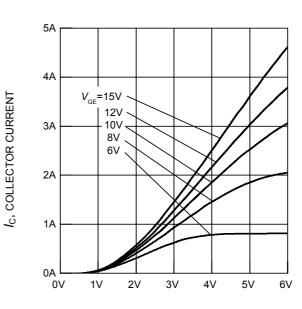
Figure 4. Collector current as a function of case temperature

 $(V_{\rm GE} \le 15 \rm V, \ T_i \le 150^{\circ} \rm C)$

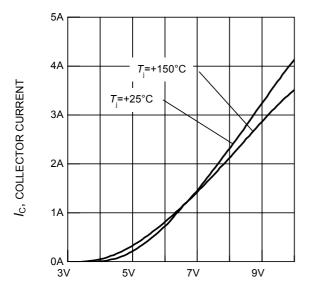




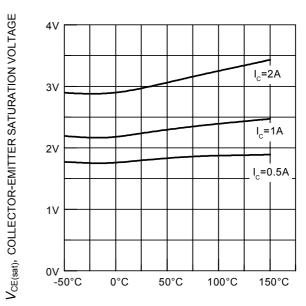
 V_{CE} , COLLECTOR-EMITTER VOLTAGE Figure 5. Typical output characteristics ($T_{\text{i}} = 25^{\circ}\text{C}$)



 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE Figure 6. Typical output characteristics ($T_{\rm i}$ = 150°C)

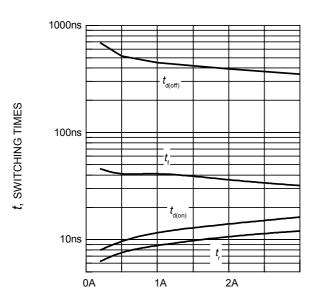


 $V_{\rm GE}$, GATE-EMITTER VOLTAGE Figure 7. Typical transfer characteristics ($V_{\rm CE}$ = 20V)



 $T_{\rm j},$ JUNCTION TEMPERATURE Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature ($V_{\rm GE}$ = 15V)

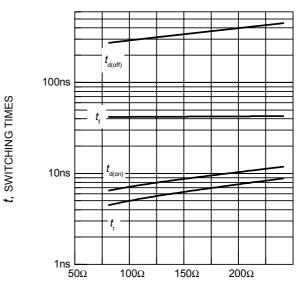




 $I_{\rm C}$, COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current (inductive load, $T_j = 150^{\circ}\text{C}$,

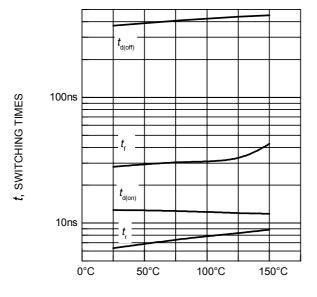
(inductive load, $I_j = 150^{\circ}\text{C}$, $V_{\text{CE}} = 800\text{V}$, $V_{\text{GE}} = +15\text{V}/0\text{V}$, $R_{\text{G}} = 241\Omega$, dynamic test circuit in Fig.E)



 $R_{\rm G}$, GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor

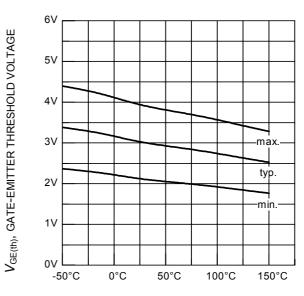
(inductive load, $T_{\rm j}$ = 150°C, $V_{\rm CE}$ = 800V, $V_{\rm GE}$ = +15V/0V, $I_{\rm C}$ = 1A, dynamic test circuit in Fig.E)



 $T_{\rm i}$, JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature (inductive load, $V_{\rm CE}$ = 800V,

 $V_{\rm GE}$ = +15V/0V, $I_{\rm C}$ = 1A, $R_{\rm G}$ = 241 Ω , dynamic test circuit in Fig.E)

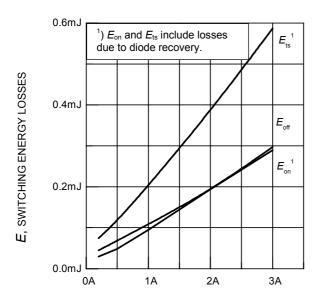


 $T_{\rm i}$, JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature

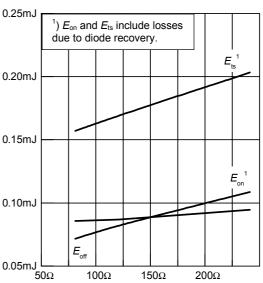
 $(I_{\rm C} = 0.03 {\rm mA})$





0.20mJ 0.15mJ 0.10mJ

SWITCHING ENERGY LOSSES



 $I_{\rm C}$, COLLECTOR CURRENT

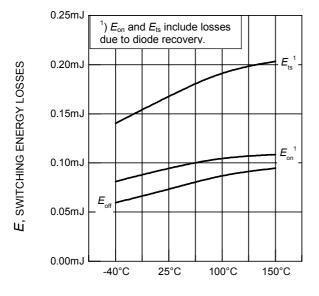
Figure 13. Typical switching energy losses as a function of collector current

(inductive load, $T_i = 150$ °C, V_{CE} = 800V, V_{GE} = +15V/0V, R_{G} = 241 Ω , dynamic test circuit in Fig.E)

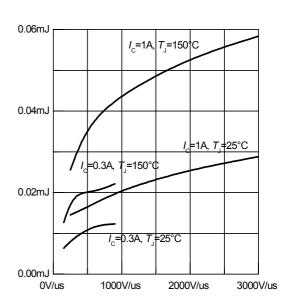
 $R_{\rm G}$, gate resistor

Figure 14. Typical switching energy losses as a function of gate resistor

(inductive load, $T_i = 150^{\circ}$ C, $V_{\text{CE}} = 800\text{V}, \ V_{\text{GE}} = +15\text{V}/0\text{V}, \ I_{\text{C}} = 1\text{A},$ dynamic test circuit in Fig.E)



Eoff, TURN OFF SWITCHING ENERGY LOSS



 $T_{\rm i}$, JUNCTION TEMPERATURE

Figure 15. Typical switching energy losses as a function of junction temperature (inductive load, $V_{CE} = 800V$,

 $V_{\rm GE}$ = +15V/0V, $I_{\rm C}$ = 1A, $R_{\rm G}$ = 241 Ω , dynamic test circuit in Fig.E)

dv/dt, VOLTAGE SLOPE

Figure 16. Typical turn off switching energy loss for soft switching

(dynamic test circuit in Fig. E)



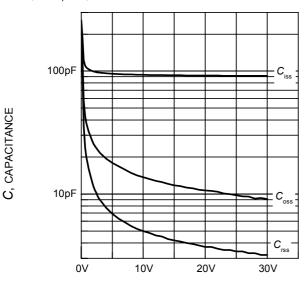
 Z_{thJC} , TRANSIENT THERMAL IMPEDANCE

D=0.5 D=0.5 D=0.5 R, (K/W) T, (s) 0.05 0.02 0.02 0.03 0.00066 0.00066 0.00066 0.00021 0.01

 $V_{
m GE}$, gate-emitter voltage

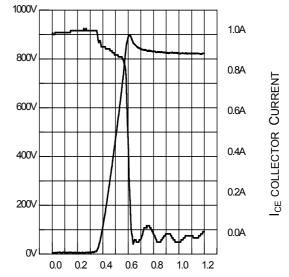
 $t_{
m p}$, PULSE WIDTH

Figure 17. IGBT transient thermal impedance as a function of pulse width $(D = t_p / T)$



 V_{CE} , collector-emitter voltage

 $Q_{\text{GE}}, \, \text{GATE CHARGE}$ Figure 18. Typical gate charge ($I_{\text{C}} = 1\text{A}$)



 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

Figure 19. Typical capacitance as a function of collector-emitter voltage ($V_{GE} = 0V, f = 1MHz$)

 $t_{
m p}$, PULSE WIDTH

Figure 20. Typical turn off behavior, hard switching

(V_{GE}=15/0V, R_G =220 Ω , T_j = 150°C, Dynamic test circuit in Figure E)



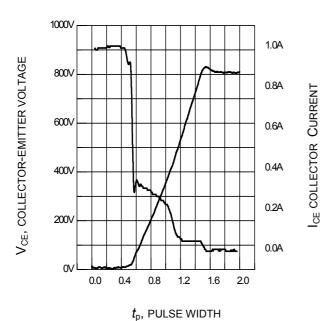
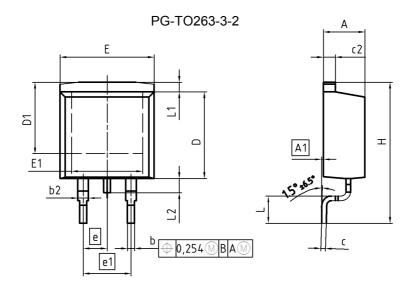
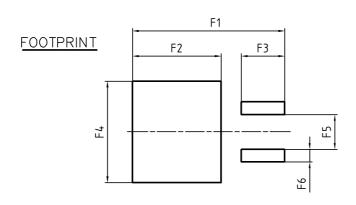


Figure 21. Typical turn off behavior, soft switching

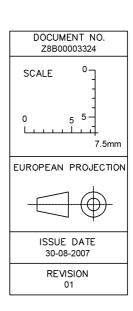
(V_{GE}=15/0V, R_G =220 Ω , T_j = 150°C, Dynamic test circuit in Figure E)



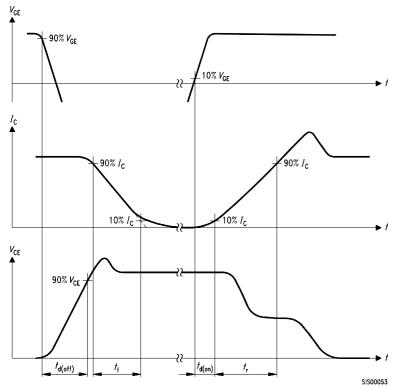




DIM	MILLIMI	ETERS	INCH	HES	
DIM	MIN	MAX	MIN	MAX	
Α	4.30	4.57	0.169	0.180	
A1	0.00	0.25	0.000	0.010	
Ь	0.65	0.85	0.026	0.033	
ь2	0.95	1.15	0.037	0.045	
С	0.33	0.65	0.013	0.026	
c2	1.17	1.40	0.046	0.055	
D	8.51	9.45	0.335	0.372	
D1	7.10	7.90	0.280	0.311	
E	9.80	10.31	0.386	0.406	
E1	6.50	8.60	0.256	0.339	
е	2.5	54	0.1	100	
e1	5.0	8	0.200		
N		2	2		
Н	14.61	15.88	0.575	0.625	
L	2.29	3.00	0.090	0.118	
L1	0.70	1.60	0.028	0.063	
L2	1.00	1.78	0.039	0.070	
F1	16.05	16.25	0.632	0.640	
F2	9.30	9.50	0.366	0.374	
F3	4.50	4.70	0.177	0.185	
F4	10.70	10.90	0.421	0.429	
F5	3.65	3.85	0.144	0.152	
F6	1.25	1.45	0.049	0.057	







 $i_{,V}$ $di_{_{F}}/dt$ $t_{_{\Gamma\Gamma}} = t_{_{S}} + t_{_{F}}$ $Q_{_{\Gamma\Gamma}} = Q_{_{S}} + Q_{_{F}}$ $t_{_{\Gamma\Gamma}}$ $t_{_{\Gamma\Gamma}}$ $Q_{_{S}} - Q_{_{F}} - t_{_{F}}$ $di_{_{\Gamma\Gamma}}/dt$ $V_{_{R}}$

Figure C. Definition of diodes switching characteristics

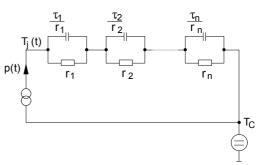


Figure A. Definition of switching times

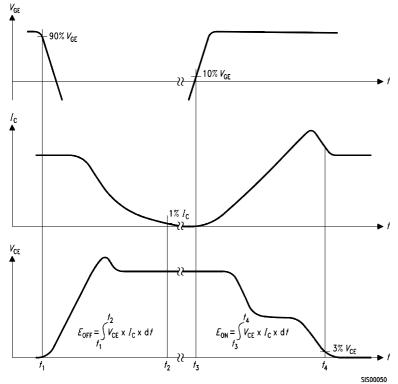
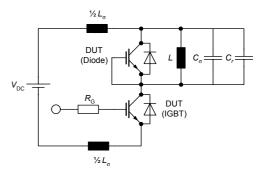


Figure D. Thermal equivalent circuit



Leakage inductance L_{σ} = 180nH, Stray capacitor C_{σ} = 40pF, Relief capacitor C_{r} = 1nF (only for ZVT switching)

Figure E. Dynamic test circuit

Figure B. Definition of switching losses



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