

Preliminary

SIDC56D120E6

Fast switching diode chip in EMCON-Technology

FEATURES:

- 1200V EMCON technology 130 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

• EUPEC power modules and discrete devices



Applications:

 SMPS, resonant applications, drives

Chip Type	V _R	I _F	Die Size	Package	Ordering Code
SIDC56D120E6	1200V	75A	7.5 x 7.5 mm ²	sawn on foil	Q67050-A4127- A001

MECHANICAL PARAMETER:

Raster size	7.5 x 7.5				
Area total / active	56.25 / 46.65	mm ²			
Anode pad size	6.78 x 6.78				
Thickness	130	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	248 pcs				
Passivation frontside	Photoimide				
Anode metallisation	3200 nm AlSiCu				
Cathode metallisation	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	Al, ≤500µm				
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm				
Recommended Storage Environmentstore in original container, in dry nitro< 6 month at an ambient temperature of					

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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V _{RRM}		1200	V
Continuous forward current limited by T _{jmax}	I _F		75	
Single pulse forward current (depending on wire bond configuration)	I _{FSM}	$t_P = 10 ms sinusoidal$	tbd	А
Maximum repetitive forward current limited by T _{jmax}	I _{FRM}		150	
Operating junction and storage temperature	T_{j} , T_{stg}		-55+150	°C

Static Electrical Characteristics (tested on chip), T_j =25 °C, unless otherwise specified

Parameter	Symbol	Cond	Value			Unit	
Falameter	Symbol	Conditions		min.	Тур.	max.	Onic
Reverse leakage current	I _R	V _R =1200V	$T_j=25 °C$			27	μA
Cathode-Anode breakdown Voltage	V _{Br}	I _R =4mA	<i>T_j</i> =25°C	1200			V
Forward voltage drop	V _F	I _F =75A	<i>T_j</i> =25 °C		1.9		V

Dynamic Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol	Conditions		Value			Unit	
Falallelel	Symbol			min.	Тур.	max.		
Reverse recovery time	t _{rr1}	I _F =75A	$T_j = 25 °C$		tbd			
	t _{rr2}	<i>di/dt=1950A/m</i> s <i>V_R=600V</i>	$T_j = 125 ^{\circ}C$				ns	
Peak recovery current	I _{RRM1}	$I_F=75A$	$T_j = 25 °C$		88.5		А	
	I _{RRM2}	di/dt=1950A/ms $V_R=600V$	$T_j = 125 ^{\circ}C$		110]^	
Reverse recovery charge	Q _{rr1}	IF=75A	<i>T_j</i> =25°C		8.77		μC	
	Q _{rr2}	di/dt=1950A/ms $V_R= 600V$	<i>T_j</i> =125°C		16.44		٦٣٥	
Peak rate of fall of reverse	di _{rr1} /dt	I _F =75A	$T_{\rm j}=25^{\circ}C$		tbd		A (
recovery current	di _{rr2} /dt	di/dt=1950A/ms V _R = 600V	<i>T_j</i> =125°C				- A/μs	
Softness	S1	<i>I_F</i> =75A di/dt=1950A/ m s	<i>T_j</i> =25°C		tbd		1	
	S2	$V_R = 600V$	<i>T_j</i> =125°C					

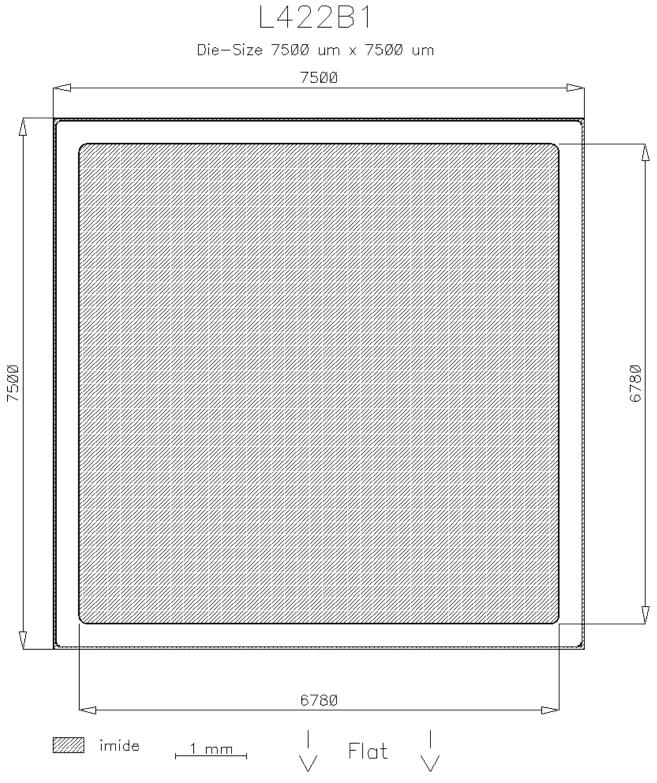
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CHIP DRAWING:



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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the	INFINEON TECHNOLOGIES /	tbd
device data sheet	EUPEC	lbu

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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