

SPOC™+2 User Manual

Multichannel SPI High-Side Power Controller

About this document

Scope and purpose

This User Manual is intended to enable users to integrate the SPOCTM+2 Software for the SPOCTM+2-Demoboard.

Intended audience

This document is intended for anyone using the SPOC[™]+2 Software.

Document conventions

Table 1 Conventions

Convention	Explanation
Bold	Emphasizes heading levels, column headings, table and figure captions, screen names, windows, dialog boxes , menus and sub-menus
Italics	Denotes variable(s) and reference(s)
Courier New	Denotes APIs, functions, interrupt handlers, events, data types, error handlers, file/folder names, directories, command line inputs, code snippets

Conventions for reading the configuration class field

The following examples help the integrator to identify the configuration class of the parameter for a given delivery type.



Abbreviations and definitions

Table 2	Abbreviations
Abbreviation	Definition
SPOC TM +2	SPI Power Controller
SPOC TM +2 MB	SPOC [™] +2 Motherboard
SPOC TM +2 DB	SPOC [™] +2 Daughterboard
NC	Not Connected
LHI	Limp Home Input
IS	Sense current



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1 General information

1.1 Required hardware

First of all some special hardware is needed:

- SPOCTM+2 MB
 - \circ SPOCTM+2 Motherboard
 - o See Figure 1



Figure 1 – SPOC[™]+2 MB

- **SPOC[™]+2 DB**
 - Product specific (BTSxxxxx-xxxx)
 - See Figure 2



Figure 2 – SPOC[™]+2 DB



- µIO-Stick
 - o Communication between your computer and the Demoboard
 - o Isar Number: SP001215532
 - See Figure 3



Connection cable

- Ribbon cable
 - 16 pin female connector
- See figure 4

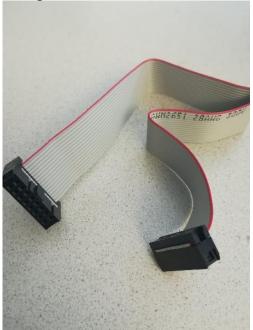


Figure 4 - Connection cable



- USB-Stick
 - For software installation
 - See Figure 5



Figure 5 - Software installation USB-Stick

1.2 Software Installation

1.2.1 SPOCTM+2 Application

How to install software for the **SPOC[™]+2 Evaluation Board**:

- Plug in the Software-USB-Stick into a USB port of your computer
- Run **setup.exe** in the following location:
 - USB-Drive:\SPOC+2_Installer\Volume\setup.exe (see Figure 6)

This PC > USB Drive (E:) > SPOC+2_Installer > Volume

^	Name	Date modified	Туре	Size
	bin license	19.10.2017 13:20 19.10.2017 13:20	File folder File folder	
	supportfiles	19.10.2017 13:20	File folder	
	nidist.id	04.09.2017 12:03	ID File	1 KB
	😽 setup	17.02.2016 16:26	Application	1 423 KB
Figu	⊌ setup setup 6 – SPOC™+2 - setup.exe	04.09.2017 12:03	Configuration sett	22 KB

- Note: You must log in as administrator!
- Follow the steps of the Installation Wizard (see Figures below):
 - Select installation directory, then click "next"

ination Directory ect the primary installation directory.		
tware will be installed in the following I nt location, click the Browse button a		
ctory for SPOC+2 Program Files (x86)\SPOC+2\	 Bro	wse
 ctory for National Instruments product Program Files (x86)\National Instrumer	 Bro	wse

Figure 7 - SPOC[™]+2 Application - Select Directory

Again click "next"



	I SPOC+2		_		×
	Start Installation Review the following summary before continuing	,			
	Adding or Changing • SPOC-22 Files • NI-VISA 14.0 Run Time Support				
	, Click the Next button to begin installation. Click the Back bu	itton to change the	installation setting	s.	
	Save File Figure 8 – SPOC TM +2 - Applica	<pre><< Back ation - St</pre>	Next>> art Instal	Cance	I
The insta	Illation will start, after that o	click "fin	ish"	_	
	🦏 SPOC+2		-		×
	The installer has finished updating your system.				
		<< Back	Next>>	Finish	

Figure 9 – SPOC[™]+2 - Application – Finish

1.2.2 µIO-Stick Driver Install

After installing the SPOCTM+2-Application keep the Software-USB plugged in and plug in the μ IO-Stick. In order to use the virtual COM-port (neccesairy for the application) go to <u>https://www.ehitex.de/usb-application-sticks/infineon/2529/uio-stick</u>, scroll to **Available Downloads** and select **Download uIO Updater** (*zip file*) see Figure 10. Extract the zip file and run UpdatePEK afterwards.



Figure 10 - µIO-Updater (for vCOM)

1.3 Setup Hardware

• Plug the SPOCTM+2 DB onto the SPOCTM+2 MB (see Figure 11)





Figure 11 – SPOC[™]+2 DB plugged onto SPOC[™]+2 MB

- Connect the μ IO-Stick to the SPOCTM+2 MB via the connector cable (see Figure 12)
 - **Be careful**: Position of Pin 1 is marked with a dot on the SPOCTM+2 MB!

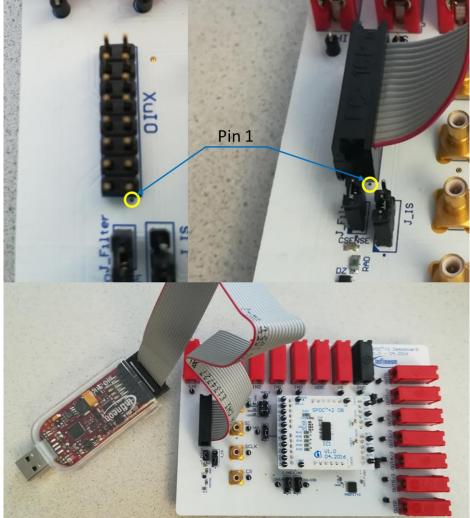


Figure 12 - Connecting µIO-Stick to SPOC[™]+2 MB

• Connect the μ IO-Stick to your computer and run the SPOCTM+2-Application. (see Figure 13)



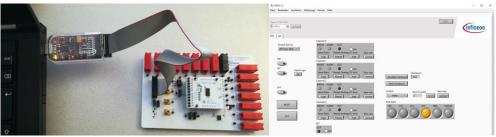


Figure 13 - Finished setup

1.3.1 Setup details

Table 3	
ΧμΙΟ	
Pin #	
0 1	NC
0 2	GND
03	NC
0 4	+5VμΙΟ
° 5	NC
0 6	NC
° 7	NC
0 8	IN ₃
09	CS
0 10	IN2
0 11	SCLK
0 12	IN1
0 13	SO
0 14	INo
o 1 5	SI
o 16	Sense
J_Filter	Closed by default
J_IS	1-2 closed by default
	If 2-3 is closed:
	 J_Filter has to be opened!
	 Sense directly switched to IS (Filter
	disconnected)
J_GND	1-2 closed: R_GND = 150 Ω (default)
	3-4 closed: R_GND = 50 Ω
	5-6 closed: R_GND = 0 Ω
J_IN2	3-2 closed by default.
	If 1-2 is closed:
	Connect PROFET ²
J_IN ₃	3-2 closed by default.
	If 1-2 is closed:
	Connect PROFET ²
J_VDD	1-2 closed (default VDD via µIO-Stick):
	 Use +5VμIO (USB) as digital supply
	voltage
	2-3 closed:
	VDD via external supply
J_cho¹	Closed if 4 Channel SPOC [™] +2 is used:



	OUTo and OUT1/o in parallel.
J_ch3 ¹	Closed if 4 Channel SPOC+2 is used:
	 OUT4/3 and OUT5/3 in parallel.
SO	Slave Out (SPI Interface)
SI	Slave In (SPI Interface)
SCLK	Serial Clock (SPI Interface)
CS	Chip Select (SPI Interface)
INo	activate the corresponding output channel
IN1	activate the corresponding output channel
IN2/EDD ²	activate the corresponding output channel
IN ₃ /EDO ²	activate the corresponding output channel
OUTo	Output channel o
OUT1/0 ¹	Output channel 1 if J_cho is open,
	Output channel o if J_cho is closed
OUT2/1 ¹	Output channel 2 if J_cho is open,
	Output channel 1 if J_cho is closed
OUT3/2 ¹	Output channel 3 if J_cho is open,
	Output channel 2 if J_cho is closed
OUT4/3 ¹	Output channel 4 if J_ch3 is open,
	Output channel 3 if J_ch3 is closed
OUT5/3 ¹	Output channel 5 if J_ch3 is open,
	Output channel 3 if J_ch3 is closed
OUTP	Output PROFET ²
IS	Sense current
LIH	Limp Home Input
VDD	Digital supply voltage
GND	Ground
VS	Supply voltage

¹ If 4-Channel-SPOC[™] are used J_cho and J_ch₃ had to be closed, otherwise (6-Channel-SPOC) opened.

² If Jumpers J_IN₂ and J_IN₃ are set to 1-2: SPOC[™] Outputs EDD and EDO can control the PROFET.

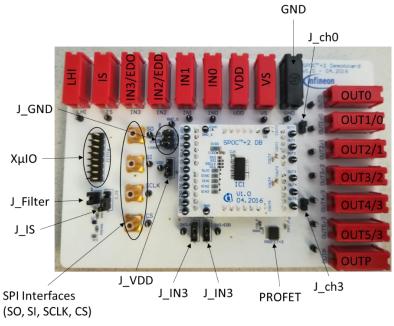


Figure 14 - Device components



2 Using the software

2.1 Starting the program

Installed Application can be found in the windows start menu in the section "all programs" → "SPOC+2"
 → SPOC+2 (see Figure 15):

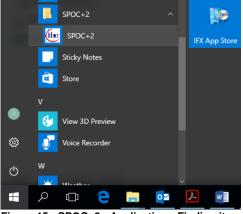


Figure 15 - SPOC+2 - Application - Finding it

or by using the search bar (see Figure 16):

<u></u>	- the sea	i chi bui	(See Figor
≡	ē D	ŝ	Filters \checkmark
ඛ	Best match		
	SPOC+ Desktop		
	Folders		
	SPOC+2		
	Documents		
	SPOC+ Po v0.2.doc	ower easy Kit	getting started
ŝ			
2			
	P spoc+2		
	<u>ں</u> م	е 蒚	oz 📐 🕡
Fig	uro 16 – S	POCTM_	2 - Applicatio

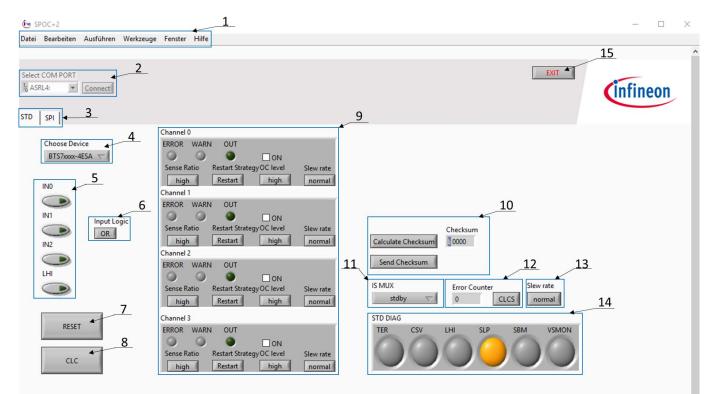
Figure 16 – SPOC[™]+2 - Application - searching for it

• Run the program by clicking on the file (see Figures 15 and 16)



2.2 User Interface

2.2.1 STD-View



Tabl	e 4 - STD-View		
1	Menu bar	*	
2	Port selection	Software should detect the Comport automatically.	
		 If not: select the needed Comport. 	
3	Standard or SPI view	Change between a button based control(Figure 17) and a low level SPI command	
		control(Figure 18)	
4	Device selection	Select the used device type	
5	Direct inputs and LHI	Switch INo-IN2 on or off	
		LHI can be set on or off	
6	Input logics	OR/AND operation between direct inputs (INo-IN3) with the channel's on-status	
		(channel o to 3)	
7	Reset	Reset SPOC [™] +2	
8	Clear	Clear all Error latches and error counter	
9	Channels	Indicates the Status of a channel (Error, Warn, OUT, Slew rate)	
		and configures the channel (ON, Sense ratio, Restart Strategy, OC level)	
		•	
10	Checksum	Shows the calculated checksum reflecting the configuration. This value will be	
		transmitted when clicking on send checksum.	
11	IS MUX	Configure the mux setting. For more information see datasheet of SPOC [™] +2	
12	Error counter	Error counter of the selected channel	
		•	
13	Slew rate settings	configures the slew rate of the selected channel	
14	Status display	Visualizes the standard diagnosis (spi response)	
15	Exit button	Closes connection and program	



 \times

SPI-View 2.2.2

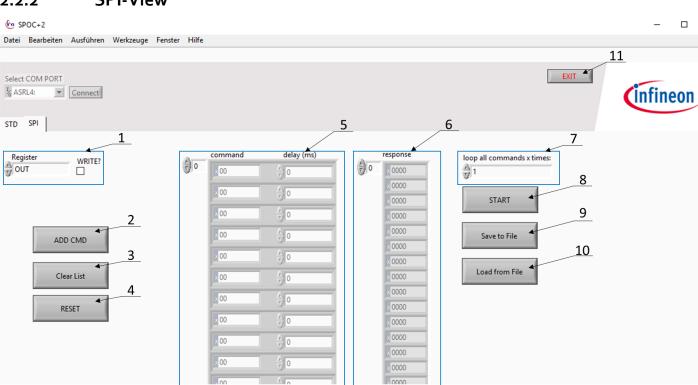


Figure 18 - User Interface - SPI-View

Table 5	Table 5 - SPI-View						
1	Register selection	Select the register addres and in case WRITE is enabled the content for the					
		next spi command. If WRITE is disabled a read command will be added.					
2	Add command	Adds the composed command to the command list (see 8)					
3	Clear command list	Clears the content of the command list					
4	Reset	Resets SPOC [™] +2					
5	Command list	Displays all added commands in ascending order					
6	Response list	Displays SPOC [™] +2's response to the currently processed command (see					
		datasheet of the used SPOC [™] +2)					
7	Loop configuration	Commands are embedded in a loop					
		Change the number of iterations (default is 1)					
8	Start button	Starts the command sequence resp. the loop					
9	Save to file	Saves the command sequence to a file					
10	Load from File	Loads a saved command sequence from a file					
11	Exit button	Closes connection and program					

*...menu description not necessairy at the moment \rightarrow Tobias



2.3 Examples: Command sequences, SPI-View

Figure 19 below illustrates a possible test setup. The examples in 2.3.1 and 2.3.2 refer to this setup.

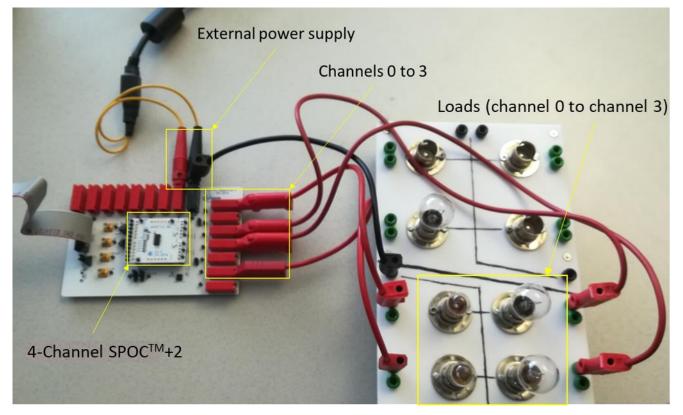


Figure 19 - Possible setup

STD SPI

2.3.1 Example 1: Switching on 4 lights step by step with 1 Second delay

• Switch to SPI view and select register OUT (See Figure 20)

egister	WRITE?	a	command	delay (ms)
√ OUT		(†) o	× 00	
RCS			-	
OCR			× 00	0
RCD				
KRC			× 00	(<u>^</u>)
SRC				
HWCR	IMD		× 00	0
ICS			× 00	$\left(\frac{\lambda}{\tau}\right)$ 0
PCS			MOO	5/0
DCR	List		× 00	A) 0
WRNDIAG			Part -	
STDDIAG			× 00	
ERRDIAG	ET			
/			× 00	() O

Figure 20 - Select register OUT

1



• Tick the WRITE?-Box and select no Output (See Figure 21)

STD SPI			
Register WRITE?	() o	command	delay (ms)
OUT	ψ.	× 00	
OUT0 OUT1 OUT2 OUT3 extD OUT5		× 00	
		× 00	0
ADD CMD		× 00	0
		× 00	0
Clear List		× 00	
RESET		× 00	
		× 00	0
		×00	

Figure 21 - Write to register OUT

• Click ADD CMD and your command is added to the command list (See Figure 22)

STD SPI		
Register WRITE?	mmand 80	delay (ms)
OUT0 OUT1 OUT2 OUT3 extD OUT5	00	
ADD CMD	00	↔ 0
Clear List	00	A) 0
RESET	00	
	00	A 0

Figure 22 - Add command to command list

• Select OUTo in the WRITE-Box (See Figure 23)

STD SPI			
Register WRITE?	0 comr	nand delay (m	s)
OUT0 OUT1 OUT2 OUT3 extD OUT5	× 00	() () () ()	
ADD CMD	×00		
Clear List	× 00	0 () ()	_
RESET	× 00		
			_

Figure 23 - Write OUT0



• Click ADD CMD (See Figure 24)

STD SPI		
Register WRITE?	comma	and delay (ms)
	×81	
ADD CMD	× 00	
Clear List	00	
RESET	× 00	

Figure 24 - Add to command list

• Change the delay of each command to 1000 (delay of 1 second, See Figure 25)

STD SPI				
Register WRITE?	0	command	delay (ms)	
OUTO OUT1 OUT2 OUT3 extD OUT5		×81	0 7	
		×00	0	
ADD CMD		× 00	() ()	
		× 00	(<u>)</u>	
Clear List		×00	() ()	
RESET		× 00	<u>()</u> 0	
NESE I		x 00		

Figure 25 - change delay of cammands

• Repeat the last three steps until you reach OUT₃ (See Figure 26)

STD SPI			
Register WRITE?	<u>/</u> 0	command	delay (ms)
OUT0 OUT1 OUT2 OUT3 extD OUT5		x 81	A 1000 ★ 1000
ADD CMD		x 87	() 1000 () 1000
Clear List		×00	
RESET		× 00	

Figure 26 - repeat until all OUTS (OUT0-OUT3) are selected



• Switch to STD view (See Figure 27) and select a channel (o - 3) at IS MUX (See Figure 28)

STD	SPI		
		Channel 0	
	Choose Device	ERROR WARN OUT	
	BTS7xxxx-4ESA 🤝	○ ○ ○ □ ON	
		Sense Ratio Restart Strategy OC level Slew rate	
	INO	high Restart high normal	
		Channel 1	
		ERROR WARN OUT	
	IN1 Input Logic	○ ○ ○ □ ON	
		Sense Ratio Restart Strategy OC level Slew rate	Checksum
	IN2	high Restart high normal	Calculate Checksum
		Channel 2	
	- Harrison - Contraction - Con	ERROR WARN OUT	Send Checksum
	LHI	○ ○ ○ □ ON	
		Sense Ratio Restart Strategy OC level Slew rate	IS MUX Error Counter Slew rate
		high Restart high normal	stdby To 0 CLCS normal
		Channel 3	STD DIAG
	RESET	ERROR WARN OUT	TER CSV LHI SLP SBM VSMON
		○ ○ ○ □ ON	
		Sense Ratio Restart StrategyOC level Slew rate	
	CLC	high Restart high normal	
		· · · · · · · · · · · · · · · · · · ·	

Figure 27 - switch to STD view

STD SPI			
Choose Device BTS7xxxx-4ESA マ IN0	Channel 0 ERROR WARN OUT Sense Ratio Restart StrategyOC level Slew rat high Restart high norma Channel 1 ERROR WARN OUT		
IN1 Input Logic IN2 LHI IN2	ON Sense Ratio Restart Strategy OC level Slew rat high Restart high norma Channel 2 ERROR WARN OUT Sense Ratio Restart Strategy OC level Slew rat high Restart high norma	I Channel 2 Channel 3 Channel 4 Channel 5 none v stdby	Checksum 00000 Error Counter Slew rate 0 CLCS normal
CLC	Channel 3 ERROR WARN OUT Sense Ratio Restart StrategyOC level Slew rat high Restart high norma	STD DIAG	

Figure 28 - Select channel at IS MUX

• Switch back to SPI view and click the START-button (See Figure 29)

STD SPI						
Register WRITE?	× 0	command	delay (ms)	0	response	loop all commands x times:
OUTO OUT1 OUT2 OUT3 extD OUT5		x 81	€ 1000		× 0000	START
		x 83	A) 1000		× 0000	
ADD CMD		x 87	x 1000		× 0000 × 0000	Save to File
Clear List		×8F	() 1000 () 0		× 0000	Load from File
		00			× 0000	
RESET		× 00	() ()		× 0000 × 0000	





• The response of the SPOC[™]+2 is shown in the response list (See Figure 30)

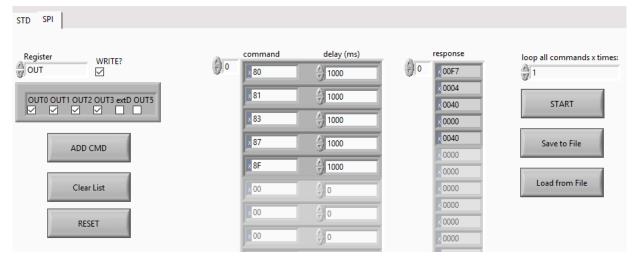


Figure 30 - response of SPOC[™]+2 to command sequence

2.3.2 Example 2: Let one light blink 10 times

	Description	t [ms]
T_ON	Duration light on	500
T_OFF	Duration light off	500

• Switch to SPI view and select register OUT (See Figure 31)

STD SPI				
Register		a	command	delay (ms)
✓ OUT RCS		0	× 00	0
OCR RCD			× 00	
KRC SRC			× 00	0
HWCR ICS	IMD		× 00	0
PCS DCR			× 00	0
WRNDIAG	List		× 00	
ERRDIAG	ET		× 00	
			× 00	
			× 00	0

Figure 31 - Select Register OUT



• Tick the WRITE?-Box and select OUTo and click ADD CMD (See Figure 32)

STD SPI			
Register WRITE?	() ()	command	delay (ms)
		×00	
ADD CMD		×00	
Clear List		×00	
RESET		×00	

Figure 32 - Select OUT0 and add command

• Select no output in the WRITE-Box and click ADD CMD (See Figure 33)

STD SPI			
Register WRITE?	() 0	command	delay (ms)
OUT0 OUT1 OUT2 OUT3 extD OUT5		× 80	
ADD CMD		× 00	() ()
Clear List		× 00	(1) 0 (1) 0
RESET		× 00	

Figure 33 - Select no output and add command

- Also typing in commands directly is possible (See SPOC[™]+2 datasheet for command reference)
 E.g.:
 - The command 80h means Write no output to OUT
 - The command 81_h would mean *Write to OUT and set OUTo high*.
 - See Figure 32 above
- Change the delay of command 81 to T_ON, the delay of command 80 to T_OFF (See Figure 34)



STD SPI			
Register WRITE?	<u><</u> 0	command	delay (ms)
OUTO OUT1 OUT2 OUT3 extD OUT5		× 80	
ADD CMD		×00	<u>^</u> 0
Clear List		× 00 × 00	
RESET		×00 ×00	

Figure 34 - change delay of commands

STD SPI

• Change loop all commands x times to 10 (See Figure 35)

Register WRITE?	0	command	delay (ms)	0000	loop all commands x times:
OUT0 OUT1 OUT2 OUT3 extD OUT5		× 80	500 () () ()	× 0000 × 0000 × 0000	START
ADD CMD		200		× 0000 × 0000	Save to File
Clear List		× 00		× 0000 × 0000	Load from File
RESET		200		× 0000	
REJEI		× 00		× 0000	

Figure 35 - change number of command sequences iterations

• Switch to STD view (See Figure 36) and select a channel (o - 3) at IS MUX (See Figure 37)

STD	SPI		
		Channel 0	
	Choose Device	ERROR WARN OUT	
	BTS7xxxx-4ESA 🤝	🎱 🎱 🔵 🗌 ON	
		Sense Ratio Restart Strategy OC level Slew rate	
	INO	high Restart high normal	
		Channel 1	
		ERROR WARN OUT	
	IN1 Input Logic	🔘 🔘 🔍 🗌 ON	
		Sense Ratio Restart StrategyOC level Slew rate	Checksum
	IN2	high Restart high normal	Calculate Checksum 6 0000
		Channel 2	
		ERROR WARN OUT	Send Checksum
	LHI		
		Sense Ratio Restart Strategy OC level Slew rate	IS MUX Error Counter Slew rate
	<u> </u>	high Restart high normal	stdby abla 0 CLCS normal
		Channel 3	STD DIAG
	RESET	ERROR WARN OUT	TER CSV LHI SLP SBM VSMON
		○ ○ ○ □ ON	000000
		Sense Ratio Restart Strategy OC level Slew rate	
	CLC	high Restart high normal	

Figure 36 - Switch to STD view



STD	SPI			
		Channel 0		
(Choose Device	ERROR WARN OUT		
	BTS7xxxx-4ESA 🤝	○ ○ ○ □ ON		
		Sense Ratio Restart Strategy OC level SI	lew rate	
	NO	high Restart high	normal	
		Channel 1		
(ERROR WARN OUT		
1	N1 Input Logic	O	Channel 0	
(lew rate Channel 1	Checksum
	N2	high Restart high	normal Channel 2	b 0000
0		Channel 2	Channel 3	
	H H	ERROR WARN OUT	Channel 4 Channel 5	
	H	○ ○ ○ □ ON	none	From Coupter Slew rate
(lew rate 🗸 stdby	Lifer Counter
		high Restart high	normal ext Drive	0 CLCS normal
		Channel 3	STD DIAG	
	RESET	ERROR WARN OUT	TER CSV	LHI SLP SBM VSMON
		○ ○ ○ □ ON	00	$\bigcirc \bigcirc $
	CI C	Sense Ratio Restart StrategyOC level SI	lew rate	
	CLC	high Restart high	normal	
	1	nign ivestart nign	normai	

Figure 37 - Select a channel at IS MUX

• Switch back to SPI view and click the START-button (See Figure 38)

STD SPI						
Register WRITE?	() 0	command	delay (ms)	0	× 00F7 × 0000	loop all commands x times:
OUT0 OUT1 OUT2 OUT3 extD OUT5		× 80			× 0000	START
ADD CMD		x00	(<u>)</u> 0		× 0000 × 0000	Save to File
Clear List		x 00	€0 €0		× 0000 × 0000	Load from File
RESET		×00			× 0000 × 0000 × 0000	
			50°		M0000	

Figure 38 - Switch to SPI and start command sequence

• The response of the SPOC[™]+2 is shown in the response list (See Figure 39)

515					
Register WRITE?	0	command	delay (ms)	response	loop all commands x times:
		× 80	500	× 0000 × 0000	START
ADD CMD		00		× 0000 × 0000 × 0000	Save to File
Clear List		×00		× 0000 × 0000	Load from File
RESET		× 00	() 0	× 0000 × 0000	
		× 00		× 0000	

Figure 39 - Response od SPOC[™]+2 in response list

STD SPI



Revision history

Major changes since the last revision

Date	Version	Description

Template revision history

Note: The below table is for reference purpose only. Delete this table before circulation.

Changes since the last revision

Date	Version	Author	Description
11-2017	1.0	Rasser René	Initial released version

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