

Fast switching diode chip in Emitter Controlled 3 -Technology

Features:

- 600V Emitter Controlled 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient
- This chip is used for:
- Power module



Applications:

Drives

Chip Type	V _R	I _F	Die Size	Package
SIDC26D60C8	600V	100A	6.53 x 4.02 mm ²	sawn on foil

Mechanical Parameters

6.53 x 4.02		
26.25	mm ²	
5.83 x 3.32		
70	μm	
200	mm	
1032		
Photoimide		
3200 nm AlSiCu		
Ni Ag –system suitable for epoxy and soft solder die bonding		
Electrically conductive glue or solder		
Al, ≤500µm		
Ø 0.65mm; max 1.2mm		
Store in original container, in dry nitrogen, in dark environment, < 6 month at an ambient temperature of 23°C		
	26.25 26.25 5.83×3.32 702001032Photoimide3200 nm AlSiCuNi Ag –system suitable for epoxy and soft solder die bond Electrically conductive glue or solderAl, \leq 500µm \emptyset 0.65mm; max 1.2mmStore in original container, in dry nitrogen, in	



Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	<i>T</i> _{vj} = 25 ℃	600	V
Continuous forward current	/ _F	<i>T</i> _{vj} < 150℃	1)	^
Maximum repetitive forward current	I _{FRM}	<i>T</i> _{vj} < 150℃	200	A
Junction temperature range	T _{vj}		-40+175	°C
Operating junction temperature	T _{vj}		-40+150	°C
Dynamic ruggedness ²⁾	P _{max}	$I_{\rm Fmax}$ = 200A, $V_{\rm Rmax}$ = 600V, $T_{\rm vj} \leq 150$ °C	tbd	kW

¹⁾ depending on thermal properties of assembly

²⁾ not subject to production test - verified by design/characterisation

Static Characteristics (tested on wafer), T_{vj} = 25 °C

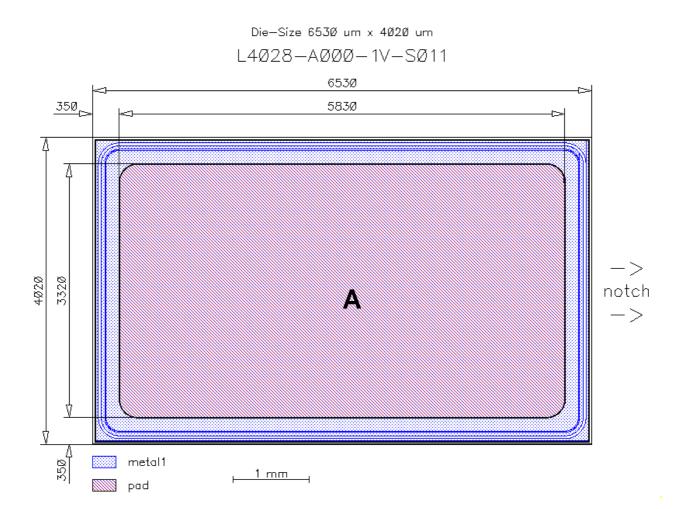
Parameter	Symbol	Conditions	Value			Unit
Falameter	Symbol	Conditions	min.	typ.	max.	Onic
Reverse leakage current	I _R	V _R =600V			27	μA
Cathode-Anode breakdown Voltage	V _{BR}	/ _R =0.25mA	600			V
Diode forward voltage	V _F	$I_{\rm F} = 100 {\rm A}$	1.2	1.6	1.9	V

Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.



Chip Drawing



A: Anode pad

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Description

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Version	Subjects (major changes since last revision)	Date

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