

SIPMOS® Small-Signal-Transistor

BSP320S

Features

Product Summary

- N channel
- Enhancement mode
- Avalanche rated

,			
Drain source voltage	$V_{\rm DS}$	60	٧
Drain-Source on-state resistance	R _{DS(on)}	0.12	Ω
Continuous drain current	I D	2.9	Α

- Pb-free lead plating; RoHS compliant
- Qualified according to AEC Q101
- Halogen-free according to IEC61249-2-21







Туре	Package	Tape and Reel	Packaging
BSP320S	PG-SOT223	H6327: 1000pcs/r	Non dry
BSP320S	PG-SOT223	H6433: 4000pcs/r	Non dry

Maximum Ratings, at Tj = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current	I _D	2.9	Α
Pulsed drain current	<i>I</i> Dpulse	11.6	
$T_{A} = 25 ^{\circ}\text{C}$			
Avalanche energy, single pulse	E _{AS}	60	mJ
$I_{D} = 2.9 \text{ A}, \ V_{DD} = 25 \text{ V}, \ R_{GS} = 25 \ \Omega$			
Avalanche current, periodic limited by T_{jmax}	/ _{AR}	2.9	Α
Avalanche energy, periodic limited by T_{jmax}	E _{AR}	0.18	mJ
Reverse diode dv/dt	d <i>v</i> /d <i>t</i>	6	kV/μs
$I_{S} = 2.9 \text{ A}, \ V_{DS} = 20 \text{ V}, \ di/dt = 200 \text{ A/}\mu\text{s},$			
$T_{\text{jmax}} = 150 ^{\circ}\text{C}$			
Gate source voltage	$V_{\rm GS}$	±20	V
Power dissipation	P _{tot}	1.8	W
$T_A = 25 ^{\circ}C$			
Operating temperature	$ T_{i} $	-55 +150	,C
Storage temperature	$T_{\rm stg}$	-55 +150	
IEC climatic category; DIN IEC 68-1		55/150/56	



Electrical Characteristics

Parameter	Symbol	Values			Unit		
at $T_i = 25$ °C, unless otherwise specified		min.	typ.	max.			
Thermal Characteristics							
Thermal resistance, junction - soldering point (Pin 4)	R _{thJS}	-	17	-	K/W		
SMD version, device on PCB:	R_{thJA}				K/W		
@ min. footprint		-	110	_			
@ 6 cm ² cooling area ¹⁾		-	-	70			

Static Characteristics

Drain- source breakdown voltage	V _{(BR)DSS}	60	-	-	V
$V_{GS} = 0 \text{ V}, I_{D} = 0.25 \text{ mA}$					
Gate threshold voltage, $V_{GS} = V_{DS}$	V _{GS(th)}	2.1	3	4	
$I_{\rm D} = 20 \; \mu {\rm A}$					
Zero gate voltage drain current	l _{DSS}				μΑ
$V_{\rm DS}$ = 60 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 25 °C		-	0.1	1	
$V_{\rm DS}$ = 60 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 150 °C		-	-	100	
Gate-source leakage current	l _{GSS}	-	10	100	nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$					
Drain-Source on-state resistance	R _{DS(on)}	-	0.09	0.12	Ω
$V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 2.9 A					

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¹ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70µm thick) copper area for drain connection. PCB is vertical without blown air.



Electrical Characteristics

Symbol	Values		Unit	
	min.	typ.	max.	
•	•	•	•	
g_{fs}	2.5	5.8	-	S
C_{iss}	-	275	340	pF
C_{oss}	-	90	120	
C_{rss}	-	50	65	
t _{d(on)}	-	11	17	ns
$t_{\rm r}$	-	25	40	
t _{d(off)}	-	25	40	
<i>t</i> _f	-	35	55	
	$g_{ m fs}$ $C_{ m iss}$ $C_{ m rss}$ $t_{ m d(on)}$ $t_{ m r}$	min. gfs 2.5 C _{iss} - C _{rss} - t _d (on) - t _d (off) -	min. typ. g_{fs} 2.5 5.8 C_{iss} - 275 C_{oss} - 90 C_{rss} - 50 $t_{d(on)}$ - 11 t_r - 25 $t_{d(off)}$ - 25	min. typ. max. g_{fs} 2.5 5.8 - C_{iss} - 275 340 C_{oss} - 90 120 C_{rss} - 50 65 $t_{d(on)}$ - 11 17 t_{r} - 25 40 $t_{d(off)}$ - 25 40



Electrical Characteristics

Parameter	Symbol	Values		Unit	
at $T_i = 25$ °C, unless otherwise specified		min.	typ.	max.	
Dynamic Characteristics	•				•
Gate charge at threshold	$Q_{G(th)}$	-	0.25	0.3	nC
$V_{\rm DD}$ = 40 V, $I_{\rm D}$ = 0.1 A, $V_{\rm GS}$ = 1 V					
Gate charge at V_{gs} =7V	$Q_{g(7)}$	-	7.4	9.3	nC
$V_{\rm DD} = 40 \text{ V}, I_{\rm D} = 2.9 \text{ A}, V_{\rm GS} = 0 \text{ to } 7 \text{ V}$					
Gate charge total	Q_q	-	9.7	12	
$V_{\rm DD}$ = 40 V, $I_{\rm D}$ = 2.9 A, $V_{\rm GS}$ = 0 to 10 V					
Gate plateau voltage	V _(plateau)	-	4.7	-	٧
$V_{\rm DD} = 40 \text{ V}, I_{\rm D} = 2.9 \text{ A}$., ,				

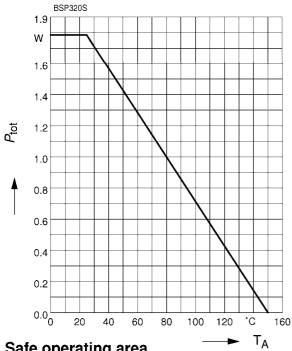
Reverse Diode

Inverse diode continuous forward current $T_A = 25 ^{\circ}\text{C}$	I _S	-	-	2.9	А
Inverse diode direct current,pulsed	/ _{SM}	-	-	11.6	
$T_{A} = 25 ^{\circ}\text{C}$					
Inverse diode forward voltage	V_{SD}	-	0.95	1.2	\ V
$V_{\rm GS} = 0 \text{ V}, I_{\rm F} = 5.8 \text{ A}$					
Reverse recovery time	$t_{\rm rr}$	-	45	56	ns
$V_{R} = 30 \text{ V}, I_{F} = I_{S}, dI_{F}/dt = 100 \text{ A/}\mu\text{s}$					
Reverse recovery charge	Q_{rr}	-	0.08	0.12	μC
V_{R} = 30 V, I_{F} = I_{S} , di_{F} / dt = 100 A/ μ s					



Power Dissipation

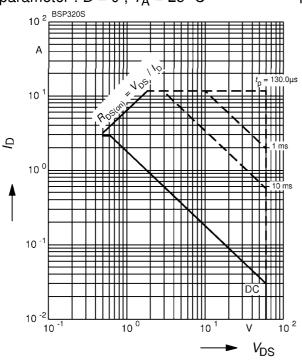
$$P_{\text{tot}} = f(\mathsf{T}_{\mathsf{A}})$$



Safe operating area

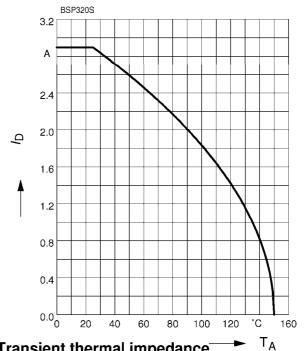
 $I_{D} = f(V_{DS})$

parameter : D = 0 , $T_A = 25$ °C



Drain current

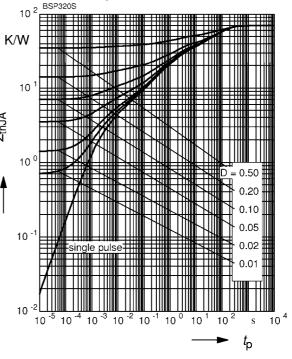
$$I_{\mathsf{D}} = f(T_{\mathsf{A}})$$



Transient thermal impedance

 $Z_{\text{thJA}} = f(t_p)$

parameter : $D = t_D/T$

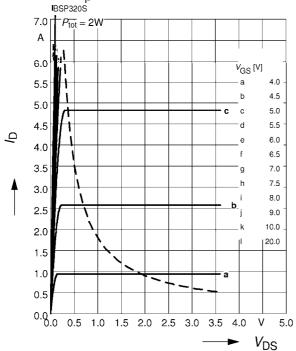




Typ. output characteristics

$$I_{\rm D} = f(V_{\rm DS})$$

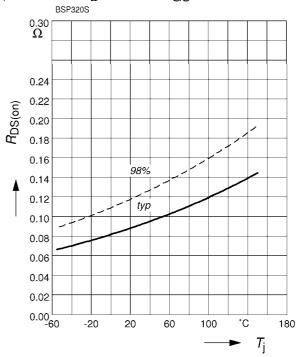
parameter:
$$t_p = 80 \mu s$$



Drain-source on-resistance

$$R_{DS(on)} = f(T_j)$$

parameter :
$$I_D$$
 = 2.9 A, V_{GS} = 10 V

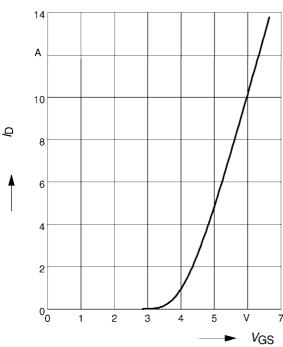




Typ. transfer characteristics I_{D} = $f(V_{GS})$

parameter: $t_p = 80 \mu s$

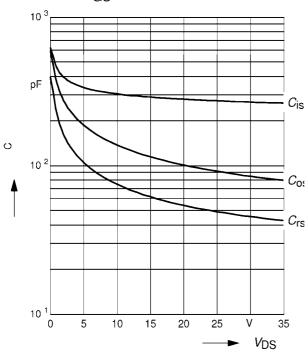
 $V_{DS} \ge 2 \times I_D \times R_{DS(on)max}$



Typ. capacitances

$C = f(V_{DS})$

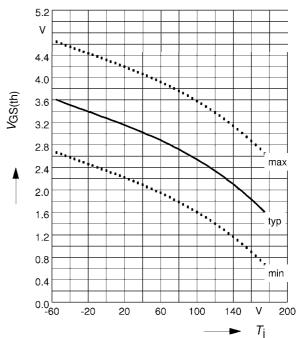
Parameter: $V_{GS}=0$ V, f=1 MHz



Gate threshold voltage

 $V_{GS(th)} = f(T_j)$

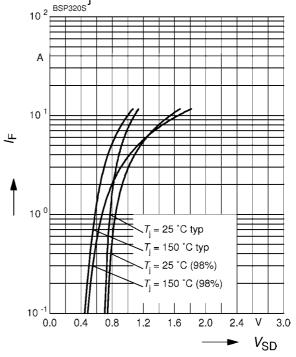
parameter : $V_{GS} = V_{DS}$, $I_D = 20 \mu A$



Forward characteristics of reverse diode

$$I_{\mathsf{F}} = f(\mathsf{V}_{\mathsf{SD}})$$

parameter: T_i , tp = 80 μ s

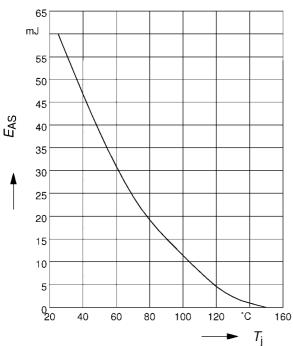




Avalanche Energy $E_{AS} = f(T_j)$

parameter:
$$I_D = 2.9 \text{ A}, V_{DD} = 25 \text{ V}$$

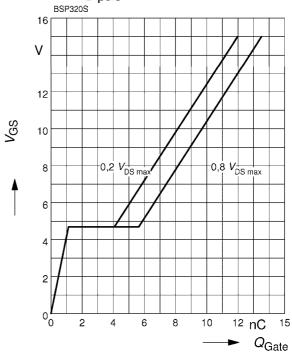
$$R_{\rm GS} = 25~\Omega$$



Typ. gate charge

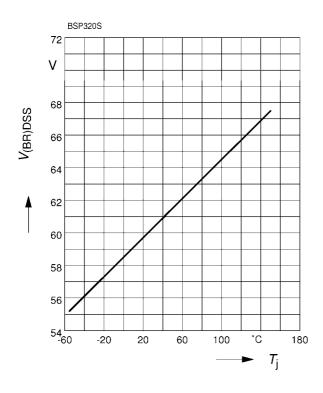
$$V_{\rm GS} = f(Q_{\rm Gate})$$

parameter: I_{D puls} =2.9A



Drain-source breakdown voltage

$$V_{(BR)DSS} = f(T_j)$$





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