

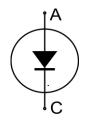
## Fast switching diode chip in Emitter Controlled Technology

#### Features:

- 1200V Emitter Controlled technology
   120 µm chip
- · Soft, fast switching
- Low reverse recovery charge
- Small temperature coefficient
- Qualified according to JEDEC for target applications

### Recommended for:

 Power modules and discrete devices



## **Applications:**

• SMPS, resonant applications, drives

Chip Type	$V_{R}$	<i>I</i> <sub>Fn</sub>	Die Size	Package
SIDC06D120H8	1200V	7.5A	2.45 x 2.45 mm <sup>2</sup>	sawn on foil

### **Mechanical Parameters**

Die size		2.45 x 2.45		
Area total		6	$\text{mm}^2$	
Anode pad size		1.73 x 1.73		
Thickness		120	μm	
Wafer size		200	mm	
Max. possible chips per wafer		4656		
Passivation frontside		Photoimide		
Pad metal		3200 nm AlSiCu		
Backside metal		Ni Ag – system  To achieve a reliable solder connection it is strongly recommended not to consume the Ni layer completely during production process		
Die bond		Electrically conductive epoxy glue and soft solder		
Wire bond		Al, ≤ 500 μm		
Reject ink dot size		Ø 0.65 mm; max 1.2 mm		
Storage environment	for original and sealed MBB bags	Ambient atmosphere air, Temperature 17 °C – 25 °C, < 6 months		
	for open MBB bags	Acc. to IEC62258-3: Atmosphere > 99% Nitrogen or inert gas Humidity < 25% RH, Temperature 17 °C – 25 °C, < 6 months		



## **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	<i>T</i> <sub>vj</sub> = 25 °C	1200	V
Continuous forward current	I <sub>F</sub>	<i>T</i> <sub>vj</sub> < 150 °C	1)	^
Maximum repetitive forward current <sup>2)</sup>	I <sub>FRM</sub>	<i>T</i> <sub>vj</sub> < 150 °C	15	
Operating junction temperature	$T_{vj}$		-40+175	°C

<sup>1)</sup> depending on thermal properties of assembly

## **Static Characteristics** (tested on wafer), $T_{vj}$ = 25 °C

Parameter	Symbol	Condition	Value			Unit
rarameter			min.	typ.	max.	Onn
Reverse leakage current	$I_{R}$	$V_{R} = 1200 V$			27	μA
Cathode-Anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 0.25 mA	1200			V
Forward voltage drop	$V_{F}$	$I_{\rm F} = 7.5  {\rm A}$	1.23	1.6	1.97	

### **Further Electrical Characteristics**

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

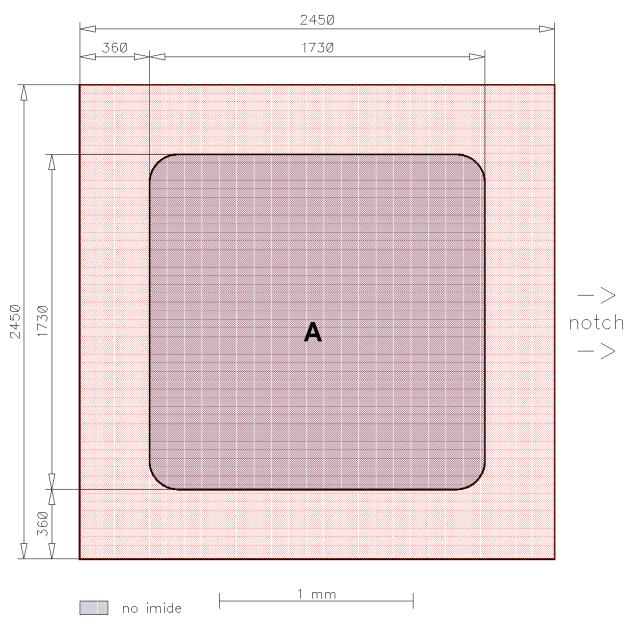
This chip data sheet refers to the device data sheet
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 $<sup>^{\</sup>rm 2)}$  not subject to production test - verified by design/characterisation



## **Chip Drawing**





A: Anode pad



## **Bare Die Product Specifics**

Test coverage at wafer level cannot cover all application conditions. Therefore it is recommended to test all characteristics which are relevant for the application at package level, including RBSOA and SCSOA.

## **Description**

AQL 0.65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

### **Revision History**

Version	Subject (major changes since last revision)	Date
2.0	Final data sheet	30.12.2014
2.1	Editorial changes	14.10.2015



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