

Diode EMCON 4 Medium Power Chip

FEATURES:

- 1200V EMCON 4 technology
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

• low / medium power modules



Applications:

low / medium power drives

Chip Type	V_R	I _F	Die Size	Package
IDC51D120T6M	1200V	100A	7.00 x 7.30 mm ²	sawn on foil

MECHANICAL PARAMETER:

Raster size	7.00 x 7.30				
Area total / active	51.10 / 39.99	mm^2			
Anode pad size	6.046 x 6.346				
Thickness	110	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	277 pcs				
Passivation frontside	Photoimide				
Pad metall	3200 nm AlSiCu				
Backside metall	Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500μm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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Maximum Ratings

Parameter	Symbol Condition		Value	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current limited by	1_		1)	
T_{jmax}	I _F			Α
Maximum repetitive forward current	I _{FRM}		200] `
limited by T _{jmax}	7F R M		200	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-40+175	°C
Safe operating area ²⁾ (SOA)	P _{Max}	Tvj= 150°C	tbd	kW

depending on thermal properties of assembly

Static Electrical Characteristics (tested on wafer), T_i=25 °C

Parameter	Symbol	Condi	Value			Unit	
raiailletei	Symbol	Condi	tions	min.	Тур.	max.] 01111
Reverse leakage current	I_{R}	V _R =1200V	T _j =25 °C			18	μΑ
Cathode-Anode breakdown Voltage	V _{Br}	I _R =0.25mA	<i>T_j</i> =25°C	1200			V
Forward voltage drop	V_{F}	I _F = 100A	<i>T_j</i> =25 °C	1.35	1.7	2.05	V

Dynamic Electrical Characteristics inductive load (not subject to production test - verified by design/characterization)

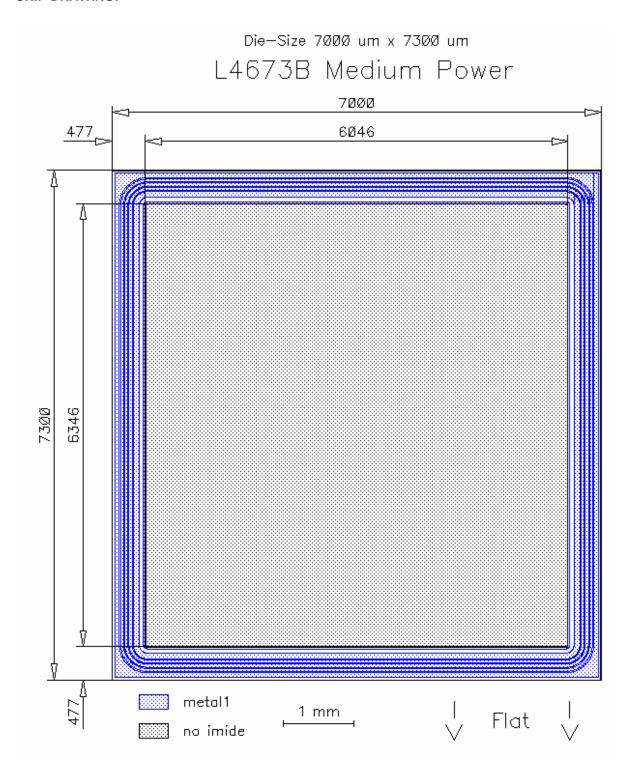
Parameter	Symbol	Conditions		Value 2)			Unit
raiailletei	Symbol			min.	Тур.	max.	
Peak reverse recovery current	I _{RM}	$I_F=A$ di/dt=A/ms $V_R=V$ $V_{GE}=-15V$	$T_j = 25 ^{\circ}\text{C}$ $T_j = 125 ^{\circ}\text{C}$ $T_j = 150 ^{\circ}\text{C}$		tbd		А
Reverse recovery charge	Q _r	$I_F=A$ di/dt=A/ms $V_R=V$ $V_{GE}=-15V$	$T_j = 25 ^{\circ}\text{C}$ $T_j = 125 ^{\circ}\text{C}$ $T_j = 150 ^{\circ}\text{C}$		tbd		μC
Reverse recovery energy	E _{rec}	$I_F = A$ di/dt = A/ms $V_R = V$ $V_{GE} = -15V$	$T_{j} = 25 ^{\circ}\text{C}$ $T_{j} = 125 ^{\circ}\text{C}$ $T_{j} = 150 ^{\circ}\text{C}$		tbd		mJ

 $^{^{2)}\ \}text{values}$ also influenced by parasitic L- and C- in measurement and package.

²⁾ not subject to production test- verified by design/characterisation



CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:					
This chip data sheet refers to the device data sheet	tbd				
Description:					
AQL 0,65 for visual inspection according to failure	catalog				
7 tal 0,00 for violati inopositori according to fandro	odialog				
Electrostatic Discharge Sensitive Device according					

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