



#### **DUAL CHANNELS CURRENT SENSE HIGH SIDE SWITCH**

#### **Features**

- 24V battery system
- Over current shutdown
- Over temperature shutdown
- Current sensing
- Active clamp
- Low quiescent current
- ESD protection
- Optimized Turn On/Off for EMI
- Lead free and RoHS compliant

#### **Applications**

- 21W Filament lamp
- Solenoid
- 24V truck loads

#### Description

The AUIPS7142G is a fully protected dual high side switch specifically designed for driving lamp. It features current sensing, over-current, over-temperature, ESD protection and drain to source active clamp. The Ifb pin is used for current sensing. The over-current shutdown is higher than inrush current of the lamp.

#### **Product Summary**

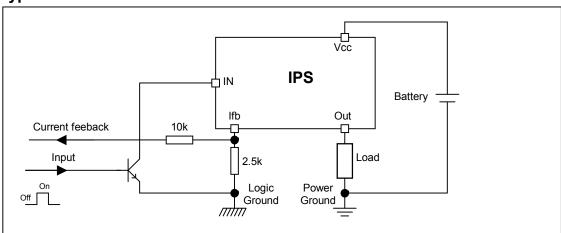
 $\begin{array}{ll} Rds(on) & 100m\Omega \ max. \\ Vclamp & 65V \\ Current \ shutdown & 20A \ min. \\ \end{array}$ 

### Package



SOIC16L-Wide Body

### **Typical Connection**





#### Qualification Information<sup>†</sup>

<u> </u>	tion information					
Qualification Level		Automotive (per AEC-Q100 <sup>††</sup> )  Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.				
Moisture Sensitivity Level		SOIC-16L WB	MSL2, 260°C (per IPC/JEDEC J-STD-020)			
	Machine Model		Class M4 (+/-450V) (per AEC-Q100-003)			
ESD	Human Body Model	Class H2 ( (per AEC-0	,			
	Charged Device Model	Class C4 (per AEC-0				
IC Latch-Up	Test		ClassII, Level A (per AEC-Q100-004)			
RoHS Comp	liant	Yes				

Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>
Exceptions to AEC-Q100 requirements are noted in the qualification report.



Absolute Maximum Ratings
Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. (Tj= -40°C..150°C, Vcc=6..50V unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vout	Maximum output voltage	Vcc-60	Vcc+0.3	V
l rev	Maximum reverse pulsed current (t=100µs) see page 8	_	30	
Isd cont.	Maximum diode continuous current Tambient=25°C, Rth=40°C/W / per channel		1.7	Α
Vcc-Vin max.	Maximum Vcc voltage	-16	60	V
lifb, max.	Maximum feedback current	-50	10	mA
Vcc sc.	Maximum Vcc voltage with short circuit protection see page 8	_	50	V
Pd	Maximum power dissipation (internally limited by thermal protection)			W
ru	Rth=40°C/W	_	3	VV
Tj max.	Max. storage & operating junction temperature	-40	150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient 6cm² footprint one Mosfet on	45		°C/W
Rth2	Thermal resistance junction to ambient 6cm² footprint two Mosfet on	40	_	C/VV

note: Tj-Tambient=Power dissipated in the 2 channel x Rth

# Recommended Operating Conditions These values are given for a quick design.

Symbol	Parameter	Min.	Max.	Units
lout	Continuous output current, Tambient=85°C, Tj=125°C			Δ
	Rth=40°C/W, 6cm² footprint	_	1.5	^
Rlfb	Ifb resistor	1.5	_	kΩ



#### **Static Electrical Characteristics**

Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
Vcc op.	Operating voltage	6	_	60	V		
Rds(on)	ON state resistance Tj=25°C	_	75	100	mo	lds=2A	
	ON state resistance Tj=150°C(2)	_	135	180	mΩ	lus-ZA	
Icc off	Supply leakage current	_	1	3		Vin=Vcc / Vifb=Vgnd	
lout off	Output leakage current	I —	1	3	μA	Vout=Vgnd, Tj=25°C	
I in on	Input current while on	0.6	2	4	mA	Vcc-Vin=28V, Tj=25°C	
V clamp1	Vcc to Vout clamp voltage 1	60	64	_		Id=10mA	
V clamp2	Vcc to Vout clamp voltage 2	60	65	72		Id=6A see fig. 2	
Vih(1)	High level Input threshold voltage	_	3	5		Id=10mA	
Vil(1)	Low level Input threshold voltage	1.5	2.3	_	\ \ \		
Vf	Forward body diode voltage Tj=25°C		0.8	0.9		If=1A	
	Forward body diode voltage Tj=125°C	_	0.65	0.75			

<sup>(1)</sup> Input thresholds are measured directly between the input pin and Vcc.

#### **Switching Electrical Characteristics**

Vcc=28V, Resistive load=27Ω, Tj=-40°C..150°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn on delay time to 20%	4	10	20	116	
Tr	Rise time from 20% to 80% of Vcc	2	5	10	μs	See fig. 1
Tdoff	Turn off delay time	20	40	80	110	See lig. 1
Tf	Fall time from 80% to 20% of Vcc	2.5	5	10	μs	

#### **Protection Characteristics**

Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tsd	Over temperature threshold	150(2)	165	_	°C	See fig. 3 and fig.11
Isd	Over-current shutdown	20	25	37	Α	See fig. 3 and page 7
I fault	Ifb after an over-current or an over-temperature (latched)	2.2	3	5	mA	See fig. 3

Current Sensing Characteristics

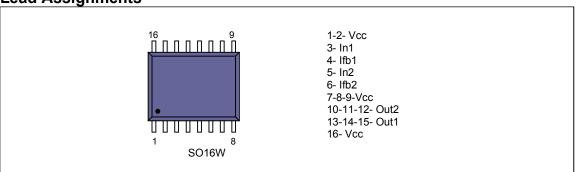
Tj=-40°C..150°C, Vcc=6-50V (unless otherwise specified). Specified 500µs after the turn on. Vcc-Vifb>4V

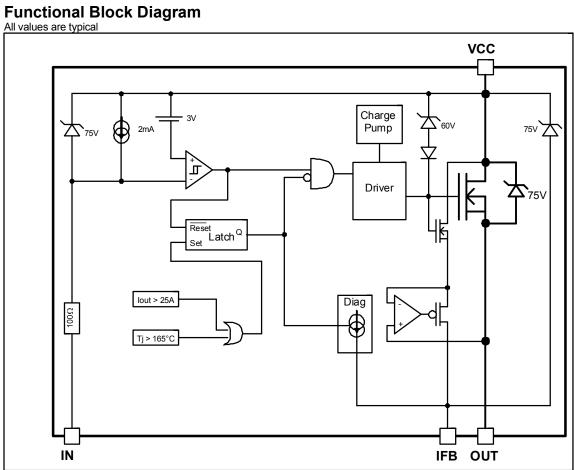
Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ratio	I load / Ifb current ratio	2000	2400	2800		Iload<4A
Ratio_TC	I load / Ifb variation over temperature(2)	-5%	0	+5	%	Tj=-40°C to +150°C
I offset	Load current offset	-0.02	0	0.02	Α	lout<4A
Ifb leakage	Ifb leakage current On in open load	0	1	10	μΑ	Iout=0A, Vcc-Vin=28V

<sup>(2)</sup> Guaranteed by design



**Lead Assignments** 







#### **Truth Table**

Op. Conditions	Input	Output	lfb pin voltage
Normal mode	Н	L	0V
Normal mode	L	Н	I load x Rfb / Ratio
Open load	Н	L	0V
Open load	L	Н	0V
Short circuit to GND	Н	L	0V
Short circuit to GND	L	L	V fault (latched)
Over temperature	Н	Ĺ	0V
Over temperature	Ĺ	Ĺ	V fault (latched)

#### Operating voltage

Maximum Vcc voltage: this is the maximum voltage before the breakdown of the IC process.

**Operating voltage**: This is the Vcc range in which the functionality of the part is guaranteed. The AEC-Q100 qualification is run at the maximum operating voltage specified in the datasheet.

#### Reverse battery

During the reverse battery the Mosfet is kept off and the load current is flowing into the body diode of the power Mosfet. Power dissipation in the IPS: P = I load \* Vf

There is no protection, so Tj must be lower than 150°C in the worst case condition of current and ambient temperature. If the power dissipation is too high in Rifb, a diode in serial can be added to block the current.

The transistor used to pull-down the input should be a bipolar in order to block the reverse current. The 100ohm input resistor can not sustain continuously 16V (see Vcc-Vin max. in the Absolute Maximum Ratings section)

#### **Active clamp**

The purpose of the active clamp is to limit the voltage across the MOSFET to a value below the body diode break down voltage to reduce the amount of stress on the device during switching.

The temperature increase during active clamp can be estimated as follows:

$$\Delta_{\mathsf{T}_{\mathsf{I}}} = P_{\mathsf{CL}} \cdot \mathsf{Z}_{\mathsf{TH}}(\mathsf{t}_{\mathsf{CLAMP}})$$

Where:  $Z_{TH}(t_{CLAMP})$  is the thermal impedance at  $t_{CLAMP}$  and can be read from the thermal impedance curves given in the data sheets.

$$P_{CL} = V_{CL} \cdot I_{CLavg}$$
: Power dissipation during active clamp

$$V_{\scriptscriptstyle CL} = 65 V$$
 : Typical  $V_{\scriptscriptstyle CLAMP}$  value.

$$I_{\text{CLavg}} = \frac{I_{\text{CL}}}{2} : \text{Average current during active clamp}$$

$$t_{\text{CL}} = \frac{I_{\text{CL}}}{\left| \frac{di}{di} \right|}$$
: Active clamp duration

$$\frac{di}{dt} = \frac{V_{Battery} - V_{CL}}{L}$$
: Demagnetization current

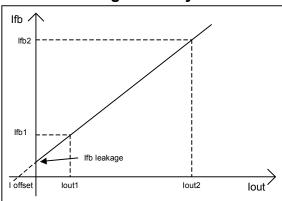
Figure 9 gives the maximum inductance versus the load current in the worst case : the part switch off after an over temperature detection. If the load inductance exceed the curve, a free wheeling diode is required.



#### **Over-current protection**

The threshold of the over-current protection is set in order to guaranteed that the device is able to turn on a load with an inrush current lower than the minimum of lsd. Nevertheless for high current and high temperature the device may switch off for a lower current due to the over-temperature protection. This behavior is shown in Figure 11.

#### **Current sensing accuracy**



The current sensing is specified by measuring 3 points :

- Ifb1 for lout1
- Ifb2 for lout2
- Ifb leakage for lout=0

The parameters in the datasheet are computed with the following formula :

Ratio = (lout2 - lout1)/(lfb2 - lfb1)

I offset = Ifb1 x Ratio - lout1

This allows the designer to evaluate the Ifb for any lout value using :

Ifb = ( lout + I offset ) / Ratio if Ifb > Ifb leakage

For some applications, a calibration is required. In that case, the accuracy of the system will depends on the variation of the I offset and the ratio over the temperature range. The ratio variation is given by Ratio\_TC specified in page 4.

The loffset variation depends directly on the Rdson:

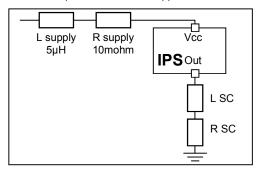
I offset@-40°C= I offset@25°C / 0.8

I offset@150°C= I offset@25°C / 1.9



Maximum Vcc voltage with short circuit protection

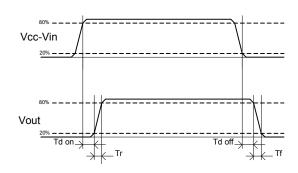
The maximum Vcc voltage with short circuit is the maximum voltage for which the part is able to protect itself under test conditions representative of the application. 2 kind of short circuits are considered: terminal and load short circuit.



	LSC	R SC
Terminal SC	0.1 µH	10 mohm
Load SC	10 μH	100 mohm

### Maximum current during reverse circulation

In case of short circuit to battery, a voltage drop of the Vcc may create a current which circulate in reverse mode. When the device is on, this reverse circulation current will not trigger the internal fault latch. This immunization is also true when the part turns on while a reverse current flows into the device. The maximum current (I rev) is specified in the maximum rating section.



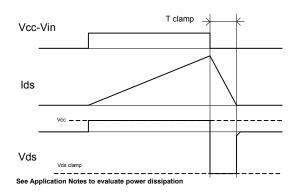


Figure 1 - IN rise time & switching definitions

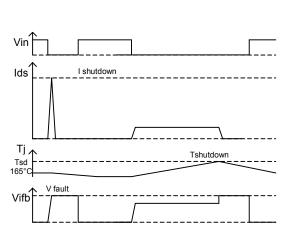


Figure 3 – Protection timing diagram

Figure 2 - Active clamp waveforms

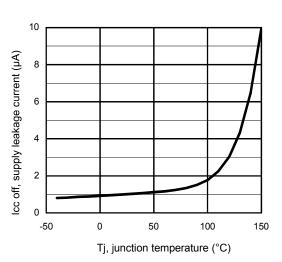
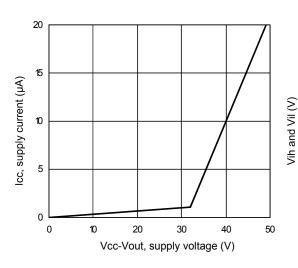


Figure 4 – Icc off (μA) Vs Tj (°C)





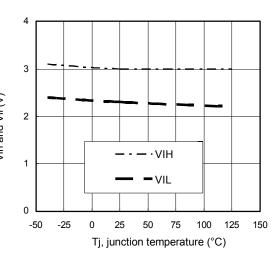
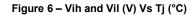
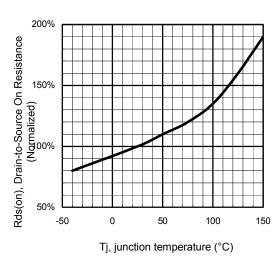


Figure 5 – Icc off (µA) Vs Vcc-Vout (V)







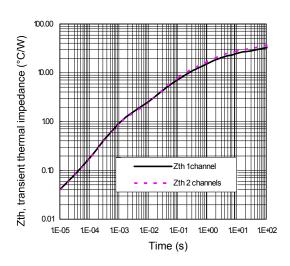
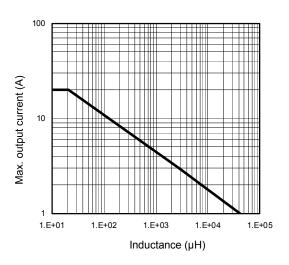


Figure 8 – Transient thermal impedance (°C/W) Vs time (s)





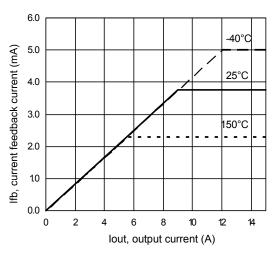


Figure 9 - Max. lout (A) Vs inductance (µH)

Figure 10 - Ifb (mA) Vs lout (A)

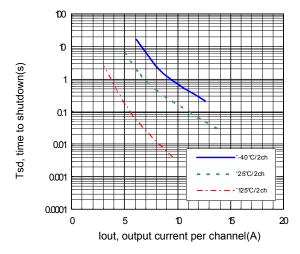
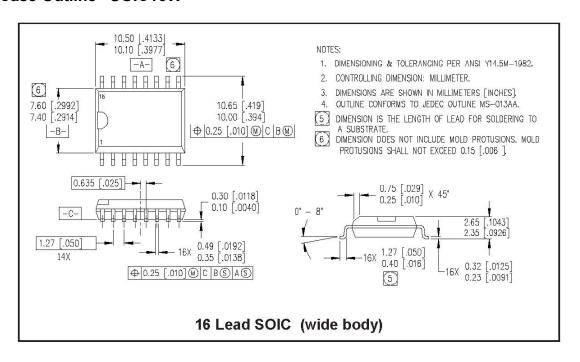


Figure 11 – Tsd (s) Vs I out (A) 2 channels on

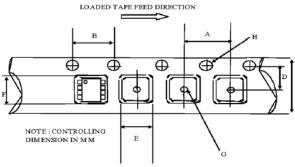


#### Case Outline SOIC16W

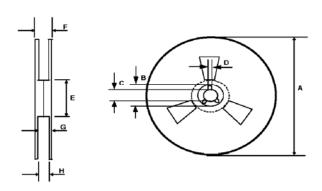




## Tape and Reel - SOIC16W



CARRIER TAPE DIMENSION FOR 16SOICW						
	Me	tric	Imperial			
Code	Min	Max	Min	Max		
Α	11.90	12.10	0.468	0.476		
В	3.90	4.10	0.153	0.161		
С	15.70	16.30	0.618	0.641		
D	7.40	7.60	0.291	0.299		
E	10.80	11.00	0.425	0.433		
F	10.60	10.80	0.417	0.425		
G	1.50	n/a	0.059	n/a		
Н	1.50	1.60	0.059	0.062		



REEL DIMENSIONS FOR 16SOICW						
	M∈	etric	Imp	erial		
Code	Min	Max	Min	Max		
Α	329.60	330.25	12.976	13.001		
В	20.95	21.45	0.824	0.844		
С	12.80	13.20	0.503	0.519		
D	1.95	2.45	0.767	0.096		
E	98.00	102.00	3.858	4.015		
F	n/a	22.40	n/a	0.881		
G	18.50	21.10	0.728	0.830		
Н	16.40	18.40	0.645	0.724		

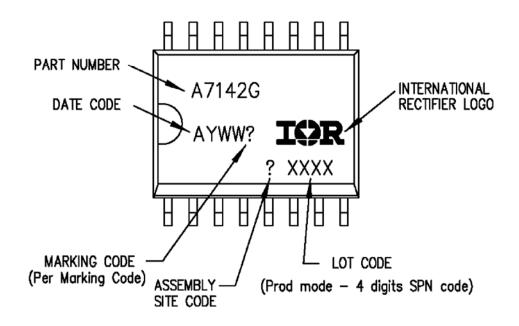
International
TOR Rectifier

## AUIPS7142G



## **Part Marking Information**

TOP MARKING (LASER)



### **Ordering Information**

Base Part Number	Package Type	Standard Pack		
		Form	Quantity	Complete Part Number
AUIPS7142G	SO28W	Tube	45	AUIPS7142G
		Tape and reel	1500	AUIPS7142GTR



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**Revision History** 

Revision	Date	Notes/Changes	
A3	April, 29 <sup>th</sup> 2010	Add tri-temp limits	
A4	March, 17 <sup>th</sup> 2011	Au release	
A5	March,18 <sup>th</sup> 2011	Update lead free and RoHS 1 <sup>st</sup> page	
A6	March, 24 <sup>th</sup> 2011	Add Tape and reel information	

## 单击下面可查看定价,库存,交付和生命周期等信息

## >>Infineon(英飞凌)