

Major Ratings and Characteristics

| Characteristics | 1N7064CCU3 | Units |
|---|------------|------------|
| $I_{F(AV)}$ | 30 | A |
| V_{RRM} (Per Leg) | 45 | V |
| I_{FSM} @ $t_p = 8.3ms$ half-sine (Per Leg) | 85 | A |
| V_F @ 30Apk, $T_J = 125^\circ C$ (Per Leg) | 0.95 | V |
| T_J, T_{STG} Operating and Storage | -65 to 150 | $^\circ C$ |

Description/Features

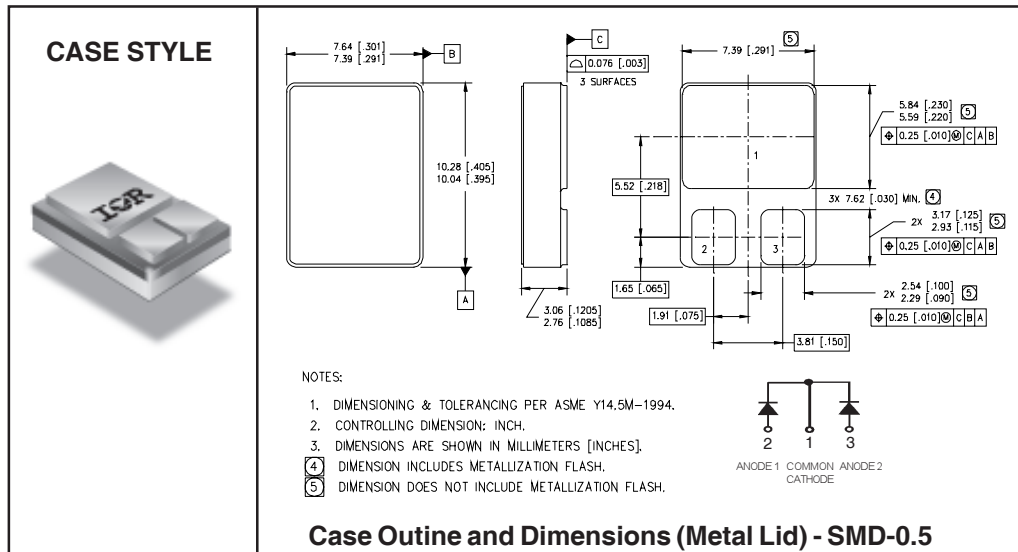
The 1N7064CCU3 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of high reliability environments. It is packaged in the hermetic surface mount SMD-0.5 ceramic package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

Additional Product Summary (Parts with Ceramic Lids) *

| Part Number |
|-------------------|
| 30CLJQ045 |
| JANTX1N7064CCU3C |
| JANTXV1N7064CCU3C |
| JANS1N7064CCU3C |

- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long Reliability
- Surface Mount
- Lightweight
- ESD Rating: Class NS per MIL-STD-750, Method 1020

* Refer to Page 5 for Case Outline and Dimensions of SMD-0.5 with Ceramic Lid



Voltage Ratings

| | |
|--|------------|
| Part number | 1N7064CCU3 |
| V _R Max. DC Reverse Voltage (V) (Per Leg) | 45 |
| V _{RWM} Max. Working Peak Reverse Voltage (V) (Per Leg) | |

Absolute Maximum Ratings

| Parameters | Limits | Units | Conditions |
|---|--------|-------|---|
| I _{F(AV)} Max. Average Forward Current See Fig. 5 | 30 | A | 50% duty cycle @ T _C = 87.6°C, square waveform |
| I _{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg) | 85 | A | @ t _p = 8.3 ms half-sine |

Electrical Specifications

| Parameters | Limits | Units | Conditions |
|---|--------|-------|--|
| V _{FM} Max. Forward Voltage Drop (Per Leg) See Fig. 1 ^① | 0.72 | V | @ 10A |
| | 0.81 | V | @ 15A |
| | 1.09 | V | @ 30A |
| | 0.68 | V | @ 10A |
| | 0.8 | V | @ 15A |
| | 1.09 | V | @ 30A |
| | 0.62 | V | @ 10A |
| | 0.72 | V | @ 15A |
| | 0.95 | V | @ 30A |
| I _{RM} Max. Reverse Leakage Current (Per Leg) See Fig. 2 ^① | 0.08 | mA | T _J = 25°C |
| | 10 | mA | T _J = 125°C |
| C _T Max. Junction Capacitance (Per Leg) | 375 | pF | V _R = 5V _{DC} (1MHz, 25°C) |
| L _S Typical Series Inductance (Per Leg) | 4.8 | nH | Measured from center of cathode pad to center of anode pad |

Thermal-Mechanical Specifications

| Parameters | Limits | Units | Conditions |
|---|------------|-------|-------------------------|
| T _J Max. Junction Temperature Range | -65 to 150 | °C | |
| T _{stg} Max. Storage Temperature Range | -65 to 150 | °C | |
| R _{thJC} Max. Thermal Resistance, Junction to Case (Per Leg) | 3.5 | °C/W | DC operation See Fig. 4 |
| R _{thJC} Max. Thermal Resistance, Junction to Case (Per Package) | 1.75 | °C/W | DC operation |
| wt Weight (Typical) | 1.0 | g | |
| Die Size (Typical) | 70X92 | mils | |
| Case Style | SMD-0.5 | | |

^① Pulse Width < 300μs, Duty Cycle < 2%

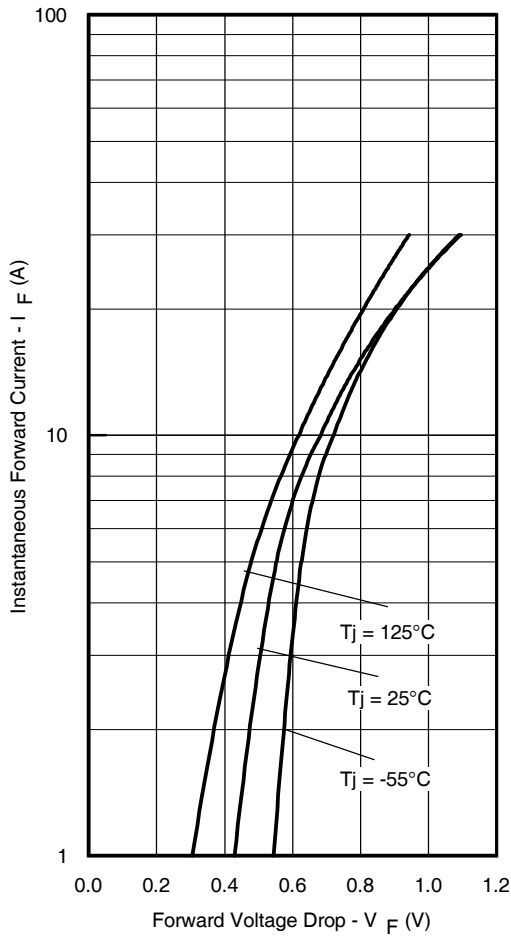


Fig. 1 - Max. Forward Voltage Drop Characteristics (Per Leg)

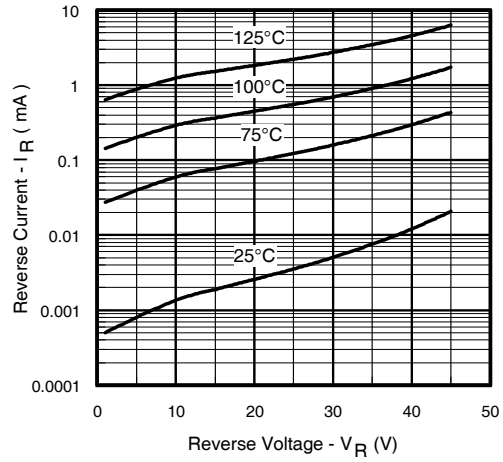


Fig. 2 - Typical Values of Reverse Current Vs. Reverse Voltage (Per Leg)

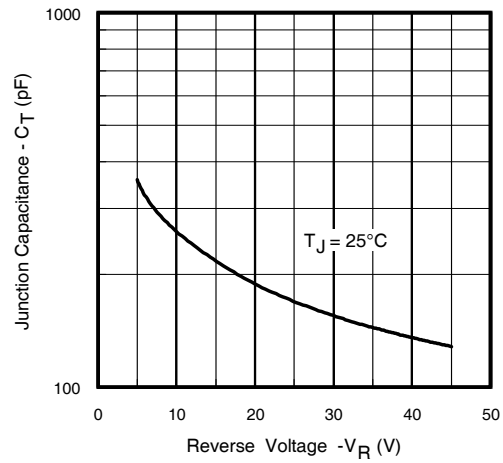


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage (Per Leg)

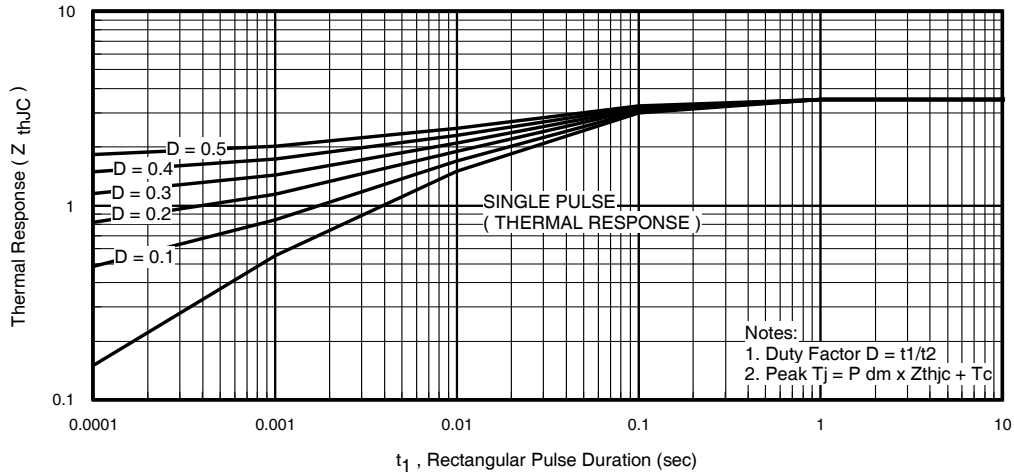


Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)

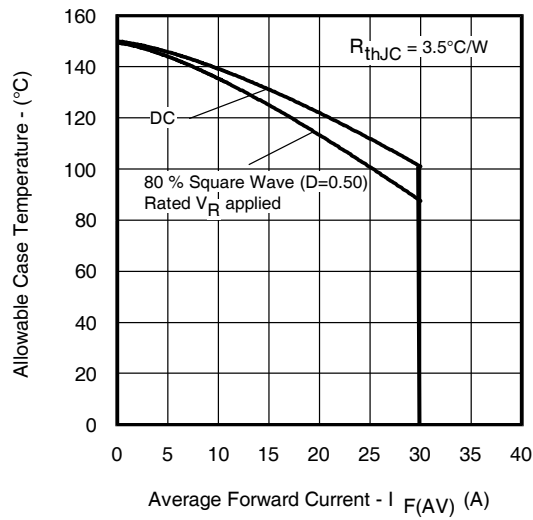
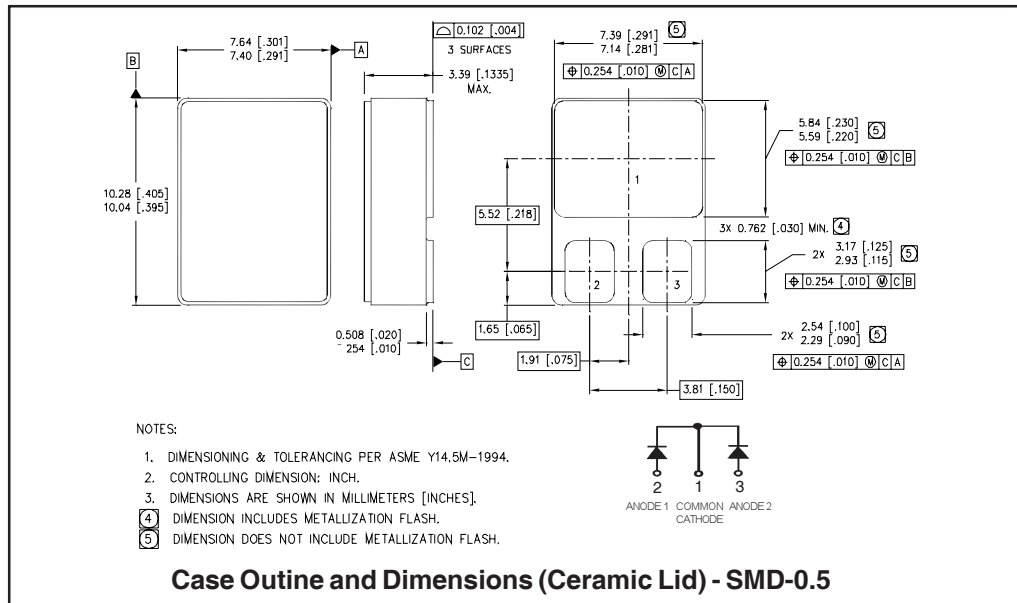


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)



单击下面可查看定价，库存，交付和生命周期等信息

[>>Infineon Technologies\(英飞凌\)](#)