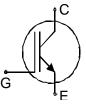


## IGBT Chip in NPT-technology

### FEATURES:

- 600V NPT technology
- 100µm chip
- positive temperature coefficient
- easy paralleling

- This chip is used for:
- IGBT Modules



App	licati	ions:

drives

Chip Type	V <sub>CE</sub>	<b>I</b> Cn	Die Size	Package	Ordering Code
SIGC25T60NC	600V	30A	4.5 x 5.71 mm <sup>2</sup>	sawn on foil	Q67050-A4143- A001

### **MECHANICAL PARAMETER:**

Raster size	4.5 x 5.71 mm			
Area total / active	25.69 / 21.4			
Emitter pad size	2x( 2.18x1.58 )			
Gate pad size	0.68 x 1.08			
Thickness	100	μm		
Wafer size	150	mm		
Flat position	270	deg		
Max.possible chips per wafer	566			
Passivation frontside	Photoimide			
Emitter metallization	3200 nm Al Si 1%			
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solder			
Wire bond	AI, ≤500µm			
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm	Ø 0.65mm ; max 1.2mm		
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C			

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### MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T <sub>j</sub> =25 °C	V <sub>CE</sub>	600	V
DC collector current, limited by T <sub>jmax</sub>	I <sub>C</sub>	1)	А
Pulsed collector current, $t_p$ limited by $T_{jmax}$	I <sub>cpuls</sub>	90	А
Gate emitter voltage	V <sub>GE</sub>	±20	V
Operating junction and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55 +150	°C

1) depending on thermal properties of assembly

**STATIC CHARACTERISTICS** (tested on chip),  $T_j$ =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
		Conditions	min.	typ.	max.	
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE}$ =0V, I <sub>C</sub> =1000µA	600			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =30A	1.7	2.0	2.5	V
Gate-emitter threshold voltage	V <sub>GE(th)</sub>	$I_C$ =700µA, $V_{GE}$ = $V_{CE}$	4.5	5.5	6.5	
Zero gate voltage collector current	I <sub>CES</sub>	V <sub>CE</sub> =600V, V <sub>GE</sub> =0V			2.1	μA
Gate-emitter leakage current	I <sub>GES</sub>	$V_{CE}$ =0V, $V_{GE}$ =20V			120	nA

## DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	Unit
Input capacitance	Ciss	V <sub>CE</sub> =25V	-	1350		pF
Output capacitance	Coss	$V_{GE}=0V$	-	tbd		
Reverse transfer capacitance	Crss	f=1MHz	-	120		

## SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

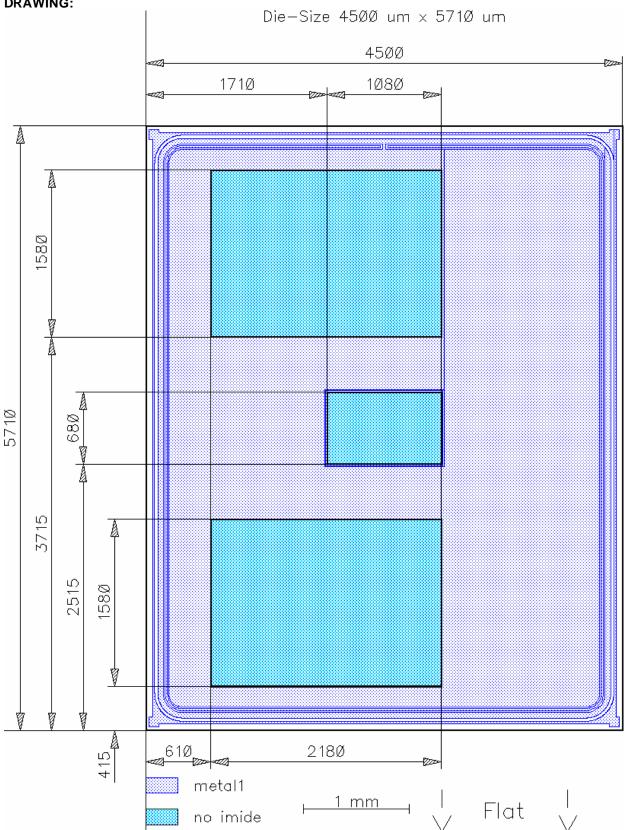
Parameter	Symbol	Conditions <sup>1)</sup>	Value			Unit
			min.	typ.	max.	Onit
Turn-on delay time	t <sub>d(on)</sub>	<i>T</i> <sub>j</sub> =125°C V <sub>CC</sub> =300V	-	21		ns
Rise time	t <sub>r</sub>	I <sub>C</sub> =30A	-	8		
Turn-off delay time	$t_{d(off)}$	$V_{GE}=\pm 15V$ $R_{G}=8.2\Omega$	-	110		
Fall time	t <sub>f</sub>		-	25		

<sup>1)</sup> values also influenced by parasitic L- and C- in measurement and package.

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CHIP DRAWING:



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### FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet

FS 30 R06 XL4

**Description:** 

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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