International
IOR Rectifier

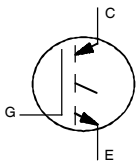
AUTOMOTIVE GRADE

PD - 96340

AUIRG4BC30S-S
AUIRG4BC30S-SL

Standard Speed IGBT

INSULATED GATE BIPOLAR TRANSISTOR



n-channel

V_{CES} = 600V

$V_{CE(on)\ typ.}$ = 1.4V

@V_{GE} = 15V, I_C = 18A

Features

- Standard: optimized for minimum saturation voltage and low operating frequencies (< 1kHz)
- Lead-Free, RoHS Compliant
- Automotive Qualified *

Benefits

- Typical Applications: PTC Heater, Discharge Switch & Relay Replacements

D^2Pak
AUIRG4BC30S-S

TO-262
AUIRG4BC30S-SL

G	C	E
Gate	Collector	Emitter

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied.Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (T_A) is 25°C, unless otherwise specified

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Breakdown Voltage	600	V
I_C @ T_C = 25°C	Continuous Collector Current	34	A
I_C @ T_C = 100°C	Continuous Collector Current	18	
I_{CM}	Pulsed Collector Current ①	68	
I_{LM}	Clamped Inductive Load Current ②	68	
V_{GE}	Gate-to-Emitter Voltage	±20	V
E_{ARV}	Reverse Voltage Avalanche Energy ③	10	mJ
P_D @ T_C = 25°C	Maximum Power Dissipation	100	W
P_D @ T_C = 100°C	Maximum Power Dissipation	42	
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	℃
	Soldering Temperature, for 10 seconds	300 (0.063 in. (1.6mm) from case)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	1.2	°C/W
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50	—	
$R_{\theta JA}$	Junction-to-Ambient, typical socket mount	—	40	
Wt	Weight	1.44	—	g (oz)

* When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994.

Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	V_{GE} = 0V, I_C = 250µA
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	V_{GE} = 0V, I_C = 1.0A
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.75	—	V/°C	V_{GE} = 0V, I_C = 1.0mA
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	—	1.40	1.6	V	I_C = 18A V_{GE} = 15V
		—	1.84	—		I_C = 34A See Fig. 2, 5
		—	1.45	—		I_C = 18A , T_J = 150°C
$V_{GE(th)}$	Gate Threshold Voltage	3.0	—	6.0		V_{CE} = V_{GE}, I_C = 250µA
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-11	—	mV/°C	V_{CE} = V_{GE}, I_C = 250µA
g_{fe}	Forward Transconductance ⑤	6.0	11	—	S	V_{CE} = 100V, I_C = 18A
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	µA	V_{GE} = 0V, V_{CE} = 600V
		—	—	2.0		V_{GE} = 0V, V_{CE} = 10V, T_J = 25°C
		—	—	1000		V_{GE} = 0V, V_{CE} = 600V, T_J = 150°C
I_{GES}	Gate-to-Emitter Leakage Current	—	—	±100	nA	V_{GE} = ±20V

Switching Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	50	75	nC	I_C = 18A
Q_{ge}	Gate - Emitter Charge (turn-on)	—	7.3	11		V_{CC} = 400V See Fig. 8
Q_{gc}	Gate - Collector Charge (turn-on)	—	17	26		V_{GE} = 15V
$t_{d(on)}$	Turn-On Delay Time	—	22	—	ns	
t_r	Rise Time	—	18	—		T_J = 25°C
$t_{d(off)}$	Turn-Off Delay Time	—	540	810		I_C = 18A, V_{CC} = 480V
t_f	Fall Time	—	390	590		V_{GE} = 15V, R_G = 23Ω
E_{on}	Turn-On Switching Loss	—	0.26	—	mJ	Energy losses include "tail"
E_{off}	Turn-Off Switching Loss	—	3.45	—		See Fig. 9, 10, 14
E_{ts}	Total Switching Loss	—	3.71	5.6		
$t_{d(on)}$	Turn-On Delay Time	—	21	—	ns	T_J = 150°C,
t_r	Rise Time	—	19	—		I_C = 18A, V_{CC} = 480V
$t_{d(off)}$	Turn-Off Delay Time	—	790	—		V_{GE} = 15V, R_G = 23Ω
t_f	Fall Time	—	760	—		Energy losses include "tail"
E_{ts}	Total Switching Loss	—	6.55	—	mJ	See Fig. 11, 14
L_E	Internal Emitter Inductance	—	7.5	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	1100	—	pF	V_{GE} = 0V
C_{oes}	Output Capacitance	—	72	—		V_{CC} = 30V See Fig. 7
C_{res}	Reverse Transfer Capacitance	—	13	—		ƒ = 1.0MHz

Notes:

① Repetitive rating; V_{GE} = 20V, pulse width limited by max. junction temperature (See fig. 13b).
② V_{CC} = 80%(V_{CES}), V_{GE} = 20V, L = 10µH, R_G = 23Ω, (See fig. 13a).
③ Repetitive rating; pulse width limited by maximum junction temperature.
④ Pulse width ≤ 80µs; duty factor ≤ 0.1%.
⑤ Pulse width 5.0µs, single shot.

Qualification Information†

Qualification Level		Automotive (per AEC-Q101) ††	
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		D^2 PAK	MSL1 ††† (per IPC/JEDEC J-STD-020)
		TO-262	N/A
ESD	Machine Model	Class M4 (400V) AEC-Q101-002	
	Human Body Model	Class H1C (2000V) AEC-Q101-001	
	Charged Device Model	Class C5 (1000V) AEC-Q101-005	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site: http://www.irf.com

†† Exceptions to AEC-Q101 requirements are noted in the qualification report.

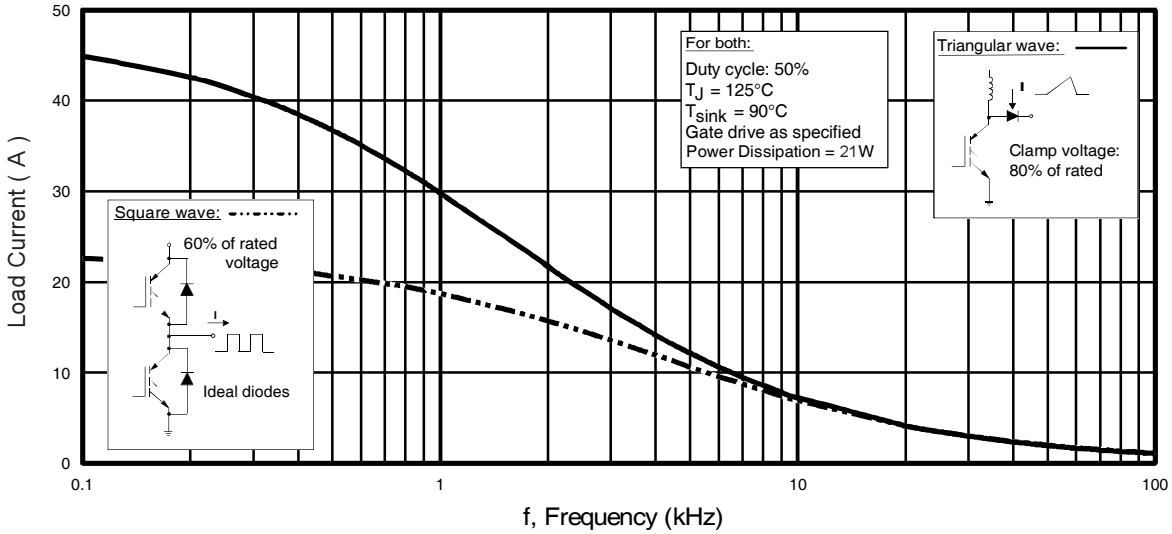


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

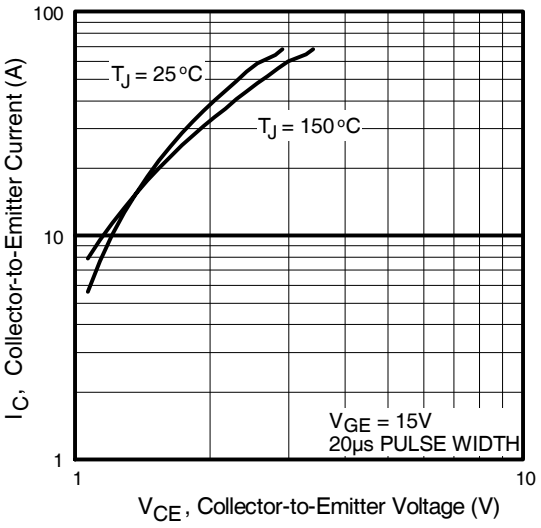


Fig. 2 - Typical Output Characteristics

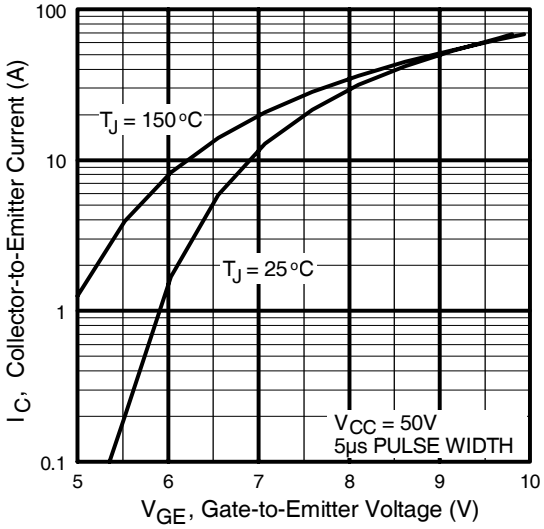


Fig. 3 - Typical Transfer Characteristics

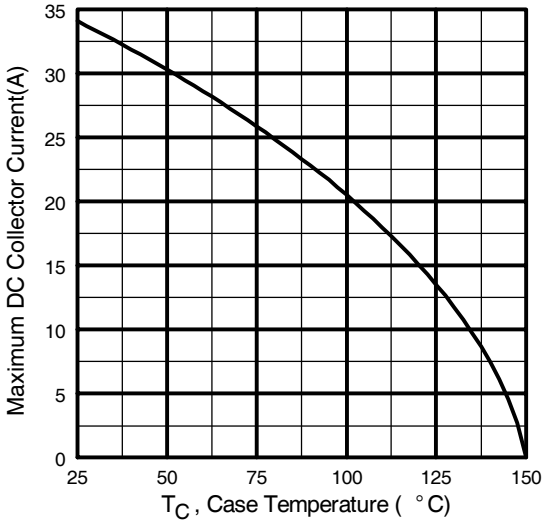


Fig. 4 - Maximum Collector Current vs. Case Temperature

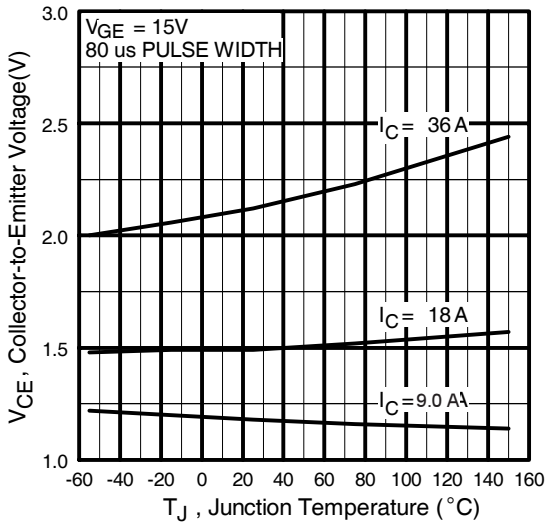


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

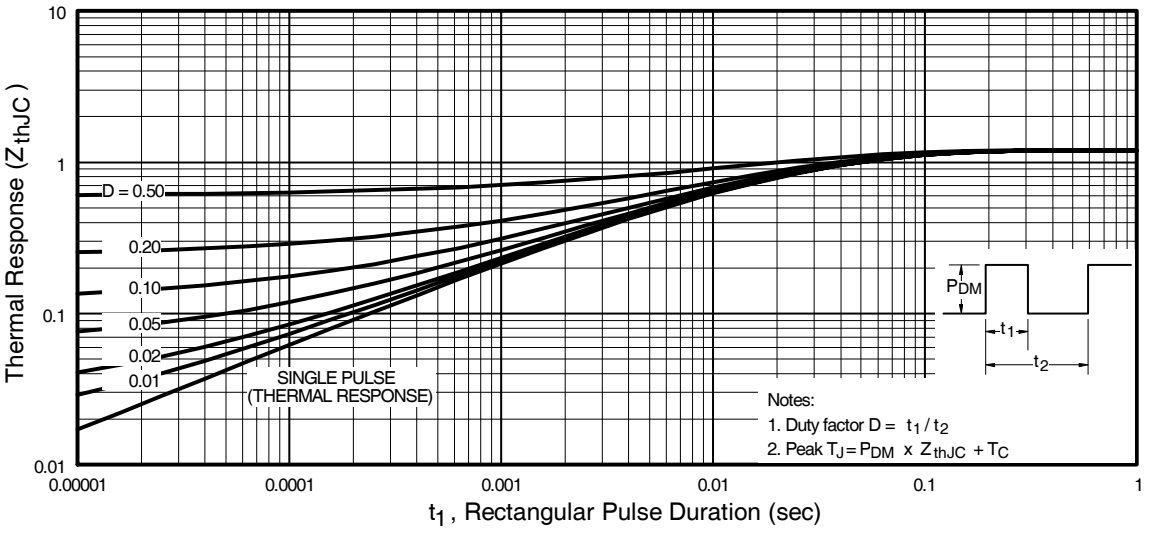


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

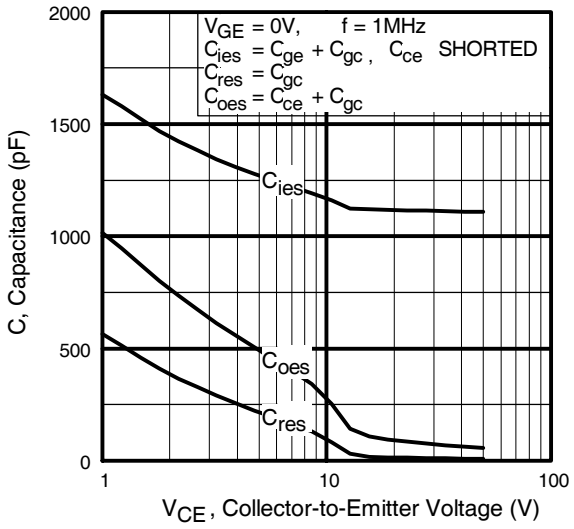


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

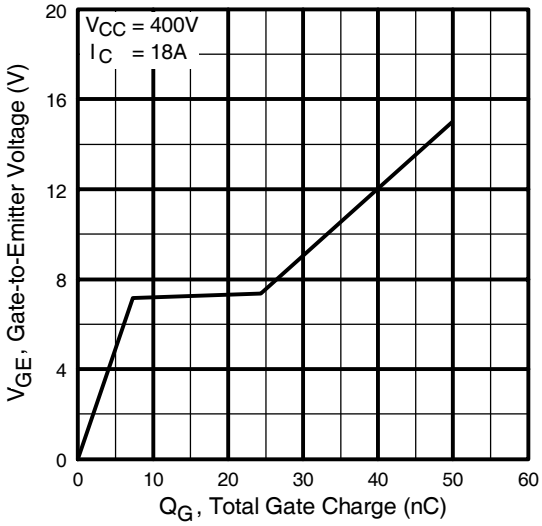


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

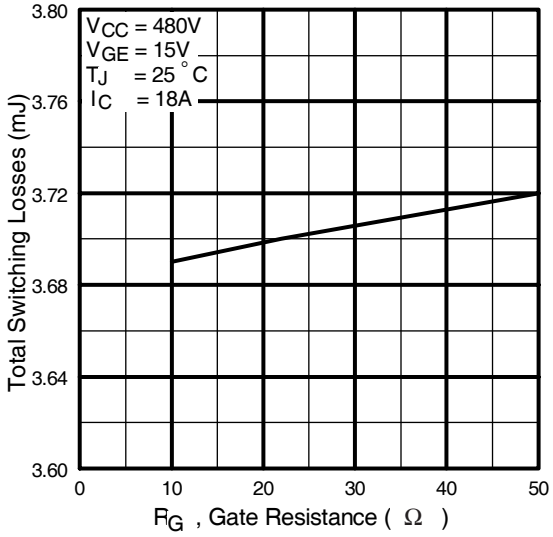


Fig. 9 - Typical Switching Losses vs. Gate Resistance

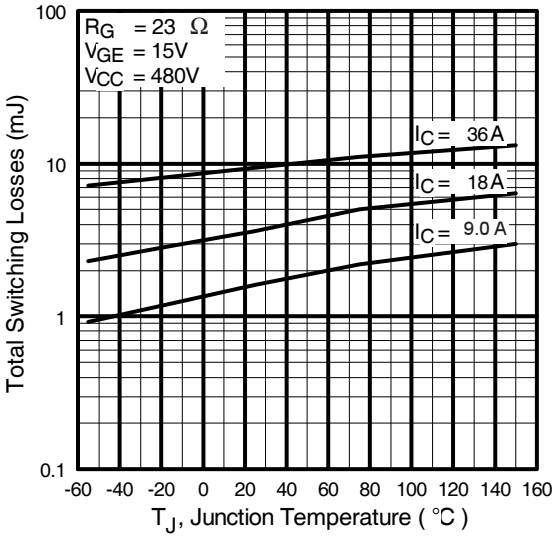


Fig. 10 - Typical Switching Losses vs. Junction Temperature

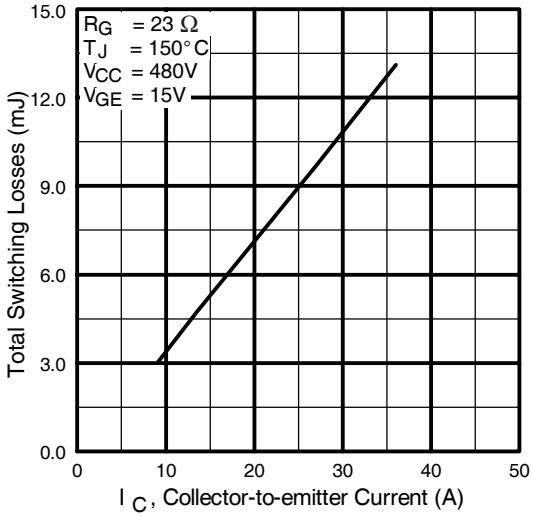


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

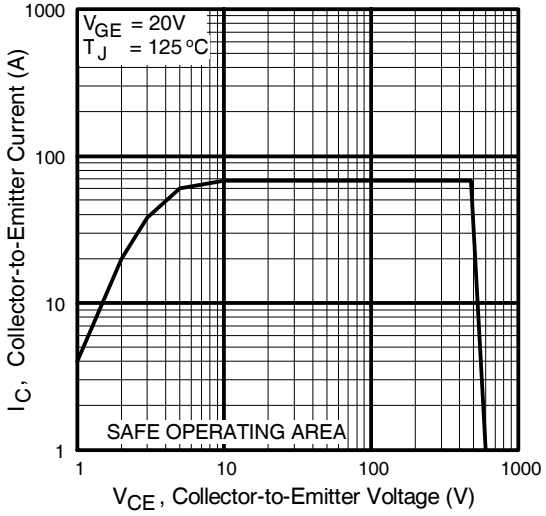
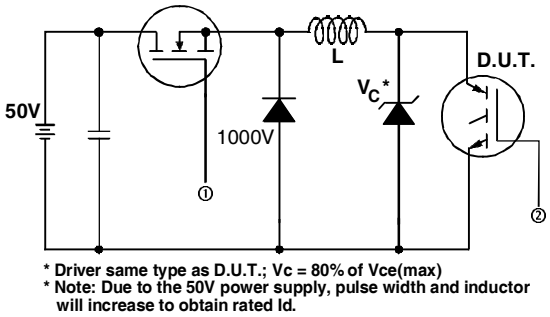
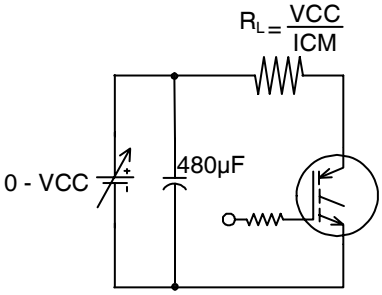


Fig. 12 - Turn-Off SOA



* Driver same type as D.U.T.; Vc = 80% of Vce(max)
* Note: Due to the 50V power supply, pulse width and inductor will increase to obtain rated Id.

Fig. 13a - Clamped Inductive Load Test Circuit



Pulsed Collector Current Test Circuit

Fig. 13b - Pulsed Collector Current Test Circuit

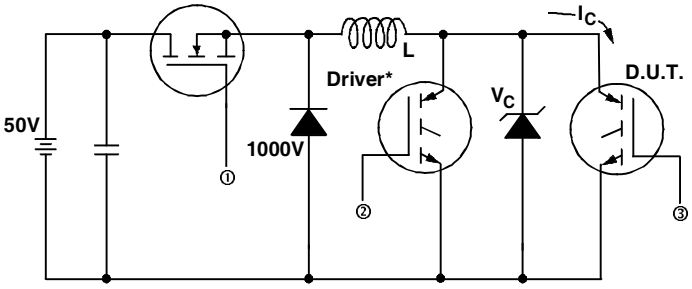


Fig. 14a - Switching Loss Test Circuit

*** Driver same type as D.U.T., VC = 480V**

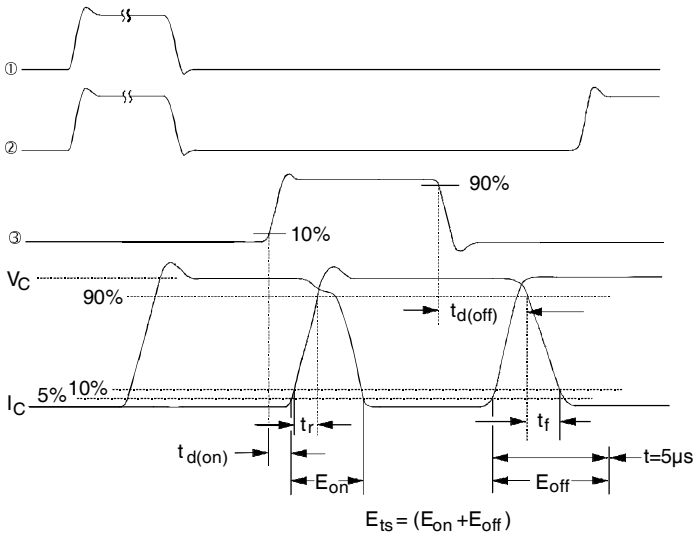
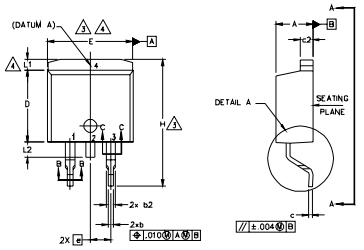
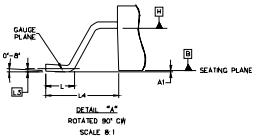


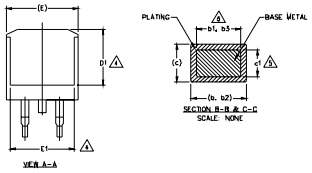
Fig. 14b - Switching Loss Waveforms

D²Pak (TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)







NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M–1994
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
5. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
6. DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
7. CONTROLLING DIMENSION: INCH.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB.

SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	
A1	0.00	0.254	.000	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	5
b2	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	5
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	3
D1	6.86	–	.270		4
E	9.65	10.67	.380	.420	3,4
E1	6.22	–	.245		4
e	2.54 BSC		.100 BSC		
H	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1	–	1.65	–	.066	4
L2	1.27	1.78	–	.070	
L3	0.25 BSC		.010 BSC		
L4	4.78	5.28	.188	.208	

LEAD ASSIGNMENTS

HEXFET

1.– GATE
2, 4.– DRAIN
3.– SOURCE

IGBTs, CoPACK

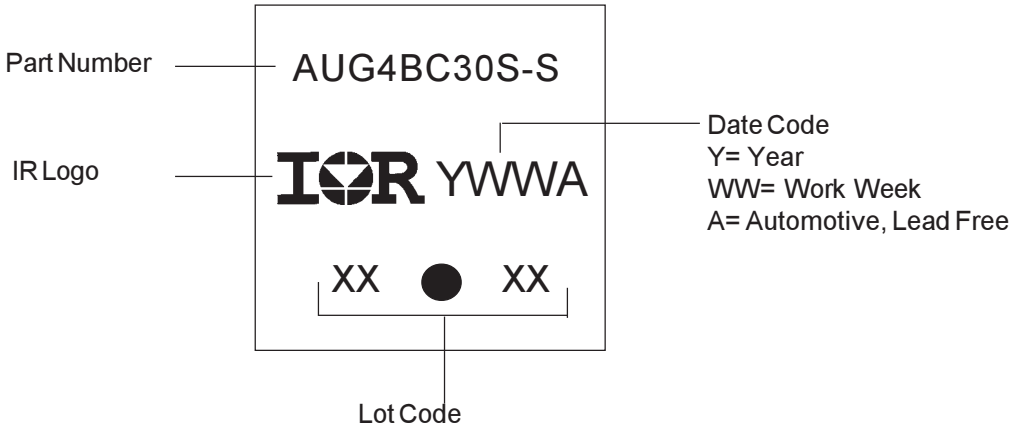
1.– GATE
2, 4.– COLLECTOR
3.– EMITTER

DIODES

1.– ANODE *
2, 4.– CATHODE
3.– ANODE

* PART DEPENDENT.

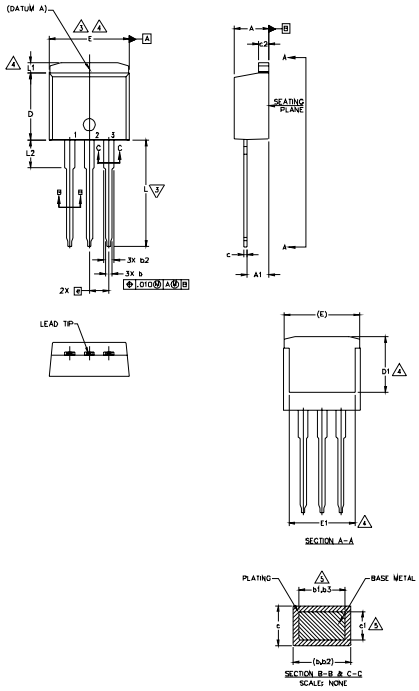
D²Pak (TO-263AB) Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

TO-262 Package Outline

Dimensions are shown in millimeters (inches)



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4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
5. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
6. CONTROLLING DIMENSION: INCH.
7. OUTLINE CONFORM TO JEDEC TO-262 EXCEPT A1(max.), b(min.) AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.

SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	
A1	2.03	3.02	.080	.119	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	5
b2	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	5
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	3
D1	6.86	–	.270	–	4
E	9.65	10.67	.380	.420	3,4
E1	6.22	–	.245		4
e	2.54 BSC		.100 BSC		
L	13.46	14.10	.530	.555	
L1	–	1.65	–	.065	4
L2	3.56	3.71	.140	.146	

LEAD ASSIGNMENTS

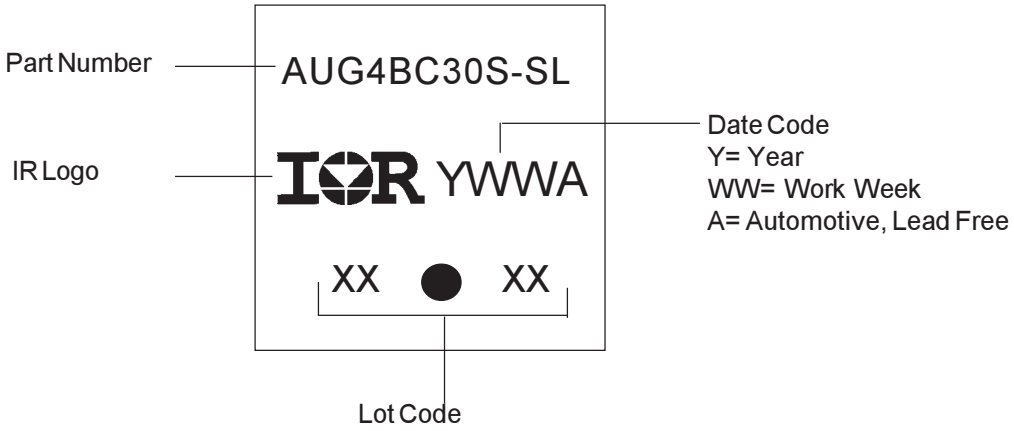
HEXFET

1.– GATE
2.– DRAIN
3.– SOURCE
4.– DRAIN

IGBTs, CoPACK

1.– GATE
2.– COLLECTOR
3.– EMITTER
4.– COLLECTOR

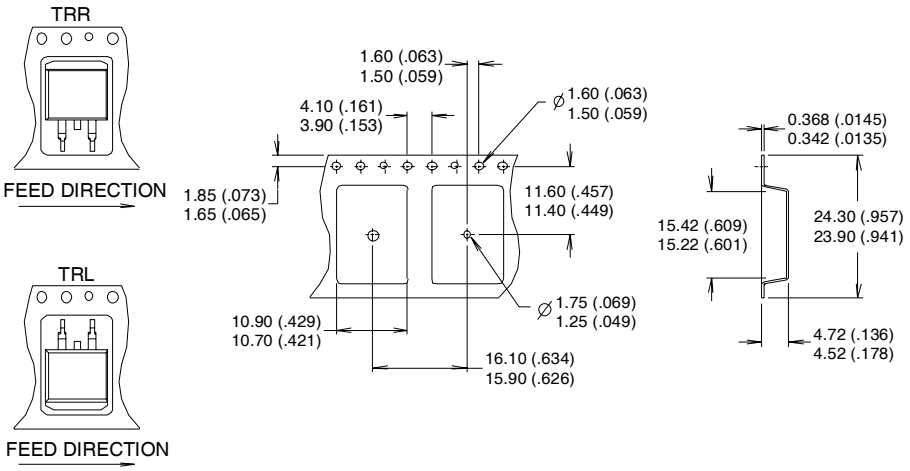
TO-262 Part Marking Information

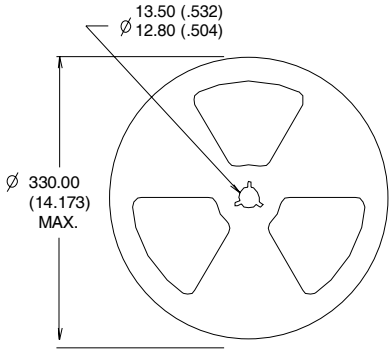


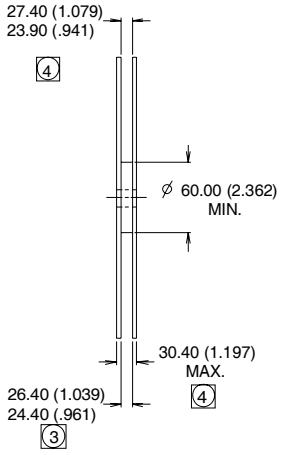
Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

D²Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)







NOTES :
1. COMFORMS TO EIA-418.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION MEASURED @ HUB.
4. INCLUDES FLANGE DISTORTION @ OUTER EDGE.

Ordering Information

Base part number	Package	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRG4BC30S-SL	TO-262	Tube	50	AUIRG4BC30S-SL
AUIRG4BC30S-S	D2Pak	Tube	50	AUIRG4BC30S-S
		Tape and Reel Left	800	AUIRG4BC30SSTRL
		Tape and Reel Right	800	AUIRG4BC30SSTRR

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IR warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with IR's standard warranty. Testing and other quality control techniques are used to the extent IR deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

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