

MOSFET

Metal Oxide Semiconductor Field Effect Transistor

CoolMOS™ C7

650V CoolMOS™ C7 Power Transistor
IPA65R065C7

Data Sheet

Rev. 2.0
Final

Power Management & Multimarket

1 Description

CoolMOS™ is a revolutionary technology for high voltage power MOSFETs, designed according to the superjunction (SJ) principle and pioneered by Infineon Technologies.

CoolMOS™ C7 series combines the experience of the leading SJ MOSFET supplier with high class innovation. The product portfolio provides all benefits of fast switching superjunction MOSFETs offering better efficiency, reduced gate charge, easy implementation and outstanding reliability.

Features

- Increased MOSFET dv/dt ruggedness
- Better efficiency due to best in class FOM $R_{DS(on)} \cdot E_{oss}$ and $R_{DS(on)} \cdot Q_g$
- Best in class $R_{DS(on)}$ /package
- Easy to use/drive
- Pb-free plating, halogen free mold compound
- Qualified for industrial grade applications according to JEDEC (J-STD20 and JESD22)

Benefits

- Enabling higher system efficiency
- Enabling higher frequency / increased power density solutions
- System cost / size savings due to reduced cooling requirements
- Higher system reliability due to lower operating temperatures

Applications

PFC stages and hard switching PWM stages for e.g. Computing, Server, Telecom, UPS and Solar.

Please note: For MOSFET paralleling the use of ferrite beads on the gate or separate totem poles is generally recommended.

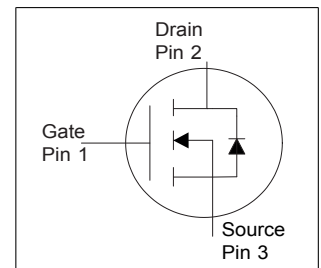


Table 1 Key Performance Parameters

| Parameter | Value | Unit |
|----------------------|-------|------|
| $V_{DS} @ T_{j,max}$ | 700 | V |
| $R_{DS(on),max}$ | 65 | mΩ |
| $Q_{g,typ}$ | 64 | nC |
| $I_{D,pulse}$ | 145 | A |
| $E_{oss@400V}$ | 8 | μJ |
| Body diode di/dt | 60 | A/μs |

| Type / Ordering Code | Package | Marking | Related Links |
|----------------------|-------------------|---------|----------------|
| IPA65R065C7 | PG-TO 220 FullPAK | 65C7065 | see Appendix A |



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2 Maximum ratings

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 2 Maximum ratings

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|--|---------------|--------|------|---------|------------------|---|
| | | Min. | Typ. | Max. | | |
| Continuous drain current ¹⁾ | I_D | - | - | 15 9 | A | $T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$ |
| Pulsed drain current ²⁾ | $I_{D,pulse}$ | - | - | 145 | A | $T_C=25^\circ\text{C}$ |
| Avalanche energy, single pulse | E_{AS} | - | - | 171 | mJ | $I_D=10.2\text{A}$; $V_{DD}=50\text{V}$; see table 10 |
| Avalanche energy, repetitive | E_{AR} | - | - | 0.85 | mJ | $I_D=10.2\text{A}$; $V_{DD}=50\text{V}$; see table 10 |
| Avalanche current, single pulse | I_{AS} | - | - | 10.2 | A | - |
| MOSFET dv/dt ruggedness | dv/dt | - | - | 100 | V/ns | $V_{DS}=0\dots400\text{V}$ |
| Gate source voltage (static) | V_{GS} | -20 | - | 20 | V | static; |
| Gate source voltage (dynamic) | V_{GS} | -30 | - | 30 | V | AC ($f>1\text{ Hz}$) |
| Power dissipation | P_{tot} | - | - | 34 | W | $T_C=25^\circ\text{C}$ |
| Storage temperature | T_{stg} | -55 | - | 150 | $^\circ\text{C}$ | - |
| Operating junction temperature | T_j | -55 | - | 150 | $^\circ\text{C}$ | - |
| Mounting torque | - | - | - | 50 | Ncm | M2.5 screws |
| Continuous diode forward current | I_S | - | - | 15 | A | $T_C=25^\circ\text{C}$ |
| Diode pulse current ²⁾ | $I_{S,pulse}$ | - | - | 145 | A | $T_C=25^\circ\text{C}$ |
| Reverse diode dv/dt ³⁾ | dv/dt | - | - | 1.5 | V/ns | $V_{DS}=0\dots400\text{V}$, $I_{SD}\leq I_S$, $T_j=25^\circ\text{C}$ see table 8 |
| Maximum diode commutation speed | di/dt | - | - | 60 | A/ μs | $V_{DS}=0\dots400\text{V}$, $I_{SD}\leq I_S$, $T_j=25^\circ\text{C}$ see table 8 |
| Insulation withstand voltage | V_{ISO} | - | - | 2500 | V | V_{rms} , $T_C=25^\circ\text{C}$, $t=1\text{min}$ |

¹⁾ Limited by $T_{j,max}$.

²⁾ Pulse width t_p limited by $T_{j,max}$

³⁾ Identical low side and high side switch with identical R_G

3 Thermal characteristics

Table 3 Thermal characteristics

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|--|------------|--------|------|------|------|-------------------------------------|
| | | Min. | Typ. | Max. | | |
| Thermal resistance, junction - case | R_{thJC} | - | - | 3.68 | °C/W | - |
| Thermal resistance, junction - ambient | R_{thJA} | - | - | 80 | °C/W | leaded |
| Thermal resistance, junction - ambient for SMD version | R_{thJA} | - | - | - | °C/W | n.a. |
| Soldering temperature, wavesoldering only allowed at leads | T_{sold} | - | - | 260 | °C | 1.6mm (0.063 in.) from case for 10s |

4 Electrical characteristics

 at $T_j=25^\circ\text{C}$, unless otherwise specified

Table 4 Static characteristics

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|----------------------------------|---------------|--------|----------------|------------|---------------|---|
| | | Min. | Typ. | Max. | | |
| Drain-source breakdown voltage | $V_{(BR)DSS}$ | 650 | - | - | V | $V_{GS}=0\text{V}$, $I_D=1\text{mA}$ |
| Gate threshold voltage | $V_{(GS)th}$ | 3 | 3.5 | 4 | V | $V_{DS}=V_{GS}$, $I_D=0.85\text{mA}$ |
| Zero gate voltage drain current | I_{DSS} | - | - | 1 | μA | $V_{DS}=650$, $V_{GS}=0\text{V}$, $T_j=25^\circ\text{C}$ $V_{DS}=650$, $V_{GS}=0\text{V}$, $T_j=150^\circ\text{C}$ |
| Gate-source leakage current | I_{GSS} | - | - | 100 | nA | $V_{GS}=20\text{V}$, $V_{DS}=0\text{V}$ |
| Drain-source on-state resistance | $R_{DS(on)}$ | - | 0.058 0.138 | 0.065 - | Ω | $V_{GS}=10\text{V}$, $I_D=17.1\text{A}$, $T_j=25^\circ\text{C}$ $V_{GS}=10\text{V}$, $I_D=17.1\text{A}$, $T_j=150^\circ\text{C}$ |
| Gate resistance | R_G | - | 0.85 | - | Ω | $f=1\text{MHz}$, open drain |

Table 5 Dynamic characteristics

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|--|--------------|--------|------|------|------|--|
| | | Min. | Typ. | Max. | | |
| Input capacitance | C_{iss} | - | 3020 | - | pF | $V_{GS}=0\text{V}$, $V_{DS}=400\text{V}$, $f=250\text{kHz}$ |
| Output capacitance | C_{oss} | - | 48 | - | pF | $V_{GS}=0\text{V}$, $V_{DS}=400\text{V}$, $f=250\text{kHz}$ |
| Effective output capacitance, energy related ¹⁾ | $C_{o(er)}$ | - | 100 | - | pF | $V_{GS}=0\text{V}$, $V_{DS}=0\dots400\text{V}$ |
| Effective output capacitance, time related ²⁾ | $C_{o(tr)}$ | - | 1110 | - | pF | $I_D=\text{constant}$, $V_{GS}=0\text{V}$, $V_{DS}=0\dots400\text{V}$ |
| Turn-on delay time | $t_{d(on)}$ | - | 17 | - | ns | $V_{DD}=400\text{V}$, $V_{GS}=13\text{V}$, $I_D=17.1\text{A}$, $R_G=5.3\Omega$; see table 9 |
| Rise time | t_r | - | 14 | - | ns | $V_{DD}=400\text{V}$, $V_{GS}=13\text{V}$, $I_D=17.1\text{A}$, $R_G=5.3\Omega$; see table 9 |
| Turn-off delay time | $t_{d(off)}$ | - | 72 | - | ns | $V_{DD}=400\text{V}$, $V_{GS}=13\text{V}$, $I_D=17.1\text{A}$, $R_G=5.3\Omega$; see table 9 |
| Fall time | t_f | - | 7 | - | ns | $V_{DD}=400\text{V}$, $V_{GS}=13\text{V}$, $I_D=17.1\text{A}$, $R_G=5.3\Omega$; see table 9 |

Table 6 Gate charge characteristics

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|-----------------------|---------------|--------|------|------|------|--|
| | | Min. | Typ. | Max. | | |
| Gate to source charge | Q_{gs} | - | 16 | - | nC | $V_{DD}=400\text{V}$, $I_D=17.1\text{A}$, $V_{GS}=0$ to 10V |
| Gate to drain charge | Q_{gd} | - | 21 | - | nC | $V_{DD}=400\text{V}$, $I_D=17.1\text{A}$, $V_{GS}=0$ to 10V |
| Gate charge total | Q_g | - | 64 | - | nC | $V_{DD}=400\text{V}$, $I_D=17.1\text{A}$, $V_{GS}=0$ to 10V |
| Gate plateau voltage | $V_{plateau}$ | - | 5.4 | - | V | $V_{DD}=400\text{V}$, $I_D=17.1\text{A}$, $V_{GS}=0$ to 10V |

¹⁾ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 400V

²⁾ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 400V

Table 7 Reverse diode characteristics

| Parameter | Symbol | Values | | | Unit | Note / Test Condition |
|-------------------------------|-----------|--------|------|------|---------|---|
| | | Min. | Typ. | Max. | | |
| Diode forward voltage | V_{SD} | - | 0.9 | - | V | $V_{GS}=0V, I_F=17.1A, T_j=25^{\circ}C$ |
| Reverse recovery time | t_{rr} | - | 800 | - | ns | $V_R=400V, I_F=15A, di_F/dt=60A/\mu s$; see table 8 |
| Reverse recovery charge | Q_{rr} | - | 10 | - | μC | $V_R=400V, I_F=15A, di_F/dt=60A/\mu s$; see table 8 |
| Peak reverse recovery current | I_{rrm} | - | 30 | - | A | $V_R=400V, I_F=15A, di_F/dt=60A/\mu s$; see table 8 |

5 Electrical characteristics diagrams

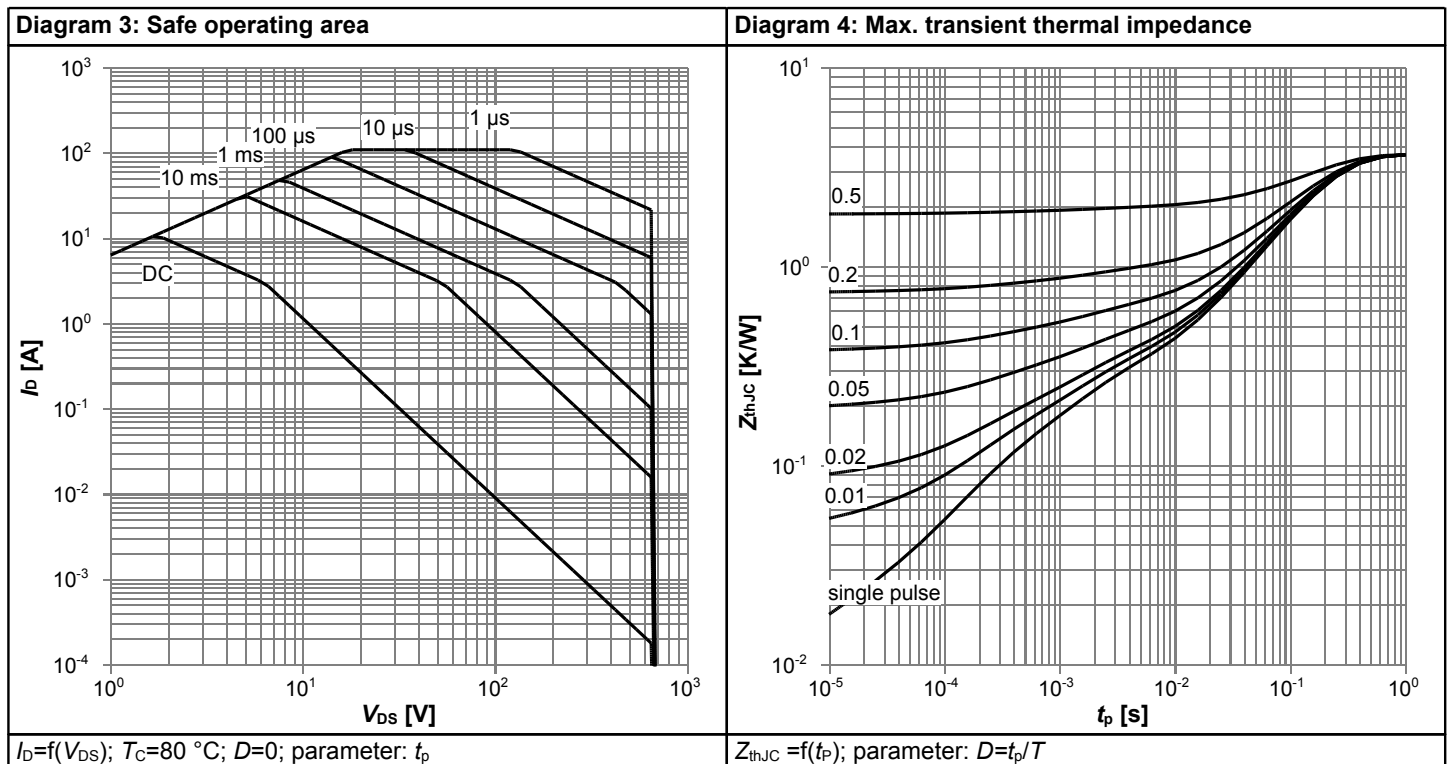
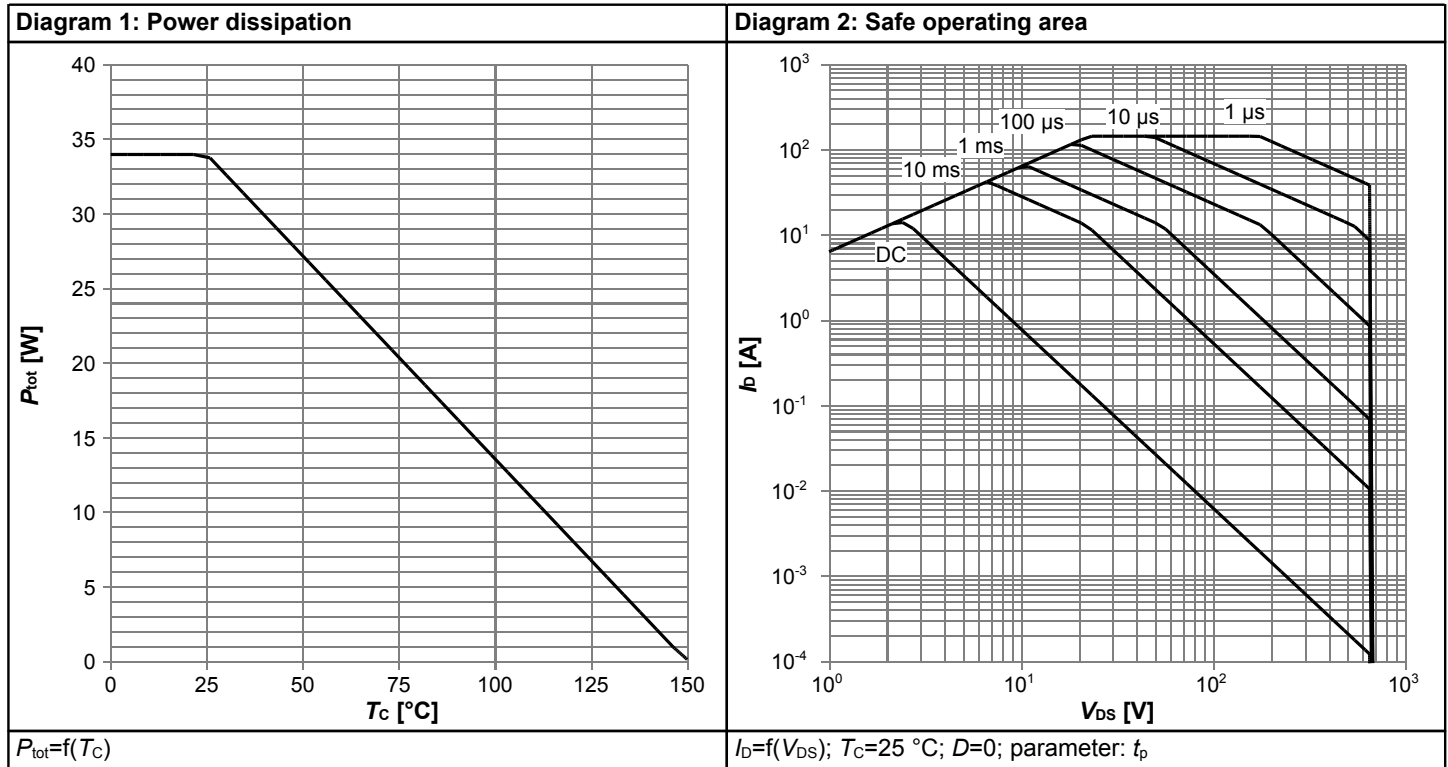
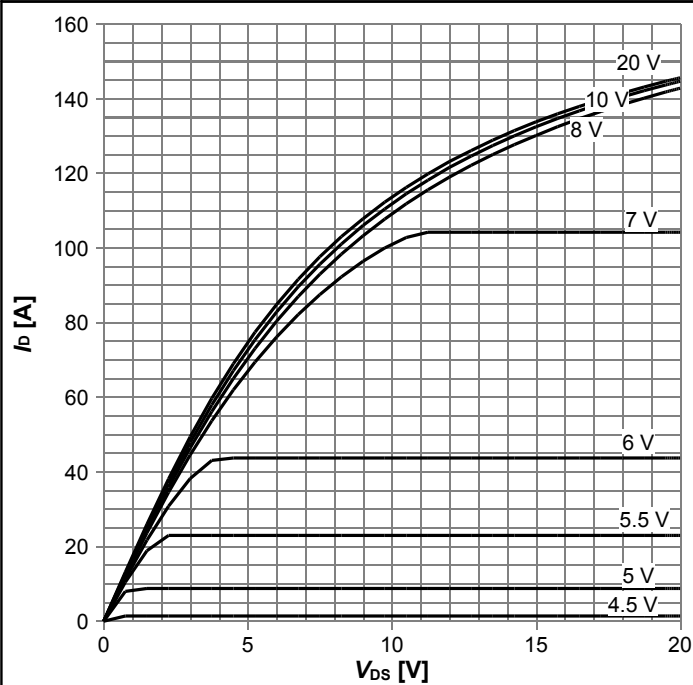
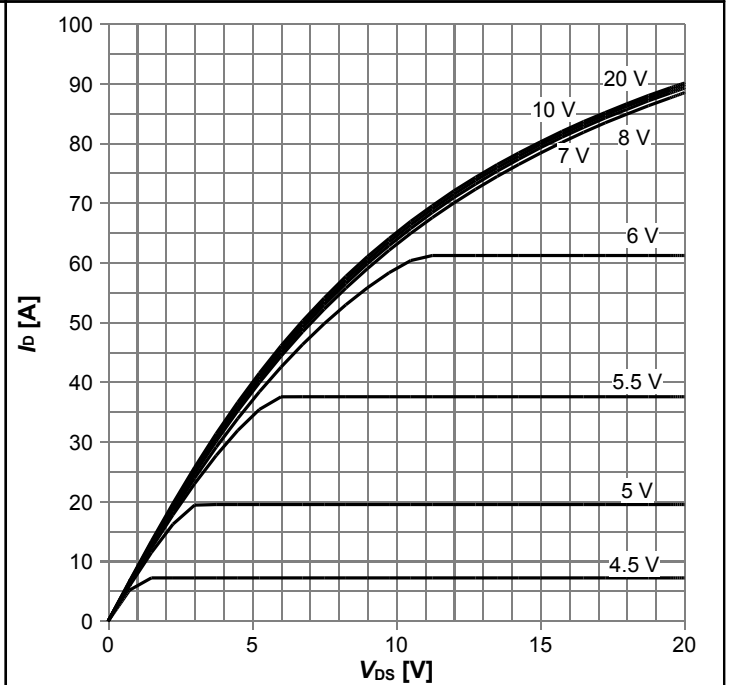


Diagram 5: Typ. output characteristics



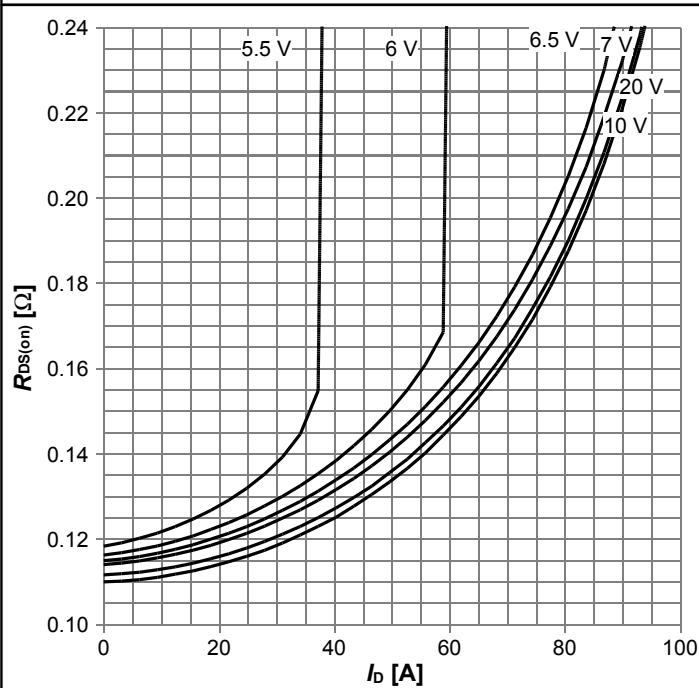
$I_D=f(V_{DS}); T_j=25\text{ }^\circ\text{C};$ parameter: V_{GS}

Diagram 6: Typ. output characteristics



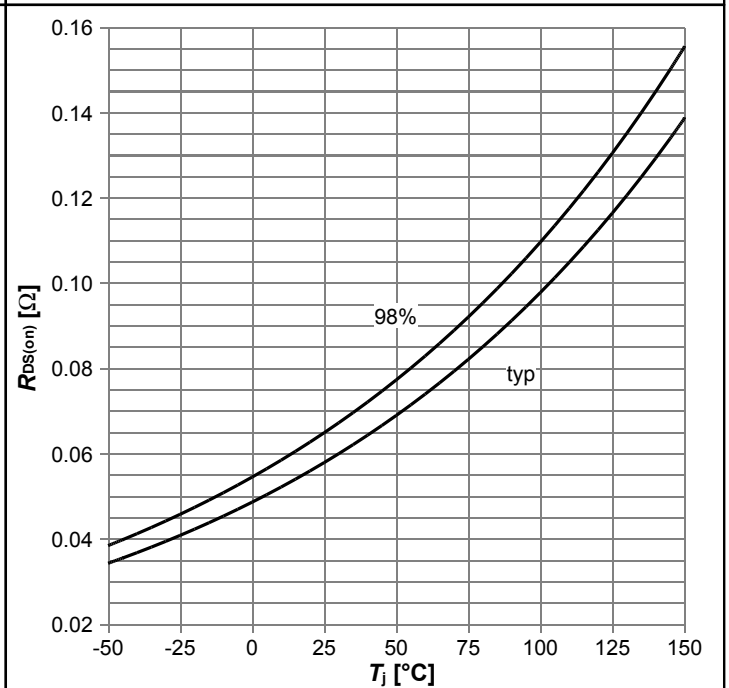
$I_D=f(V_{DS}); T_j=125\text{ }^\circ\text{C};$ parameter: V_{GS}

Diagram 7: Typ. drain-source on-state resistance



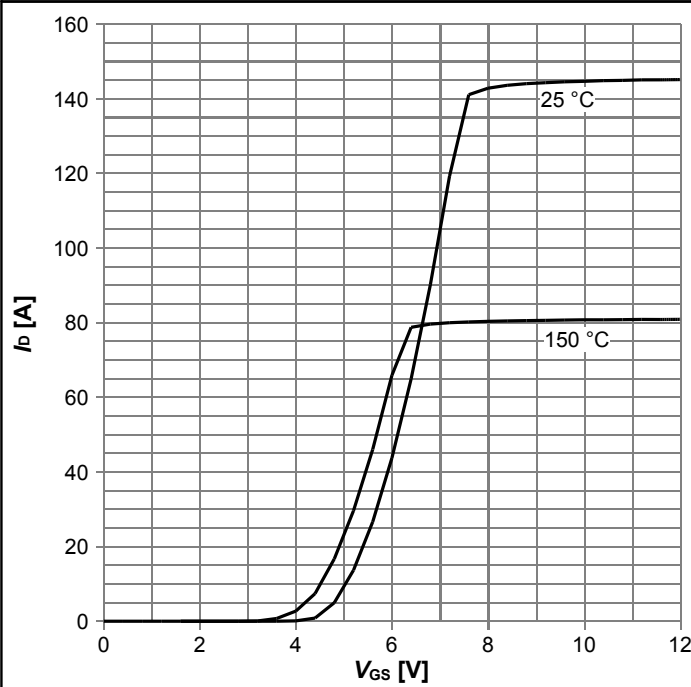
$R_{DS(on)}=f(I_D); T_j=125\text{ }^\circ\text{C};$ parameter: V_{GS}

Diagram 8: Drain-source on-state resistance



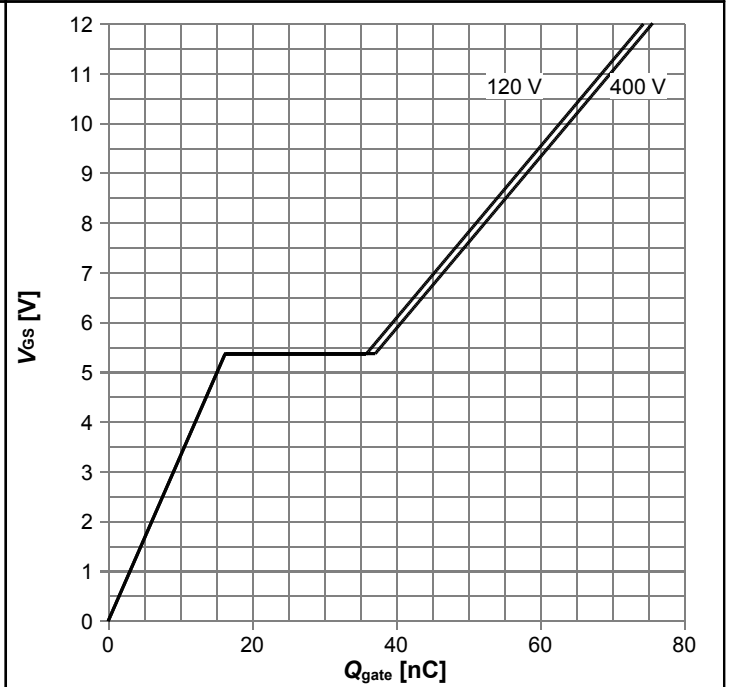
$R_{DS(on)}=f(T_j); I_D=17.1\text{ A}; V_{GS}=10\text{ V}$

Diagram 9: Typ. transfer characteristics



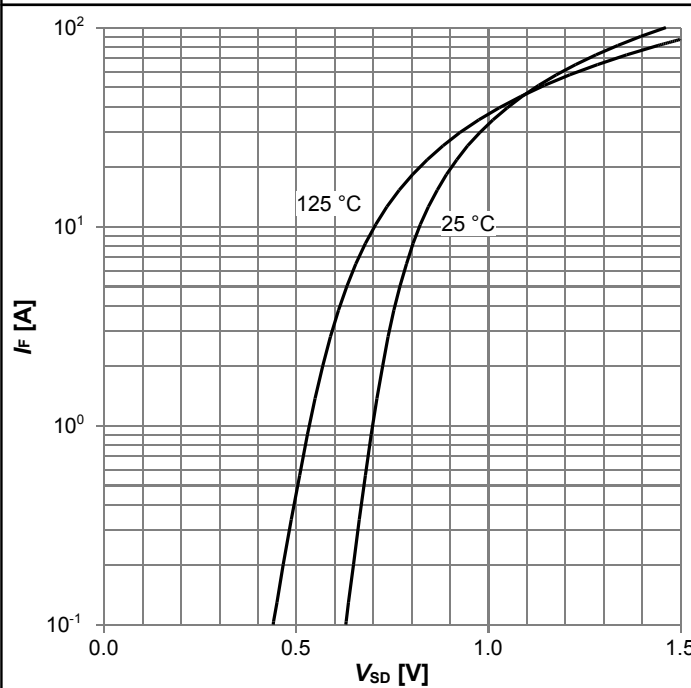
$I_D=f(V_{GS}); V_{DS}=20V; \text{parameter: } T_j$

Diagram 10: Typ. gate charge



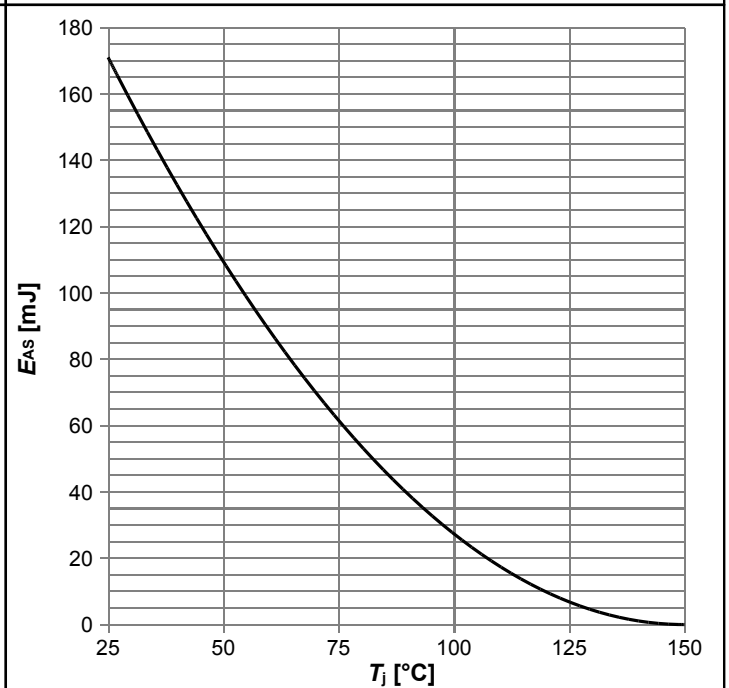
$V_{GS}=f(Q_{gate}); I_D=17.1 \text{ A pulsed}; \text{parameter: } V_{DD}$

Diagram 11: Forward characteristics of reverse diode



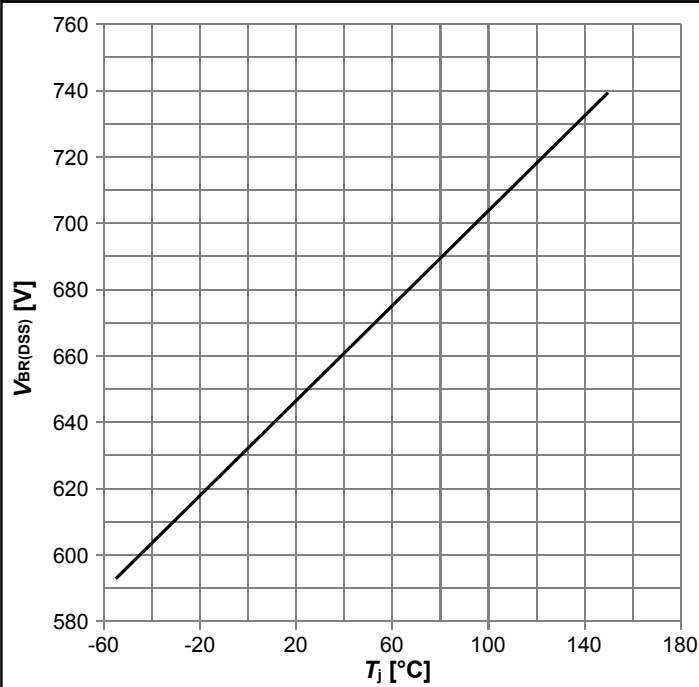
$I_F=f(V_{SD}); \text{parameter: } T_j$

Diagram 12: Avalanche energy



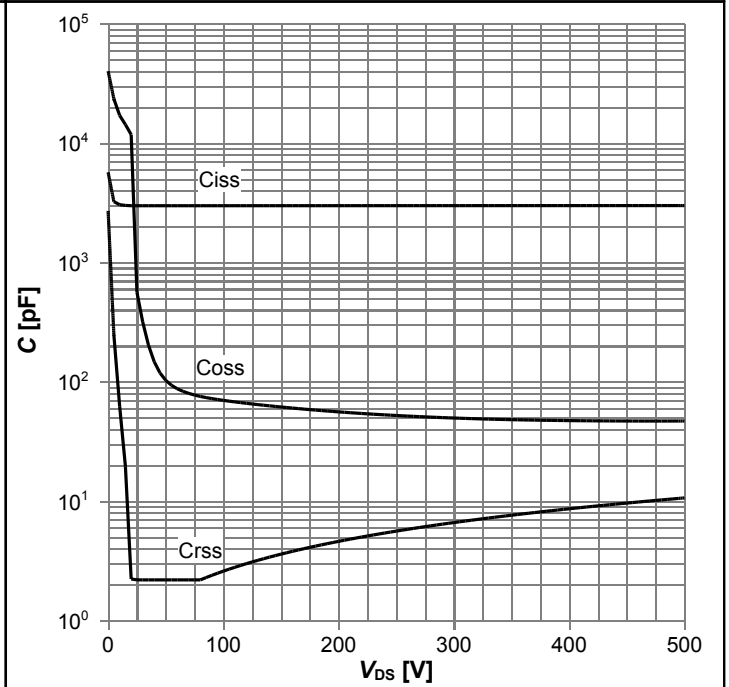
$E_{AS}=f(T_j); I_D=10.2 \text{ A}; V_{DD}=50 \text{ V}$

Diagram 13: Drain-source breakdown voltage



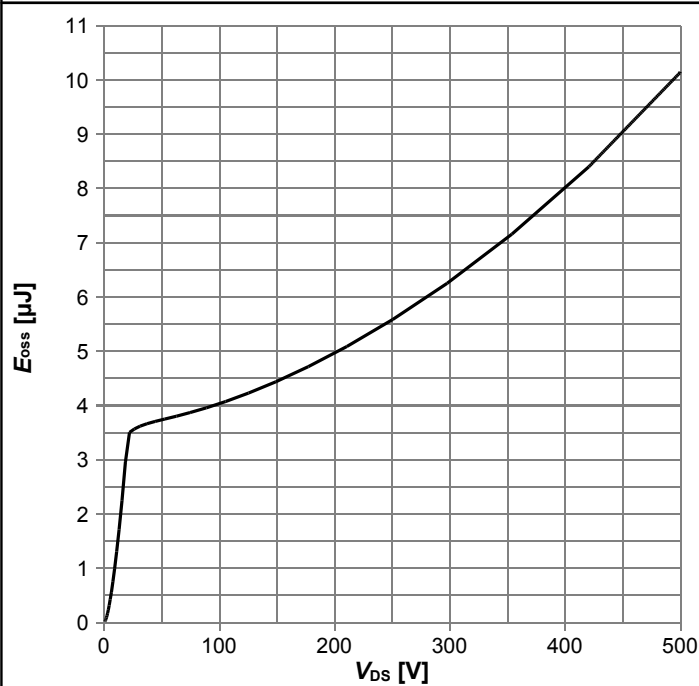
$V_{BR(DSS)}=f(T_j); I_D=1 \text{ mA}$

Diagram 14: Typ. capacitances



$C=f(V_{DS}); V_{GS}=0 \text{ V}; f=250 \text{ kHz}$

Diagram 15: Typ. Coss stored energy



$E_{oss}=f(V_{DS})$

6 Test Circuits

Table 8 Diode characteristics

| Test circuit for diode characteristics | Diode recovery waveform |
|--|---|
| <p>$R_{g1} = R_{g2}$</p> | <p> $t_{rr} = t_F + t_S$ $Q_{rr} = Q_F + Q_S$ </p> |

Table 9 Switching times

| Switching times test circuit for inductive load | Switching times waveform |
|---|--------------------------|
| | |

Table 10 Unclamped inductive load

| Unclamped inductive load test circuit | Unclamped inductive waveform |
|---------------------------------------|------------------------------|
| | |

7 Package Outlines

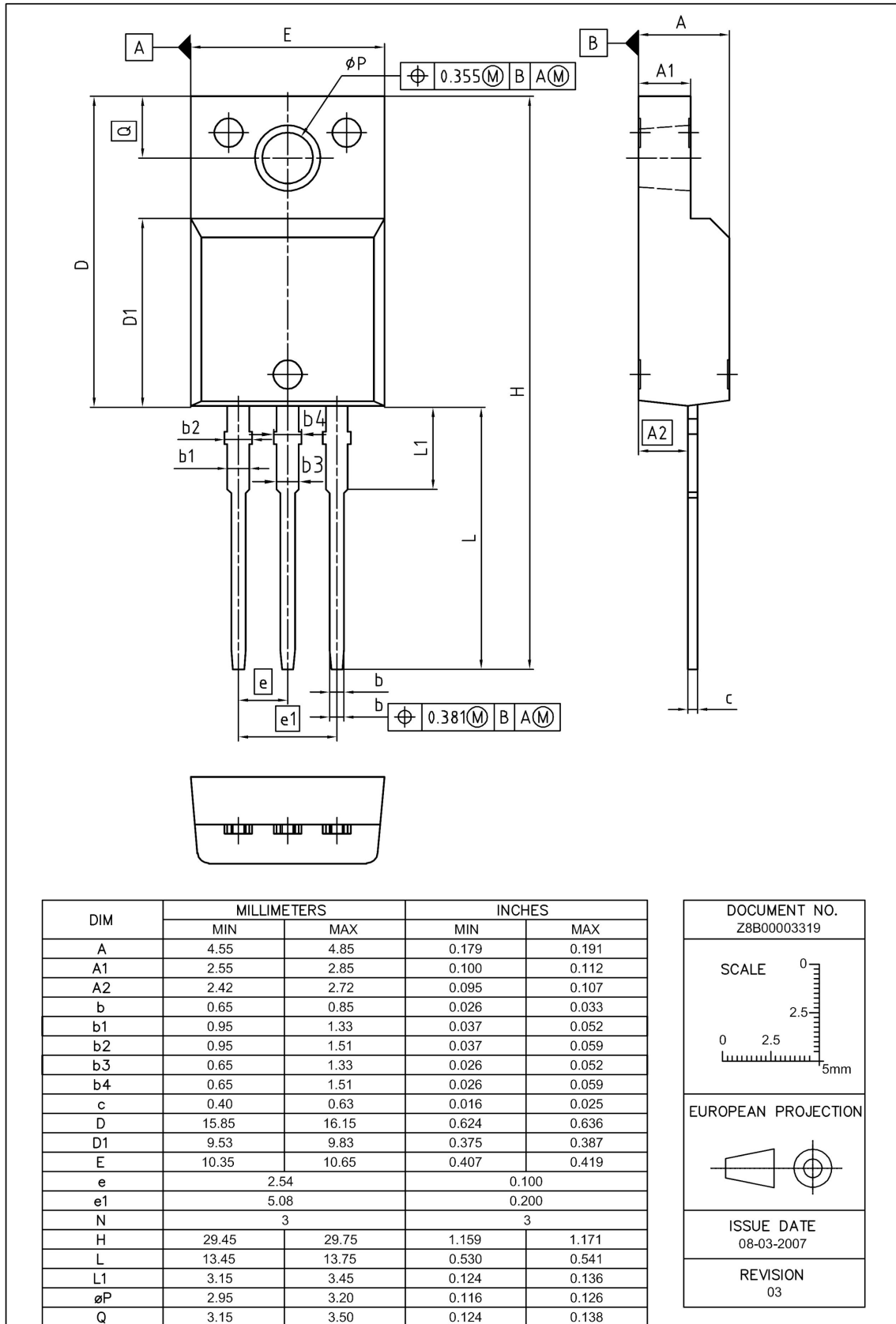


Figure 1 Outline PG-TO 220 FullPAK, dimensions in mm/inches

8 Appendix A

Table 11 Related Links

- IFX CoolMOS™ C7 Webpage: www.infineon.com
- IFX CoolMOS™ C7 application note: www.infineon.com
- IFX CoolMOS™ C7 simulation model: www.infineon.com
- IFX Design tools: www.infineon.com

Revision History

IPA65R065C7

Revision: 2013-10-11, Rev. 2.0

Previous Revision

| Revision | Date | Subjects (major changes since last revision) |
|----------|------------|--|
| 2.0 | 2013-10-11 | Release of final version |

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