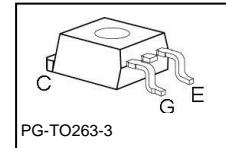
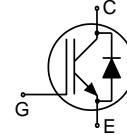


**Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode**


**Features:**

- Very low  $V_{CE(sat)}$  1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5μs
- Designed for frequency inverters for washing machines, fans, pumps and vacuum cleaners
- TRENCHSTOP™ technology for 600V applications offers :
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
- NPT technology offers easy parallel switching capability due to positive temperature coefficient in  $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Qualified according to JEDEC<sup>1</sup> for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	$V_{CE}$	$I_C$	$V_{CE(sat)}, T_j=25^\circ C$	$T_{j,max}$	Marking Code	Package
IKB10N60T	600V	10A	1.5V	175°C	K10T60	PG-T0263-3

**Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_j \geq 25^\circ C$	$V_{CE}$	600	V
DC collector current, limited by $T_{j,max}$	$I_C$	24 18	A
$T_C = 25^\circ C$		24	
$T_C = 100^\circ C$		18	
Pulsed collector current, $t_p$ limited by $T_{j,max}$	$I_{CPuls}$	30	
Turn off safe operating area, $V_{CE} = 600V$ , $T_j = 175^\circ C$ , $t_p = 1\mu s$	-	30	
Diode forward current, limited by $T_{j,max}$	$I_F$	24 18	A
$T_C = 25^\circ C$		24	
$T_C = 100^\circ C$		18	
Diode pulsed current, $t_p$ limited by $T_{j,max}$	$I_{FPuls}$	30	
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time <sup>2)</sup> $V_{GE} = 15V$ , $V_{CC} \leq 400V$ , $T_j \leq 150^\circ C$	$t_{SC}$	5	$\mu s$
Power dissipation $T_C = 25^\circ C$	$P_{tot}$	110	W
Operating junction temperature	$T_j$	-40...+175	
Storage temperature	$T_{stg}$	-55...+150	$^\circ C$
Soldering temperature (reflow soldering, MSL1)		260	

<sup>1</sup> J-STD-020 and JESD-022

<sup>2)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value		Unit
<b>Characteristic</b>					
IGBT thermal resistance, junction – case	$R_{thJC}$		1.35		K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		1.9		
Thermal resistance, junction – ambient	$R_{thJA}$	Footprint 6cm <sup>2</sup> Cu	65 40		

**Electrical Characteristic, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=10\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.5	2.05	
Diode forward voltage	$V_F$	$V_{GE}=0\text{V}, I_F=10\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.6	2.0	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=0.3\text{mA}, V_{CE}=V_{GE}$	4.1	4.6	5.7	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	-	40	$\mu\text{A}$
-			-	-	1000	
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE}=20\text{V}, I_C=10\text{A}$	-	6	-	S
Integrated gate resistor	$R_{Gint}$		none			$\Omega$

**Dynamic Characteristic**

Input capacitance	$C_{iss}$	$V_{CE}=25\text{V},$	-	551	-	pF
Output capacitance	$C_{oss}$	$V_{GE}=0\text{V},$	-	40	-	
Reverse transfer capacitance	$C_{rss}$	$f=1\text{MHz}$	-	17	-	
Gate charge	$Q_{Gate}$	$V_{CC}=480\text{V}, I_C=10\text{A}$ $V_{GE}=15\text{V}$	-	62	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	7	-	nH
Short circuit collector current <sup>1)</sup>	$I_{C(SC)}$	$V_{GE}=15\text{V}, t_{SC}\leq 5\mu\text{s}$ $V_{CC} = 400\text{V},$ $T_j = 25^\circ\text{C}$	-	100	-	A

<sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Switching Characteristic, Inductive Load, at  $T_j=25^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=10\text{A}$ , $V_{GE}=0/15\text{V}$ , $r_G=23\Omega$ , $L_\sigma=60\text{nH}$ , $C_\sigma=40\text{pF}$ $L_\sigma$ , $C_\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	12	-	ns
Rise time	$t_r$		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	215	-	
Fall time	$t_f$		-	38	-	
Turn-on energy	$E_{on}$		-	0.16	-	mJ
Turn-off energy	$E_{off}$		-	0.27	-	
Total switching energy	$E_{ts}$		-	0.43	-	

**Anti-Parallel Diode Characteristic**

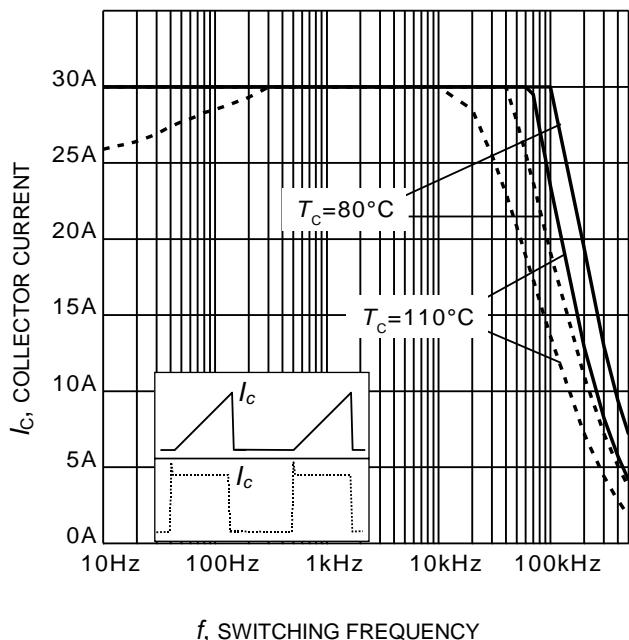
Diode reverse recovery time	$t_{rr}$	$T_j=25^\circ\text{C}$ , $V_R=400\text{V}$ , $I_F=10\text{A}$ , $di_F/dt=880\text{A}/\mu\text{s}$	-	115	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.38	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	10	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	680	-	$\text{A}/\mu\text{s}$

**Switching Characteristic, Inductive Load, at  $T_j=175^\circ\text{C}$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic</b>						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$ , $V_{CC}=400\text{V}$ , $I_C=10\text{A}$ , $V_{GE}=0/15\text{V}$ , $r_G=23\Omega$ , $L_\sigma=60\text{nH}$ , $C_\sigma=40\text{pF}$ $L_\sigma$ , $C_\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	10	-	ns
Rise time	$t_r$		-	11	-	
Turn-off delay time	$t_{d(off)}$		-	233	-	
Fall time	$t_f$		-	63	-	
Turn-on energy	$E_{on}$		-	0.26	-	mJ
Turn-off energy	$E_{off}$		-	0.35	-	
Total switching energy	$E_{ts}$		-	0.61	-	

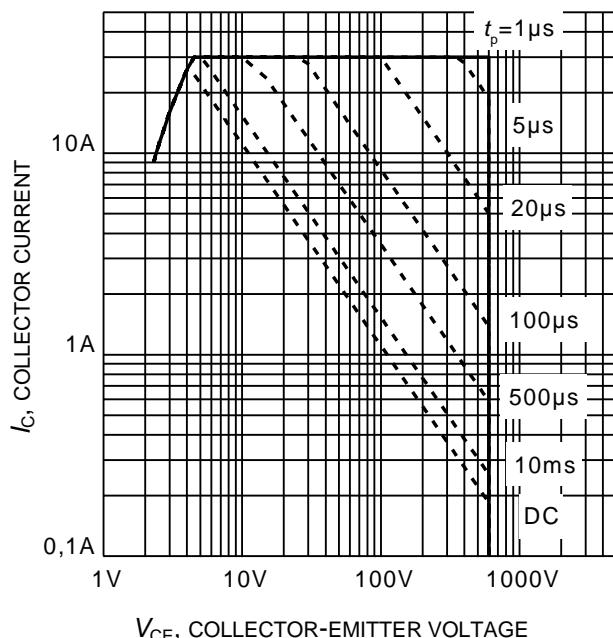
**Anti-Parallel Diode Characteristic**

Diode reverse recovery time	$t_{rr}$	$T_j=175^\circ\text{C}$ , $V_R=400\text{V}$ , $I_F=10\text{A}$ , $di_F/dt=880\text{A}/\mu\text{s}$	-	200	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.92	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	13	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	390	-	$\text{A}/\mu\text{s}$



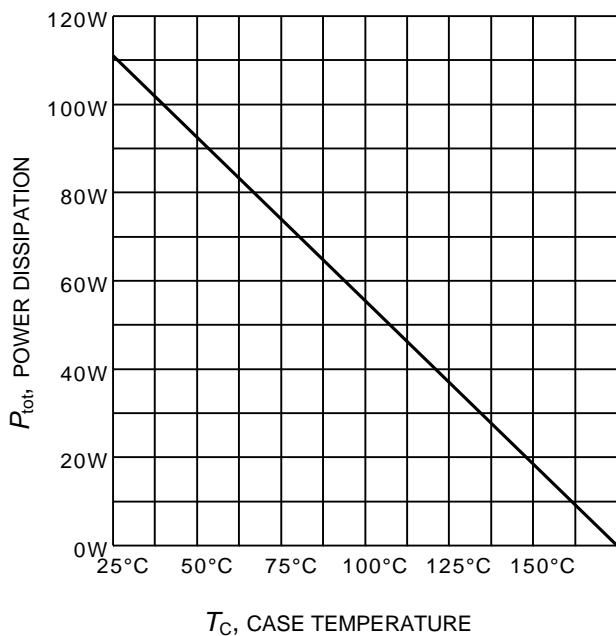
**Figure 1. Collector current as a function of switching frequency**

( $T_j \leq 175^\circ\text{C}$ ,  $D = 0.5$ ,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $r_G = 23\Omega$ )



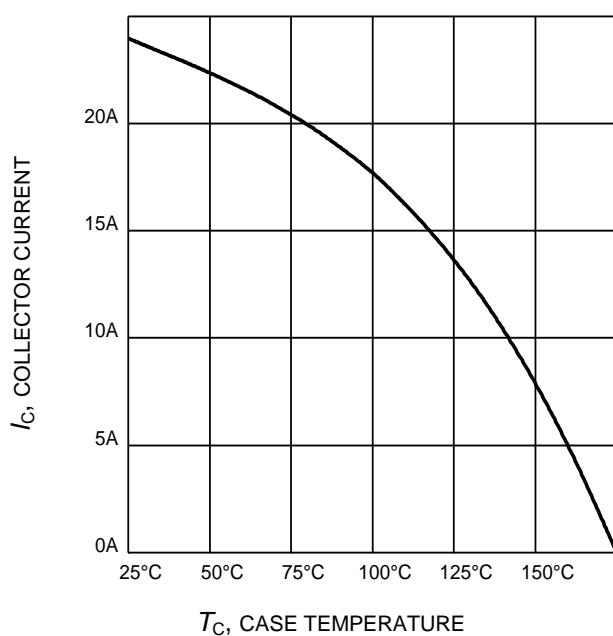
**Figure 2. Safe operating area**

( $D = 0$ ,  $T_C = 25^\circ\text{C}$ ,  $T_j \leq 175^\circ\text{C}$ ;  
 $V_{GE}=0/15\text{V}$ )



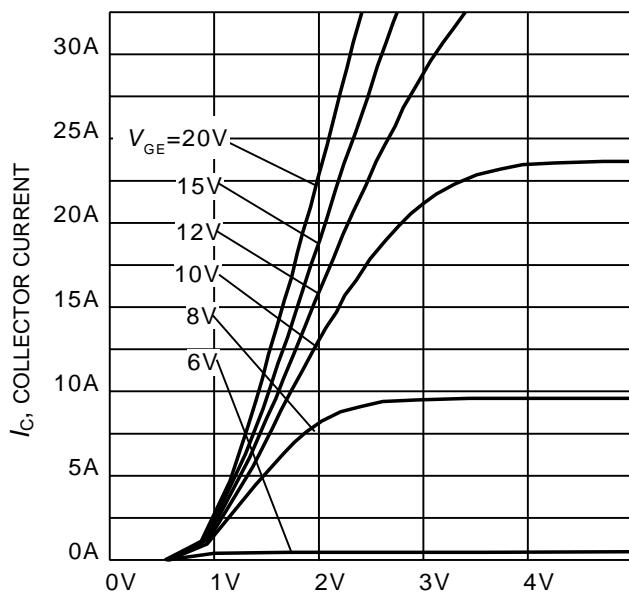
**Figure 3. Power dissipation as a function of case temperature**

( $T_j \leq 175^\circ\text{C}$ )



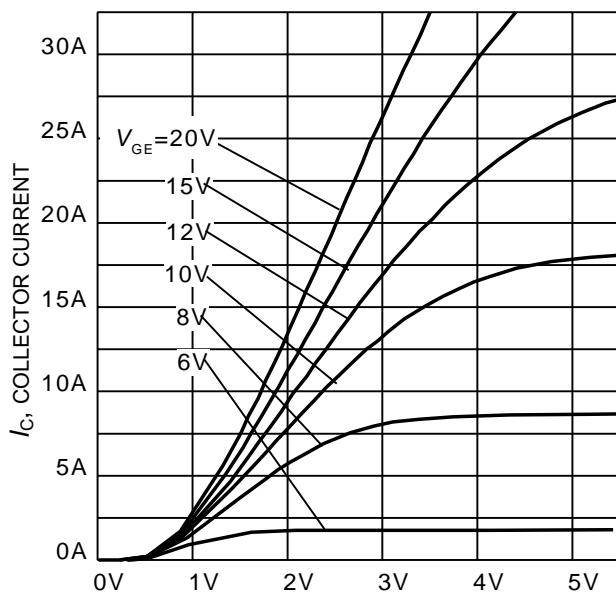
**Figure 4. Collector current as a function of case temperature**

( $V_{GE} \geq 15\text{V}$ ,  $T_j \leq 175^\circ\text{C}$ )



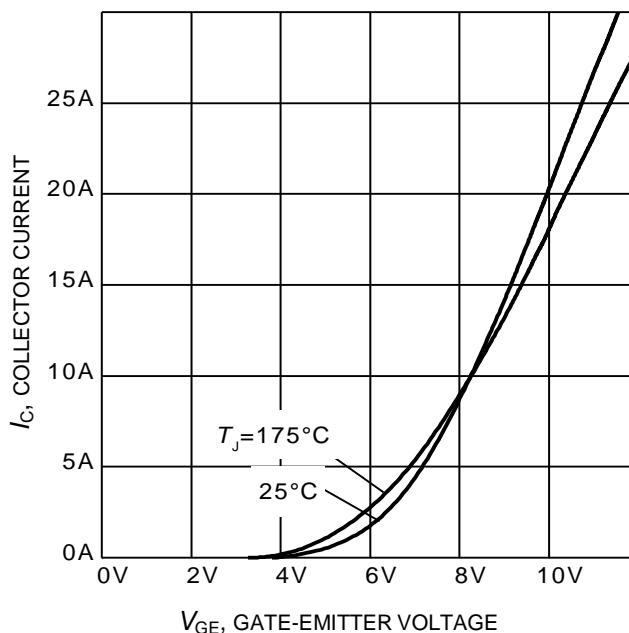
$V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 5. Typical output characteristic**  
( $T_j = 25^\circ\text{C}$ )



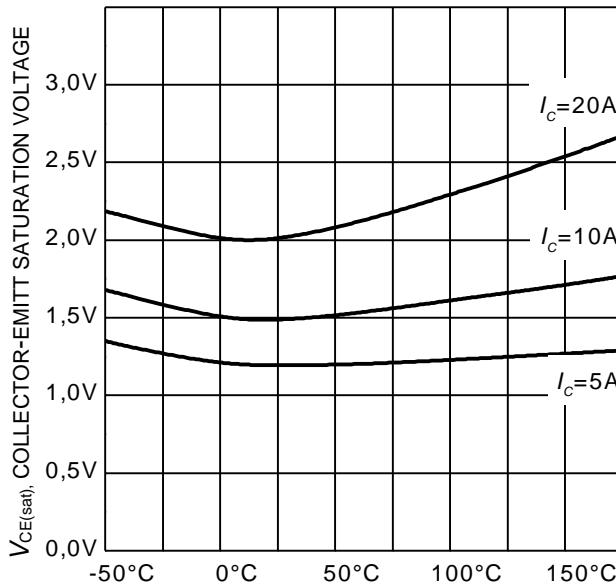
$V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 6. Typical output characteristic**  
( $T_j = 175^\circ\text{C}$ )



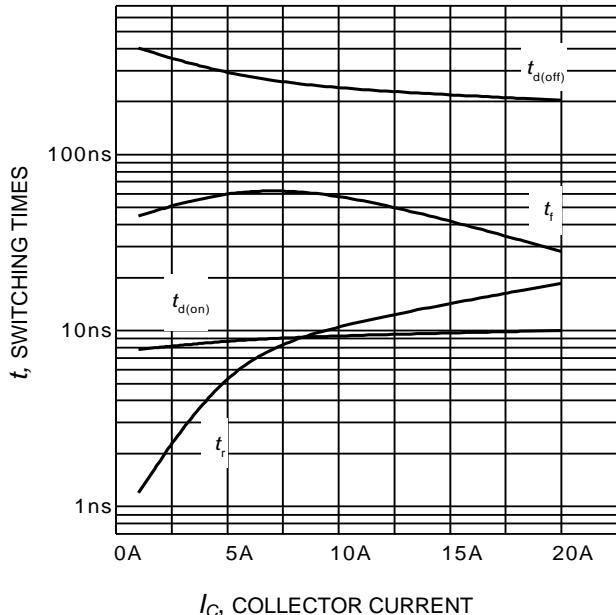
$V_{GE}$ , GATE-EMITTER VOLTAGE

**Figure 7. Typical transfer characteristic**  
( $V_{CE}=20\text{V}$ )

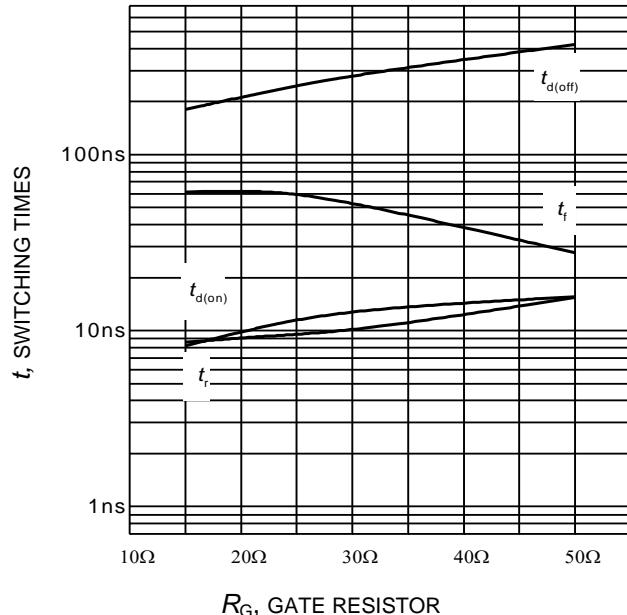


$T_j$ , JUNCTION TEMPERATURE

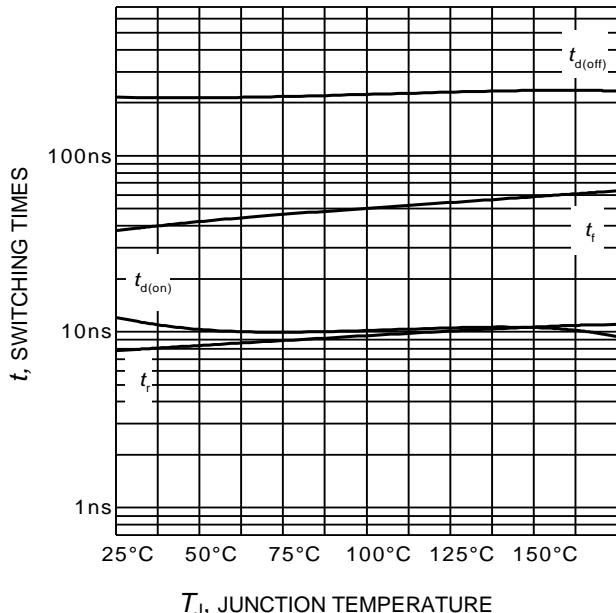
**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE} = 15\text{V}$ )



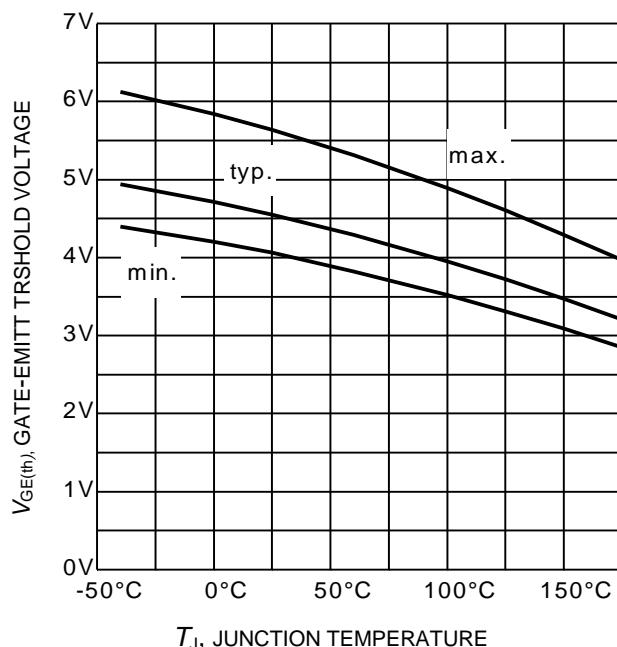
**Figure 9.** Typical switching times as a function of collector current  
 (inductive load,  $T_J=175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $r_G = 23\Omega$ ,  
 Dynamic test circuit in Figure E)



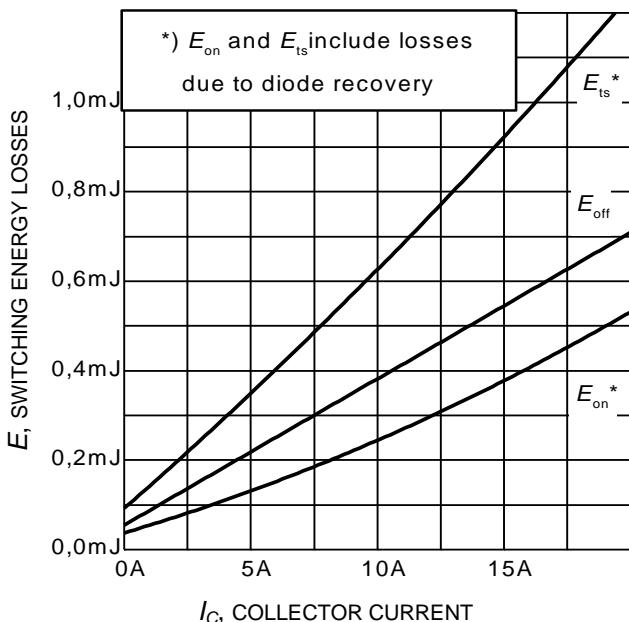
**Figure 10.** Typical switching times as a function of gate resistor  
 (inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  
 Dynamic test circuit in Figure E)



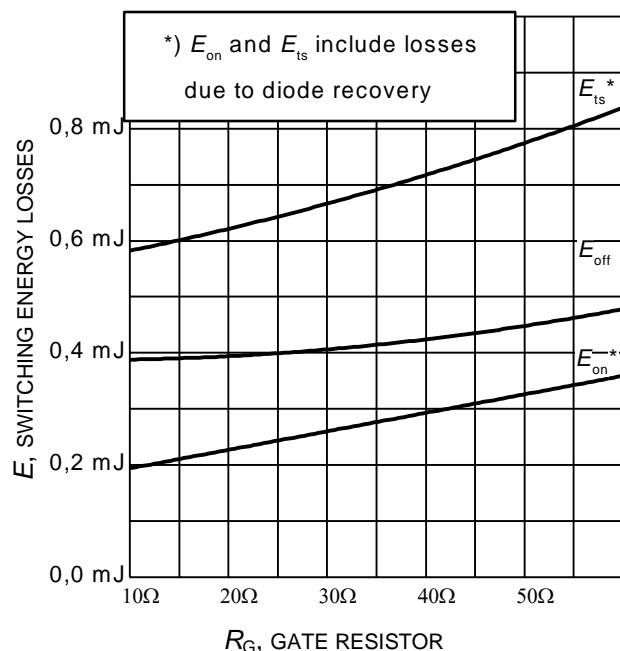
**Figure 11.** Typical switching times as a function of junction temperature  
 (inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $r_G = 23\Omega$ ,  
 Dynamic test circuit in Figure E)



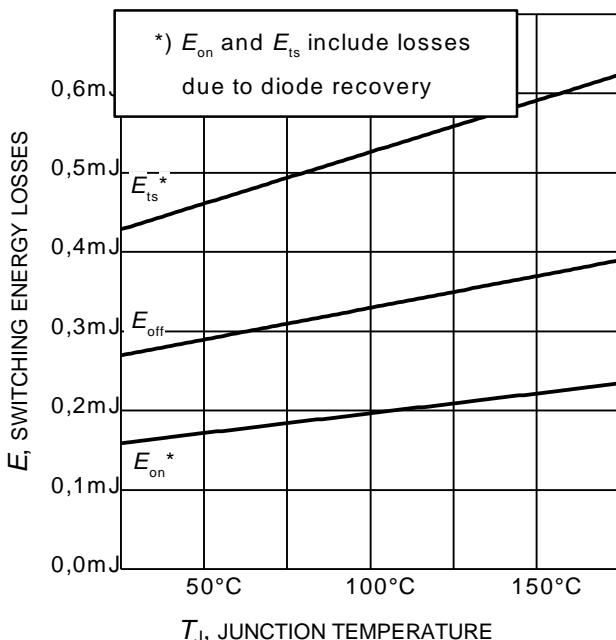
**Figure 12.** Gate-emitter threshold voltage as a function of junction temperature  
 $(I_C = 0.3\text{mA})$



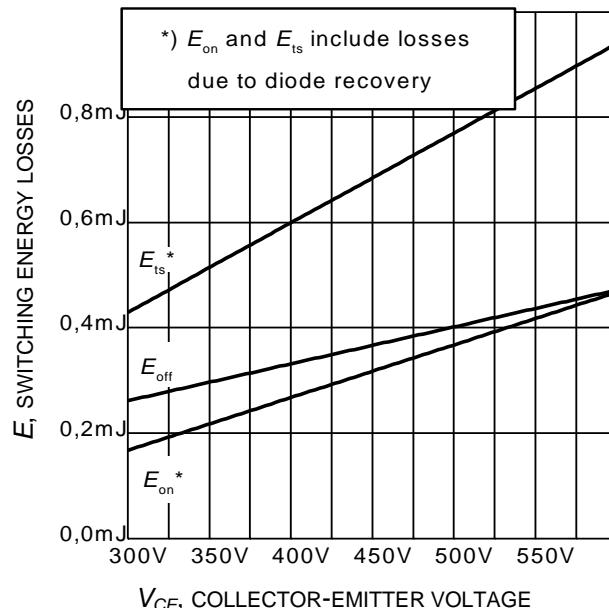
**Figure 13.** Typical switching energy losses as a function of collector current  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $r_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)



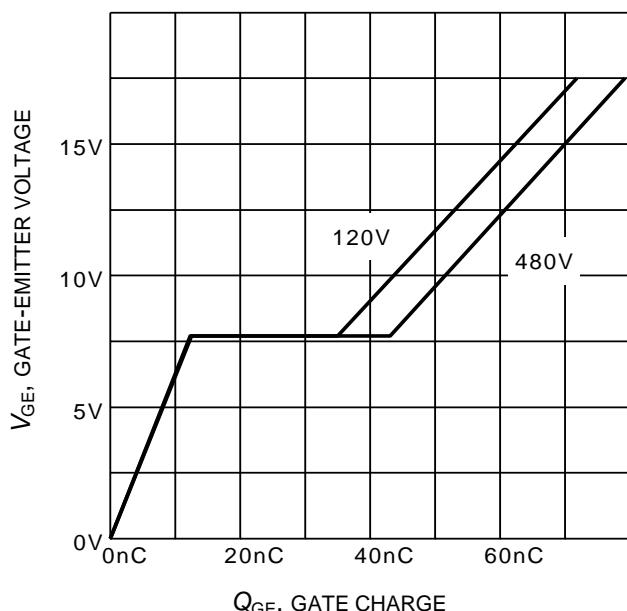
**Figure 14.** Typical switching energy losses as a function of gate resistor  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  
Dynamic test circuit in Figure E)



**Figure 15.** Typical switching energy losses as a function of junction temperature  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $r_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)

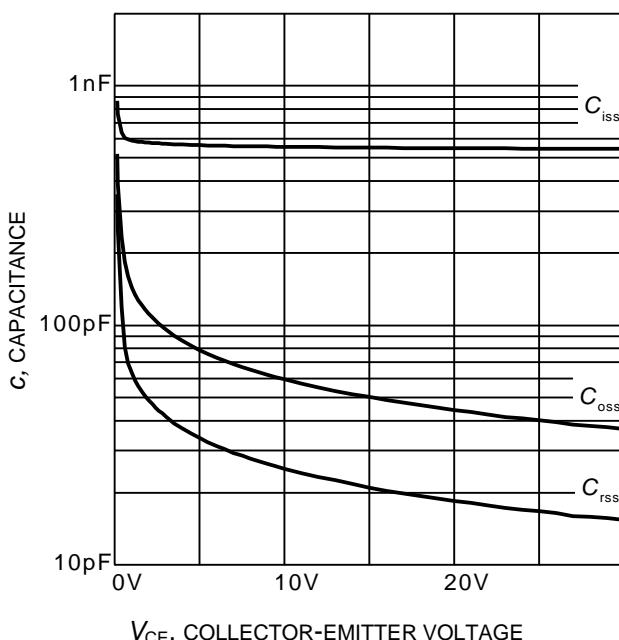


**Figure 16.** Typical switching energy losses as a function of collector-emitter voltage  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $r_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)



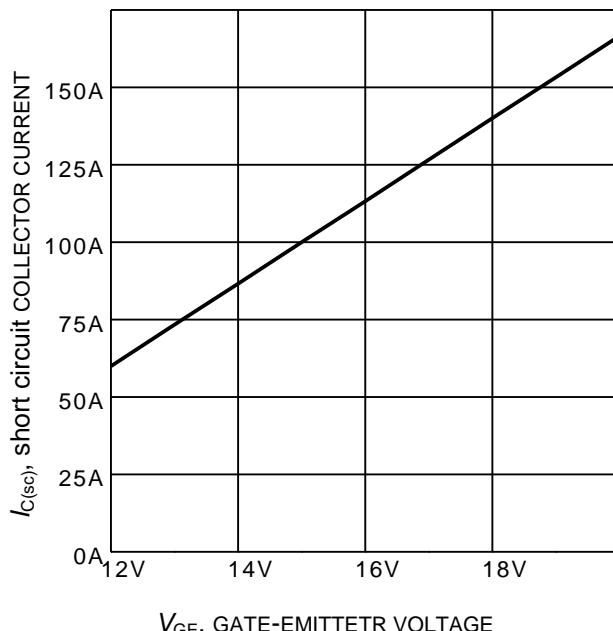
$Q_{GE}$ , GATE CHARGE

**Figure 17. Typical gate charge**  
( $I_C=10$  A)



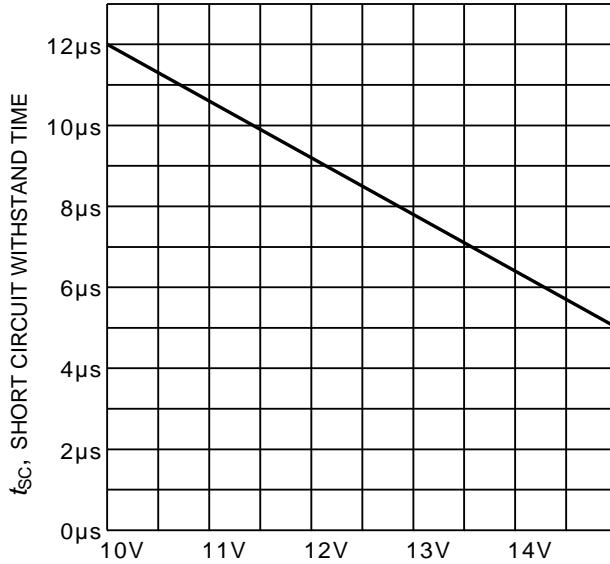
$V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

**Figure 18. Typical capacitance as a function of collector-emitter voltage**  
( $V_{GE}=0$  V,  $f = 1$  MHz)



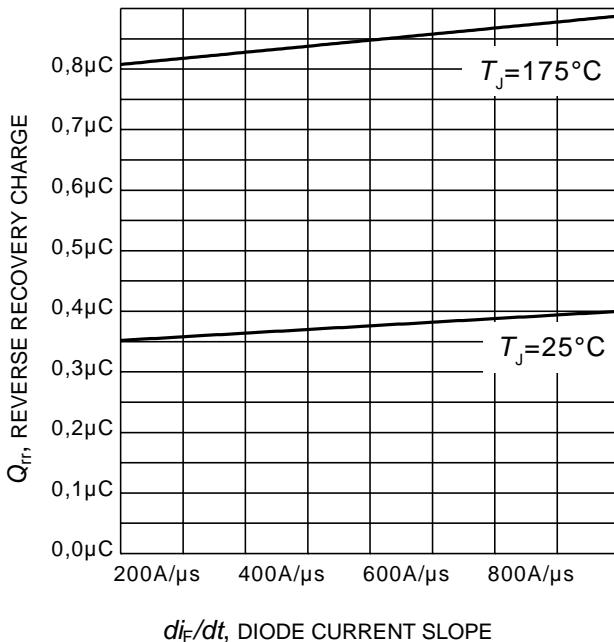
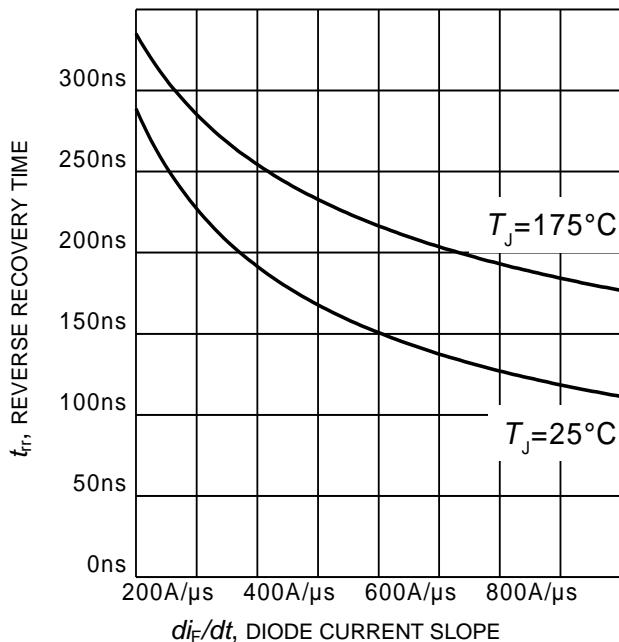
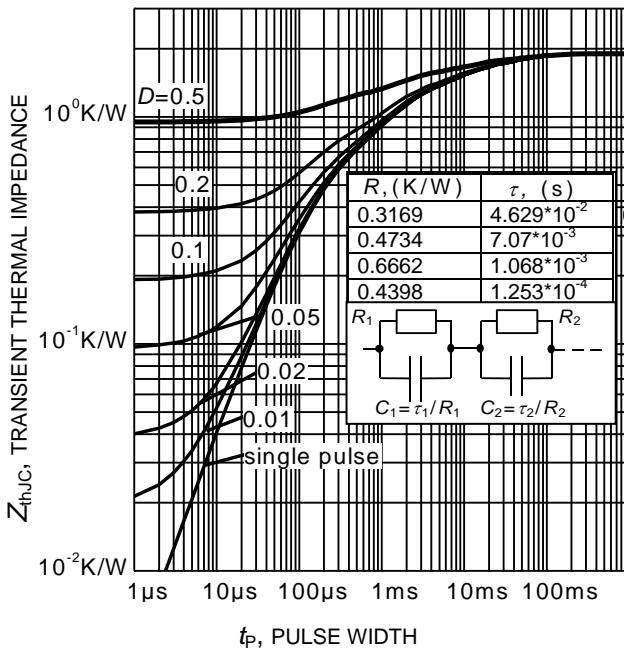
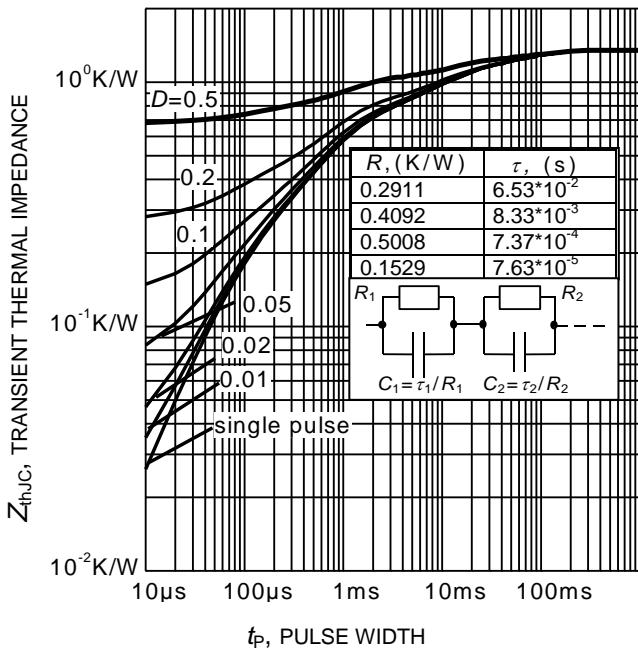
$V_{GE}$ , GATE-EMITTER VOLTAGE

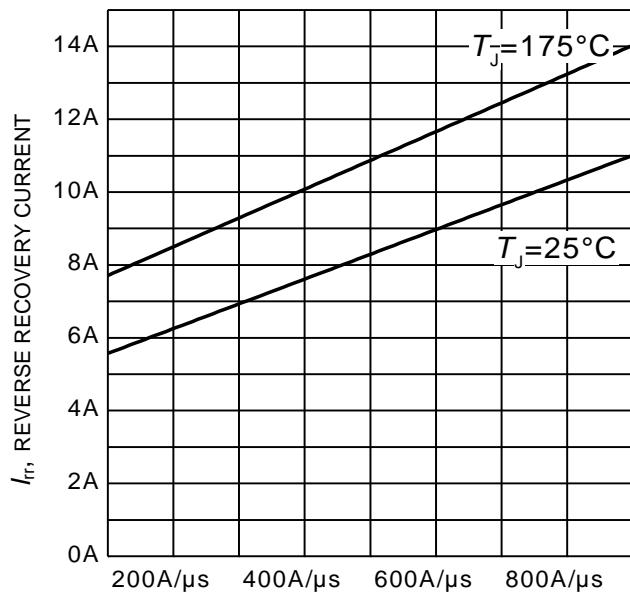
**Figure 19. Typical short circuit collector current as a function of gate-emitter voltage**  
( $V_{CE} \leq 400$  V,  $T_j \leq 150^\circ\text{C}$ )



$V_{GE}$ , GATE-EMITTER VOLTAGE

**Figure 20. Short circuit withstand time as a function of gate-emitter voltage**  
( $V_{CE}=400$  V, start at  $T_j=25^\circ\text{C}$ ,  $T_{jmax}<150^\circ\text{C}$ )

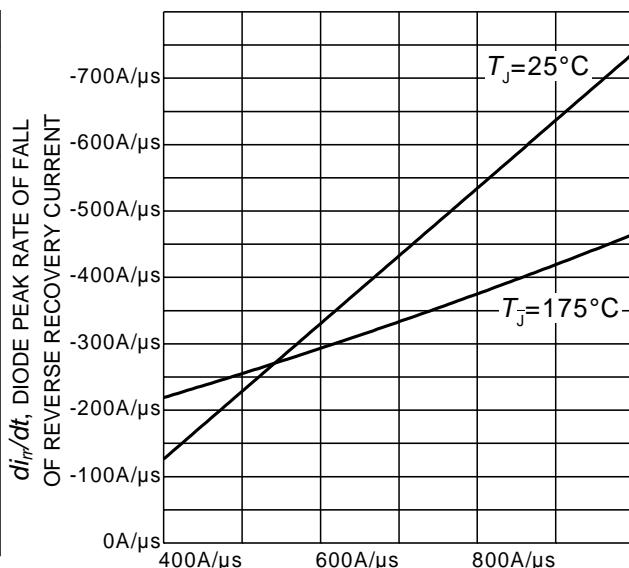




$di_F/dt$ , DIODE CURRENT SLOPE

**Figure 25. Typical reverse recovery current as a function of diode current slope**

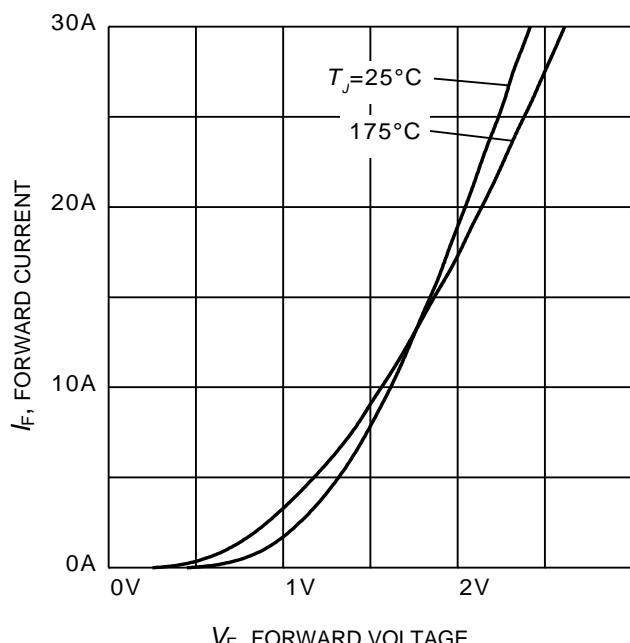
( $V_R = 400V$ ,  $I_F = 10A$ ,  
Dynamic test circuit in Figure E)



$di_F/dt$ , DIODE CURRENT SLOPE

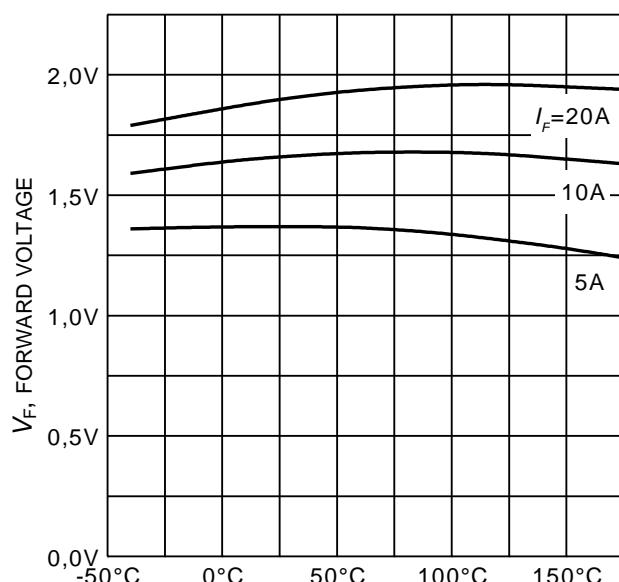
**Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**

( $V_R=400V$ ,  $I_F=10A$ ,  
Dynamic test circuit in Figure E)



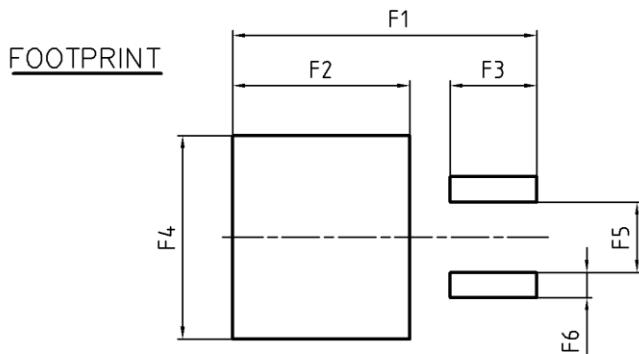
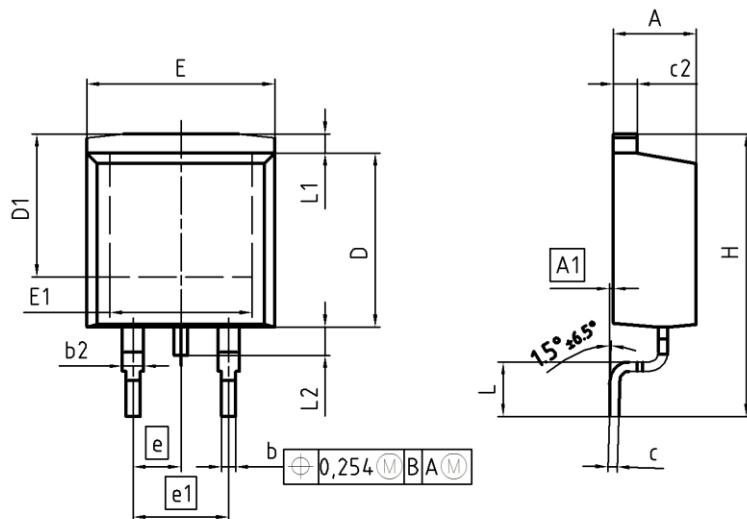
$V_F$ , FORWARD VOLTAGE

**Figure 27. Typical diode forward current as a function of forward voltage**



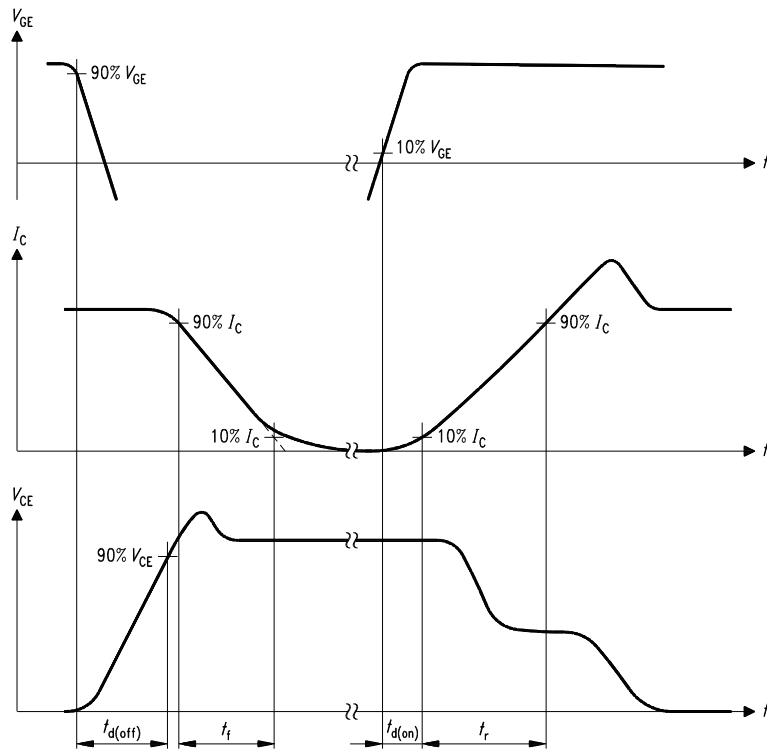
$T_J$ , JUNCTION TEMPERATURE

**Figure 28. Typical diode forward voltage as a function of junction temperature**

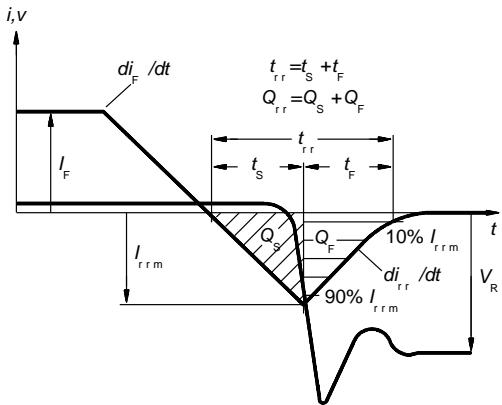
**PG-T0263-3**


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	2		2	
H	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057

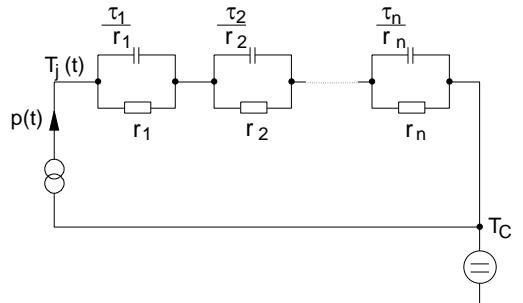
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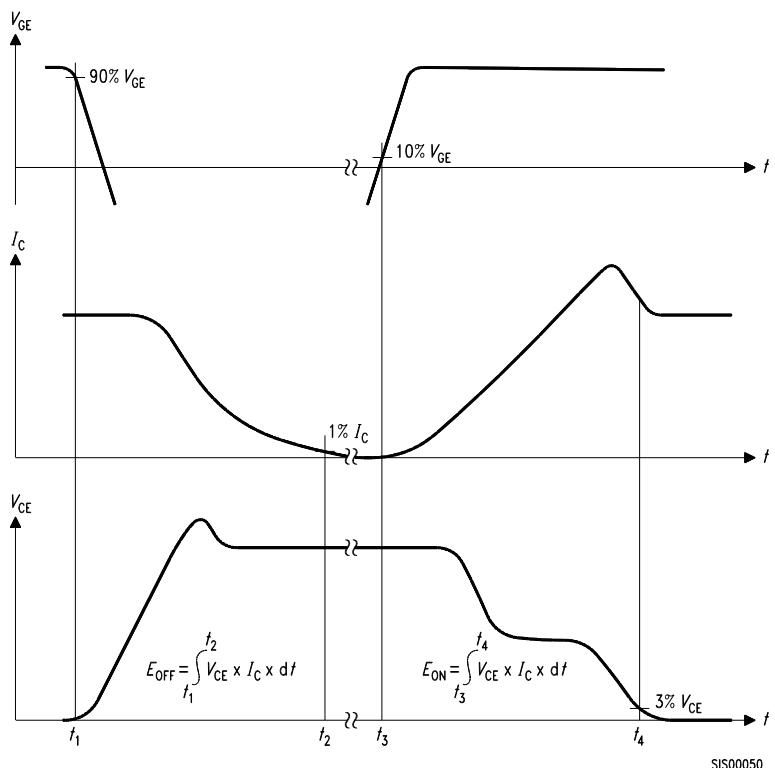
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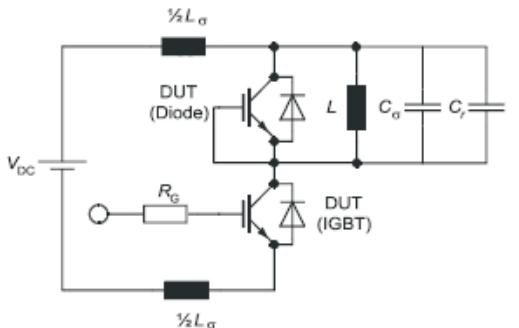
**Figure C. Definition of diodes switching characteristics**



**Figure D. Thermal equivalent circuit**



SIS00050



**Figure E. Dynamic test circuit**  
 Parasitic inductance  $L_\alpha$ ,  
 Parasitic capacitor  $C_\alpha$ ,  
 Relief capacitor  $C_r$   
 (only for ZVT switching)



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