

## Voltage Regulator

## Data Sheet



### Features

- Output voltage 3.3 V or adjustable
- 1.0 A output current
- Low drop voltage < 1.2 V @ 800 mA</li>
- Short circuit protected
- Overtemperature protected
- Operating range up to 15 V
- Industrial type
- Green Product (RoHS compliant)



For automotive and transportation applications, please refer to the Infineon TLE and TLF voltage regulator series.

### **Functional Description**

The IFX1117 is a monolithic integrated fixed NPN type voltage regulator that can supply loads up to 1.0 A. The device is housed in the small surface mounted SOT223 package. The IC is equipped with additional protection against overload, short circuit and overtemperature.

The IFX1117ME V33 supplies a regulated output voltage of 3.3 V (±2%). The IFX1117ME V supplies an output voltage with ±2% precision adjustable via an external voltage divider. The input voltage for the IFX1117ME V33 ranges from 4.5 V (=  $V_Q+V_{DR}$ ) to 15 V for a load current of 800 mA, for the maximum load current of 1.0 A a minimum input voltage of 4.7 V is required. The drop voltage  $V_{DR}$  ranges from 1.1 V to 1.4 V depending on the load current level.

The device operates in the temperature range of  $T_i = 0$  to 125 °C.

Туре	Package	Marking
IFX1117ME V33	PG-SOT223	111733
IFX1117ME V	PG-SOT223	1117V

Rev. 1.0, 2011-02-24





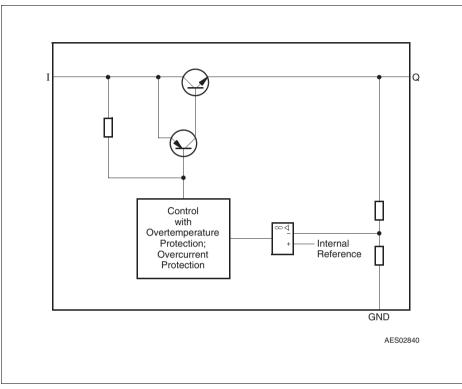
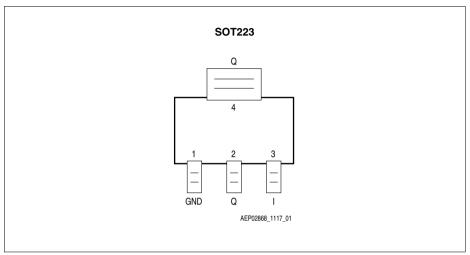


Figure 1 Block Diagram for Fixed Output Voltage IFX1117ME V33



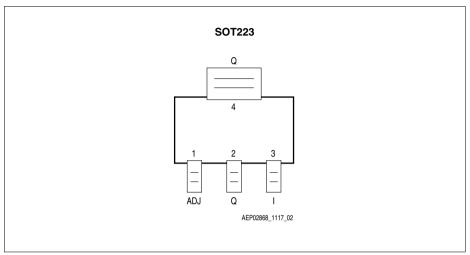


### Figure 2 Pin Configuration IFX1117ME V33 (top view)

### Table 1 Pin Definitions and Functions IFX1117ME V33

Pin No.	Symbol	Function
1	GND	Ground
2	Q	<b>Output;</b> Connect output pin to GND via a capacitor $C_{\rm Q} \ge 10 \ \mu\text{F}$ with ESR $\le 20 \ \Omega$ (see also graph "Region of Stability")
3	I	Input
4 (TAB)	Q	Output; Connect to pin 2 and heatsink area on PCB





### Figure 3 Pin Configuration IFX1117ME V (top view)

## Table 2 Pin Definitions and Functions IFX1117ME V

Pin No.	Symbol	Function
1	ADJ	<b>Adjust;</b> defines output voltage level by external voltage divider between Q, ADJ and GND.
2	Q	<b>Output;</b> Connect output pin to GND via a capacitor $C_Q \ge 10 \ \mu\text{F}$ with ESR $\le 20 \ \Omega$ (see also graph "Region of Stability").
3	I	Input
4 (TAB)	Q	Output; Connect to pin 2 and heatsink area on PCB



Parameter	Symbol	Symbol Limit Values			<b>Test Condition</b>
		Min.	Max.		
Input - Output Voltag	e Differen	ce (varia	ble device	only)	
Voltage	$V_{\rm I}$ - $V_{\rm Q}$	-0.3	20	V	-
Input Voltage (fixed	voltage ve	rsion on	ly)		
Voltage	$V_1$	-0.3	20	V	-
Output					
Voltage	$V_{Q}$	-0.3	20	V	-
Current	IQ	-	-	-	Internally limited
ESD Rating					
Electrostatic discharge voltage	$V_{ESD}$	-2	2	kV	Human Body Mode
Temperature					
Storage temperature	T <sub>stg</sub>	-50	150	°C	-
Junction temperature	Ti	-40	150	°C	-

Abaaluta Maximum Datinga Table 2

Note: Stresses above those listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### Table 4 **Operating Range**

Parameter	Symbol	Limit	Values	Unit	Remarks
		Min.	Max.		
Input Voltage	$V_1$	$V_{\rm Q}$ + $V_{\rm DR}$	15	V	-
Junction temperature	Tj	0	125	°C	-

#### Table 5 **Thermal Resistance**

Junction ambient	R <sub>thja</sub>	-	164	K/W	PG-SOT223, footprint only.
		-	81	K/W	PG-SOT223, 300 mm <sup>2</sup> heat sink area
Junction case	$R_{ m thjc}$	-	4	K/W	_

Note: In the operating range, the functions given in the circuit description are fulfilled.



### Characteristics 3.3 V Fixed Output Voltage Device IFX1117ME V33

**0** °C <  $T_{\rm i}$  < **125** °C;  $V_{\rm i}$  = 5 V,  $I_{\rm O}$  = 10 mA; unless otherwise specified.

Parameter	Symbol	Lir	imit Values		Unit	Measuring Conditions
		min.	typ.	max.	Ī	
Output voltage	VQ	3.23 5	3.300	3.36 5	V	$0 \text{ mA} \le I_{\text{Q}} \le 800 \text{ mA} \ 4.7 \text{ V} \le V_{\text{I}} \le 10 \text{ V}$
Output voltage	VQ	-	3.300	-	V	$0 \text{ mA} \le I_Q \le 1000 \text{ mA};$ 4.7 V $\le V_1 \le 15 \text{V}$
Line regulation	$\Delta V_{Q}$	-	1	6	mV	4.7 V $\leq V_{\rm I} \leq$ 15V
Load regulation	$\Delta V_{Q}$	-	1	10	mV	$0 \text{ mA} \le I_Q \le 800 \text{ mA};^{1)}$
		-	2	-	mV	$0 \text{ mA} \le I_Q \le 1.0 \text{ A}^{1)}$
Drop voltage	$V_{DR}$	-	1.00	1.10	V	$I_{\rm Q} = 100 \ {\rm mA}^{2)}$
Drop voltage	$V_{DR}$	-	1.05	1.15	V	$I_{\rm Q} = 500 \ {\rm mA}^{2)}$
Drop voltage	$V_{DR}$	-	1.10	1.20	V	$I_{\rm Q} = 800 \ {\rm mA}^{2)}$
Drop voltage	$V_{DR}$	-	1.30	1.40	V	$I_{\rm Q} = 1.0 \ {\rm A}^{2)}$
$\overline{\text{Current consumption;}} \\ I_{q} = I_{I} - I_{Q}$	Iq	-	5	10	mA	<i>I</i> <sub>Q</sub> = 10 mA
Temperature stability	$\Delta V_{Q}$	-	16.5	_	mV	3)
Long Term Stability	-	-	0.3	-	%	3)
Current limit	I <sub>Qmax</sub>	1100	-	2250	mA	$V_{\rm Q} = 0.5 \ {\rm V}$
RMS Output Noise	-	-	30	-	ppm	ppm of $V_{\rm Q}$ , $T_{\rm j}$ = 25 °C 10 Hz $\leq$ $f$ $\leq$ 10 kHz <sup>3)</sup>
Power Supply Ripple Rejection	PSRR	60	65	-	dB	$f_{\rm r} = 120 \text{ Hz}, V_{\rm r} = 1 V_{\rm PP}^{3}$

1) Measured at constant junction temperature

2) Drop voltage measured when the output voltage has dropped 100 mV from the nominal value obtained at  $V_1$  = 5.0 V.

3) Specified by design; not subject to production test.



### Characteristics Adjustable Output Voltage Device IFX1117ME V

**0** °C <  $T_{\rm i}$  < **125** °C;  $V_{\rm l}$  = 5 V,  $I_{\rm O}$  = 10 mA; unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Measuring Conditions
		min.	typ.	max.	1	
Reference voltage	$V_Q$	1.22 5	1.250	1.27 0	V	10 mA $\leq I_{Q} \leq$ 800 mA; 1.4 V $\leq (V_{I}-V_{Q}) \leq$ 10 V
Output voltage	$V_Q$	-	1.250	-	V	10 mA $\leq I_Q \leq$ 1000 mA; 2.65 V $\leq V_I \leq$ 15 V
Line regulation	$\Delta V_Q$	-	0.035	0.2	% <sup>1)</sup>	1.5 V ≤(V <sub>I</sub> -V <sub>Q</sub> )≤ 13.75 V
Load regulation	$\Delta V_Q$	-	0.2	0.4	% <sup>1)</sup>	$10 \text{ mA} \le I_Q \le 800 \text{ mA};^{2)}$
		-	0.25	_	% <sup>1)</sup>	10 mA $\leq$ $I_{\rm Q}$ $\leq$ 1.0 A $^{2)}$
Drop voltage	$V_{DR}$	-	1.00	1.10	V	$I_{\rm Q}$ = 100 mA <sup>3)</sup>
Drop voltage	$V_{DR}$	-	1.05	1.15	V	$I_{\rm Q}$ = 500 mA <sup>3)</sup>
Drop voltage	$V_{DR}$	-	1.10	1.20	V	$I_{\rm Q}$ = 800 mA <sup>3)</sup>
Drop voltage	$V_{DR}$	-	1.30	1.40	V	$I_{\rm Q} = 1.0 \ {\rm A}^{-3}$
Minimum Load Current <sup>4)</sup>	$I_q$	-	1.7	5.0	mA	V <sub>1</sub> = 15 V
Adjust Current	I <sub>ADJ</sub>	-	100	120	μA	<i>I</i> <sub>Q</sub> = 10 mA
Adjust Current Change	$\Delta I_{ADJ}$	-	2	5	μA	1.4 V $\leq (V_{\rm I} - V_{\rm Q}) \leq$ 13.6 V; 10 mA $\leq I_{\rm Q} \leq$ 800 mA
Temperature stability	$\Delta V_Q$	-	0.5	-	% <sup>1)</sup>	5)
Long Term Stability	-	-	0.3	-	% <sup>1)</sup>	5)
Current limit	I <sub>Qmax</sub>	1100	-	2250	mA	$V_{\rm Q} = 0.5  {\rm V}$
RMS Output Noise	-	-	30	-	ppm	ppm of $V_{\rm Q}$ , $T_{\rm j}$ = 25 °C 10 Hz $\leq$ f $\leq$ 10 kHz <sup>5)</sup>
Power Supply Ripple Rejection	PSRR	65	70	-	dB	$f_{\rm r}$ = 120 Hz, $V_{\rm r}$ = 1 V <sub>PP</sub> <sup>5)</sup>

1) Related to  $V_{Q}$ 

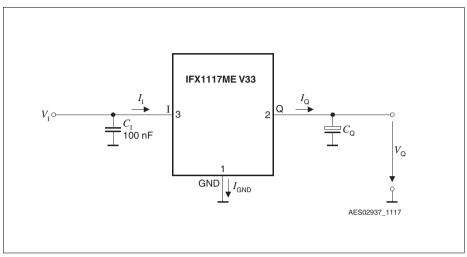
2) Measured at constant junction temperature

3) Drop voltage measured when the output voltage has dropped 100 mV from the nominal value obtained at  $V_1$  = 5.0 V.

4) Minimum load current required to maintain regulation

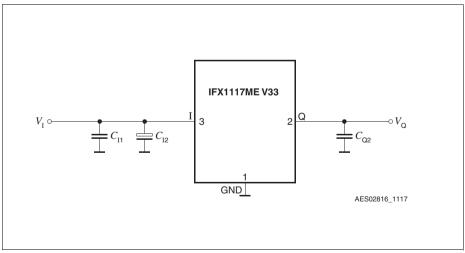
5) Specified by design; not subject to production test.

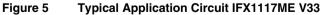






### **Application Information**







### Output

The IFX1117 requires a 10  $\mu$ F output capacitor with ESR  $\leq$  20  $\Omega$  for the stability of the regulation loop. The use of a tantalum output capacitor is recommended.

For the adjustable device IFX1117ME V the output voltage level can be defined by a voltage divider between Q, ADJ and GND.

The output voltage calculates:

$$V_{\rm Q} = V_{\rm REF} \times \left(1 + \frac{R_2}{R_1}\right) + I_{\rm ADJ} \times R_2 \tag{1}$$

At the input of the regulator a capacitor is recommended to compensate line influences. As a minimum a 100 nF ceramic input capacitor should be used. If the regulator is used in an environment with long input lines an input capacitance of 10  $\mu$ F is suggested.

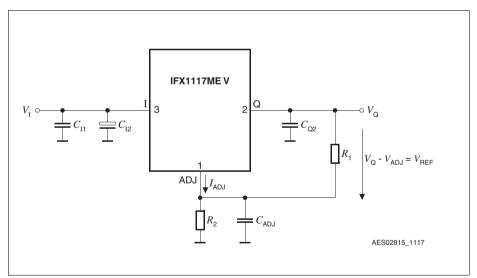
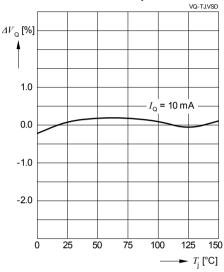


Figure 6 Typical Application Circuit IFX1117ME V

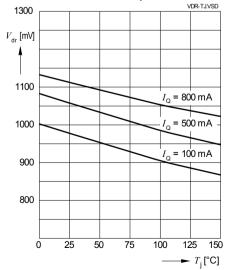


## **Typical Performance Characteristics**

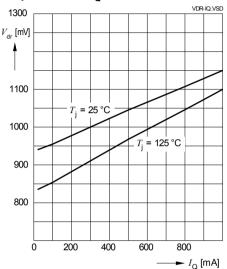




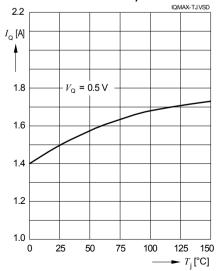
Dropout Voltage  $V_{dr}$  versus Junction Temperature  $T_{j}$ 



Dropout Voltage  $V_{dr}$  versus Output Current  $I_{O}$ 



# Maximum Output Current $I_{Q}$ versus Junction Temperature $T_{i}$

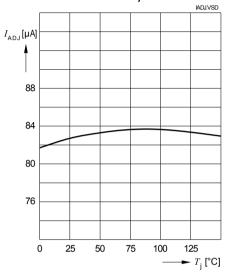


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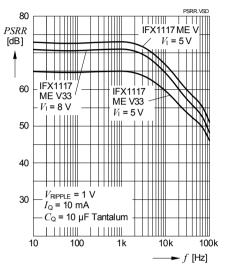


### **Typical Performance Characteristics**

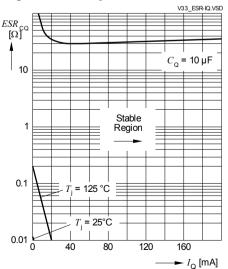
# Adjust Pin Current $I_{ADJ}$ versus Junction Temperature $T_i$



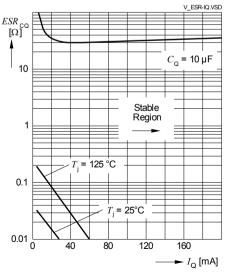
Power Supply Ripple Rejection PSRR versus Frequency f



**Region of Stability Version ME V33** 



Region of Stability Version ME V



Data Sheet

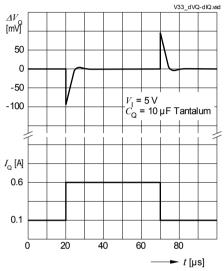
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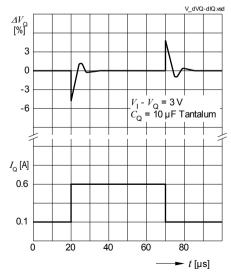


## **Typical Performance Characteristics**

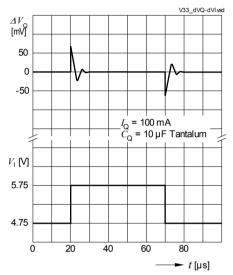
### Load Transient Response Version ME V33



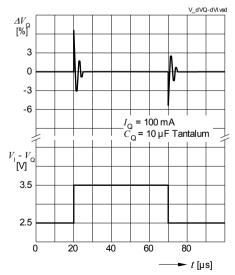
Load Transient Response Version ME V



Line Transient Response Version ME V33



### Line Transient Response Version ME V

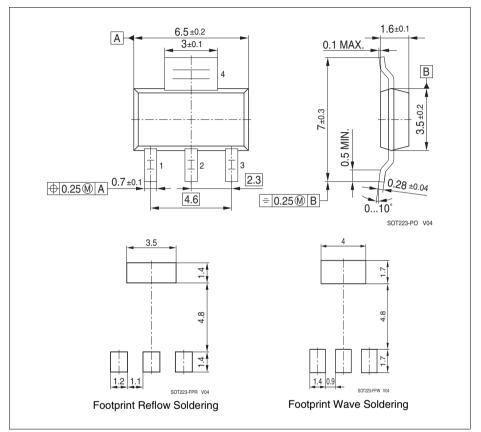


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### Package Outline



### Figure 7 Outline and footprint PG-SOT223

### Green Product (RoHS-Compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

You can find all of our packages, sorts of packing and others in our Infineon Internet Page "Products": http://www.infineon.com/products.

SMD = Surface Mounted Device

Dimensions in mm

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### **Revision History**

Version	Date	Changes
Rev. 1.0	2011-02-24	Data Sheet

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