

## Low Noise Silicon Bipolar RF Transistor

- For low noise, high-gain amplifiers up to 2 GHz
- For linear broadband amplifiers
- $f_T$  = 8 GHz,  $NF_{min}$  = 1 dB at 900 MHz
- Pb-free (RoHS compliant) package
- Qualification report according to AEC-Q101 available





## **ESD** (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package
BFR193	RCs	1 = B	2 = E	3 = C	SOT23

## **Maximum Ratings** at $T_A$ = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	12	V
Collector-emitter voltage	$V_{CES}$	20	
Collector-base voltage	$V_{\mathrm{CBO}}$	20	
Emitter-base voltage	$V_{EBO}$	2	
Collector current	I <sub>C</sub>	80	mA
Base current	l <sub>B</sub>	10	
Total power dissipation <sup>1)</sup>	P <sub>tot</sub>	580	mW
<i>T</i> <sub>S</sub> ≤ 69°C			
Junction temperature	$T_{J}$	150	°C
Storage temperature	$T_{Stq}$	-55 150	

#### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>2)</sup>	R <sub>thJS</sub>	140	K/W

 $<sup>{}^1</sup>T_{\rm S}$  is measured on the collector lead at the soldering point to the pcb

 $<sup>^2</sup>$ For calculation of  $R_{\mathrm{thJS}}$  please refer to Application Note AN077 (Thermal Resistance Calculation)



**Electrical Characteristics** at  $T_A$  = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	12	-	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0					
Collector-emitter cutoff current	I <sub>CES</sub>	-	_	100	μΑ
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I <sub>CBO</sub>	-	-	100	nA
$V_{\rm CB} = 10 \text{ V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I <sub>EBO</sub>	-	_	1	μA
$V_{\rm EB} = 1 \text{ V}, I_{\rm C} = 0$					
DC current gain	h <sub>FE</sub>	70	100	140	-
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, pulse measured					



**Electrical Characteristics** at  $T_A$  = 25 °C, unless otherwise specified

g)  f <sub>T</sub> C <sub>cb</sub>	6 -	8 0.66	- 1	GHz
f <sub>T</sub>	6		- 1	GHz
C <sub>cb</sub>	-		1	GHz
	-	0.66	1	
	-	0.66	1	+
Coo			1	pF
Coo				
Coo				
_ ce	-	0.28	-	
C <sub>eb</sub>	-	2.25	-	
NF <sub>min</sub>				dB
	-	1	-	
	-	1.6	_	
G <sub>ma</sub>				]
	-	15	-	
	-	10	_	
S <sub>21e</sub>   <sup>2</sup>				dB
	-	13	_	
	-	7.5	_	
IP <sub>3</sub>	-	30	-	dBm
P <sub>-1dB</sub>	-	13	-	1
	1	1	1	1
		NF <sub>min</sub>   -     -     -	NF <sub>min</sub> - 1 - 1.6  G <sub>ma</sub> - 15 - 10   S <sub>21e</sub>   <sup>2</sup> - 13 - 7.5  IP <sub>3</sub> - 30	NF <sub>min</sub> - 1 1.6 -  G <sub>ma</sub> - 15 10 -  S <sub>21e</sub>   <sup>2</sup> - 13 7.5 -  IP <sub>3</sub> - 30 -

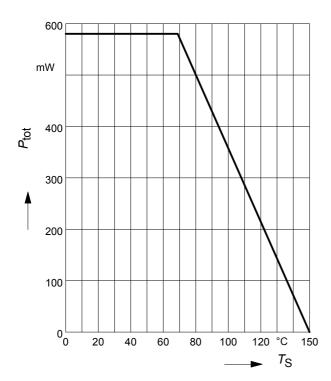
 $<sup>{}^{1}</sup>G_{\text{ma}} = |S_{21} / S_{12}| (k-(k^{2}-1)^{1/2})$ 

<sup>&</sup>lt;sup>2</sup>IP3 value depends on termination of all intermodulation frequency components.

Termination used for this measurement is  $50\Omega$  from 0.2 MHz to 12 GHz

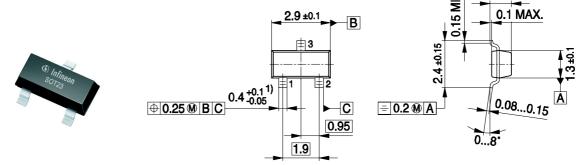


# Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$





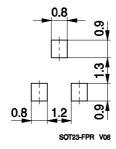
# Package Outline



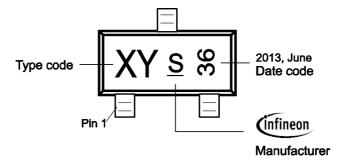
1) Lead width can be 0.6 max. in dambar area

SOT23-PO V08

## **Foot Print**



# Marking Layout (Example)



# Standard Packing

Reel Ø 180 mm = 3.000 Pieces/ Reel

Reel Ø 330 mm = 10.000 Pieces/ Reel

O.9

O.2

Pin 1

3.15

SOT23-TP V02



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