

IGBT

TRENCHSTOP[™] IGBT4 High Power Chip IGC142T120T8RH

Data Sheet

Industrial Power Control

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TRENCHSTOP[™] IGBT4 High Power Chip

Features:

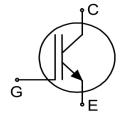
- 1200V trench & field stop technology
- Low V_{CEsat}
- Soft turn off
- Positive temperature coefficient
- Easy paralleling

Recommended for:

Medium / high power modules

Applications:

• Medium / high power drives



Chip Type	V _{CE}	I Cn	Die Size	Package
IGC142T120T8RH	1200V	150A	11.31mm x 12.56mm	Sawn on foil

Mechanical Parameters

Die size		11.31 x 12.56		
Emitter pad size		See chip drawing	mm²	
Gate pad size		1.31 x 0.811		
Area total		142.05		
Silicon thickness		140	μm	
Wafer size		200	mm	
Maximum possible ch	ips per wafer	170		
Passivation frontside	frontside Photoimide			
Pad metal		3200nm AlSiCu		
Backside metal		Ni Ag – system To achieve a reliable solder connection it is street recommended not to consume the Ni layer complete production process		
Die bond		Electrically conductive epoxy glue and soft so	lder	
Wire bond		AI, ≤500μm		
Reject ink dot size		Ø 0.65mm; max. 1.2mm		
Storage environment	for original and sealed MBB bags	Ambient atmosphere air, temperature 17°C – 25°C		
(<6 months)	for open MBB bags	Acc. IEC62258-3; Section 9.4 Storage Environ	ment.	



Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T_{vj} =25°C	V _{CE}	1200	V
DC collector current, limited by $T_{\rm vj\;max}^{\;\;\;1}$	I _C	-	Α
Pulsed collector current, t_p limited by $T_{vj \text{ max}}^2$	I _{C,puls}	450	Α
Gate-emitter voltage	V_{GE}	±20	V
Virtual junction operating temperature	$T_{\rm vj}$	-40 +175	°C
Short circuit data $^{1/2/3}$ V_{GE} =15V, V_{CC} =800V, T_{vj} =150°C	t _{sc}	10	μs

Static Characteristics (tested on wafer), T_{vi}=25°C

Parameter	Symbol	Conditions	Value			Unit
raiailietei	Symbol	Conditions	min.	typ.	max.	
Collector-emitter breakdown voltage	V _{(BR)CES}	$V_{\rm GE}$ =0V, $I_{\rm C}$ =5.7mA	1200	ı	-	
Collector-emitter saturation voltage	$V_{\sf CEsat}$	V _{GE} =15V, I _C =45A	0.97	1.1	1.26	V
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C}$ =5.7mA, $V_{\rm GE}$ = $V_{\rm CE}$	5.1	5.8	6.4	
Zero gate voltage collector current	I _{CES}	$V_{\text{CE}} = 1200 \text{V}, \ V_{\text{GE}} = 0 \text{V}$	1	1	20	μA
Gate-emitter leakage current	I _{GES}	$V_{CE} = 0V, V_{GE} = 20V$	1	1	120	nA
Integrated gate resistor	r _G		-	5	-	Ω

Electrical Characteristics 2

Parameter		Symbol	Conditions	Value			Unit
		Symbol	Conditions	min.	typ.	max.	Oilit
Collector-emitter saturation	<i>T</i> _{vj} =25°C	1/	V _{GE} =15V, I _C =150A	1	1.7	1	V
voltage	<i>T</i> _{vj} =150°C	- V _{CEsat}	V _{GE} =15V, I _C =150A	-	2.1	-	V
Input capacitance		C _{ies}	V _{CE} =25V,	-	9300	ı	n.E
Reverse transfer capacitance		C _{res}	V_{GE} =0V, f =1MHz T_{Vj} =25°C	-	510	1	pF

¹ Depending on thermal properties of assembly.

² Not subject to production test - verified by design/characterization.

³ Allowed number of short circuits: <1000; time between short circuits: >1s.



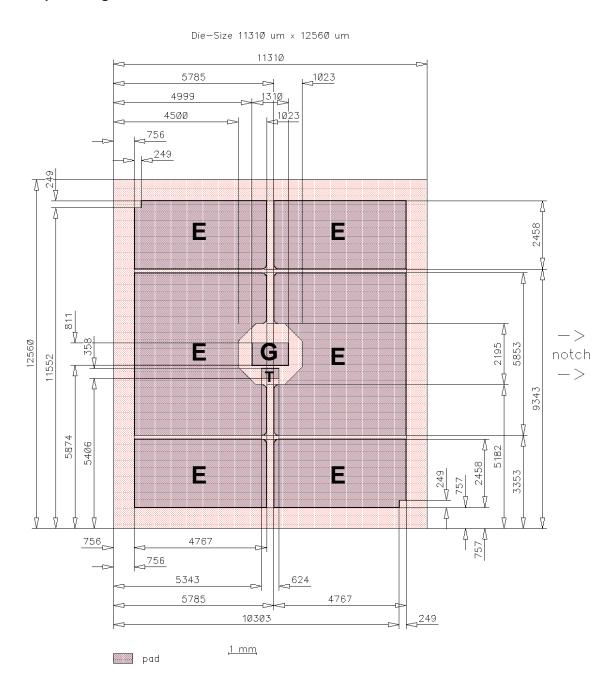
Further Electrical Characteristics

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

Application example	FZ600R12KP4	Rev. 2.2
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Chip Drawing



E = Emitter

G = Gate

T = Test pad do not contact



Bare Die Product Specif

Test coverage at wafer level cannot cover all application conditions. Therefore it is recommended to test all characteristics which are relevant for the application at package level, including RBSOA and SCSOA.

Description
AQL 0.65 for visual inspection according to failure catalogue
Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Revision	Subjects (major changes since last revision)	Date
2.0	Final data sheet	11.05.2016

Relevant App	pplication Notes	



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