# International TOR Rectifier

# POWER MOSFET THRU-HOLE (TO-257AA)

# IRFY9240C,IRFY9240CM 200V, P-CHANNEL HEXFET® MOSFET TECHNOLOGY

**Product Summary** 

| Part Number | Rds(on) | ld    | Eyelets |  |
|-------------|---------|-------|---------|--|
| IRFY9240C   | 0.51 Ω  | -9.4A | Ceramic |  |
| IRFY9240CM  | 0.51 Ω  | -9.4A | Ceramic |  |

HEXFET® MOSFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry design achieves very low on-state resistance combined with high transconductance. HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, high energy pulse circuits, and virtually any application where high reliability is required. The HEXFET transistor's totally isolated package eliminates the need for additional isolating material between the device and the heatsink. This improves thermal efficiency and reduces drain capacitance.



#### Features:

- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Electrically Isolated
- Ceramic Eyelets
- Ideally Suited For Space Level Applications

# **Absolute Maximum Ratings**

|  | Parameter                       |  | Units |
|--|---------------------------------|--|-------|
| ID @ VGS = -10V, TC = 25°C             | Continuous Drain Current        | -9.4                                     |       |
| ID @ VGS = -10V, TC = 100°C            | Continuous Drain Current        | -6.0                                     | Α     |
| IDM                                    | IDM Pulsed Drain Current ①      |  |       |
| P <sub>D</sub> @ T <sub>C</sub> = 25°C | Max. Power Dissipation          | 100                                      | W     |
|  | Linear Derating Factor          | 0.8                                      | W/°C  |
| VGS                                    | Gate-to-Source Voltage          | ±20                                      | V     |
| EAS                                    | Single Pulse Avalanche Energy ② | 700                                      | mJ    |
| IAR                                    | Avalanche Current ①             | -9.4                                     | Α     |
| EAR                                    | Repetitive Avalanche Energy ①   | 10                                       | mJ    |
| dv/dt                                  | Peak Diode Recovery dv/dt 3     | -5.5                                     | V/ns  |
| TJ                                     | Operating Junction              | -55 to 150                               |       |
| TSTG                                   | Storage Temperature Range       |  | °C    |
|  | Lead Temperature                | 300(0.063in.(1.6mm)from case for 10 sec) |       |
|  | Weight                          | 4.3 (Typical)                            | g     |

For footnotes refer to the last page

#### Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

|                                     | Parameter                                     | Min  | Тур  | Max  | Units | Test Conditions   |
|-------------------------------------|---|------|------|------|-------|---|
| BVDSS                               | Drain-to-Source Breakdown Voltage             | -200 | _    | _    | V     | VGS = 0V, ID = -1.0mA   |
| ΔBV <sub>DSS</sub> /ΔT <sub>J</sub> | Temperature Coefficient of Breakdown Voltage  | _    | -0.2 | _    | V/°C  | Reference to 25°C, I <sub>D</sub> = -1.0mA  |
| RDS(on)                             | Static Drain-to-Source On-State<br>Resistance | _    | _    | 0.51 | Ω     | VGS = -10V, ID = -6.0A (4)  |
| VGS(th)                             | Gate Threshold Voltage                        | -2.0 | _    | -4.0 | V     | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA                                   |
| 9fs                                 | Forward Transconductance                      | 4.0  | _    | _    | S (7) | V <sub>DS</sub> > -15V, I <sub>DS</sub> = -6.0A ④   |
| IDSS                                | Zero Gate Voltage Drain Current               |      | _    | -25  | μА    | V <sub>DS</sub> = -160V ,V <sub>GS</sub> =0V  |
|                                     |   | _    | _    | -250 | μΑ    | V <sub>DS</sub> = -160V,  |
|                                     |   |      |      |      |       | VGS = 0V, TJ = 125°C  |
| IGSS                                | Gate-to-Source Leakage Forward                | _    | _    | -100 | ^     | V <sub>G</sub> S = -20V   |
| IGSS                                | Gate-to-Source Leakage Reverse                | _    | _    | 100  | nA    | VGS = 20V   |
| Qg                                  | Total Gate Charge                             |      | _    | 60   |       | Vgs = -10V, ID = -9.4A  |
| Qgs                                 | Gate-to-Source Charge                         | _    | _    | 15   | nC    | V <sub>DS</sub> = -100V   |
| Q <sub>gd</sub>                     | Gate-to-Drain ('Miller') Charge               |      | _    | 38   |       |   |
| td(on)                              | Turn-On Delay Time                            | _    | _    | 35   |       | $V_{DD} = -100V, I_{D} = -9.4A,$  |
| tr                                  | Rise Time                                     | _    | _    | 85   |       | $R_G = 9.1\Omega$   |
| td(off)                             | Turn-Off Delay Time                           | _    | _    | 85   | ns    |   |
| tf                                  | Fall Time                                     | _    | _    | 65   |       |   |
| LS+LD                               | Total Inductance                              | _    | 6.8  | _    | nΗ    | Measured from drain lead (6mm/0.25in. from package) to source lead (6mm/0.25in. from package) |
| C <sub>iss</sub>                    | Input Capacitance                             | _    | 1200 | _    |       | VGS = 0V, VDS = -25V  |
| Coss                                | Output Capacitance                            | _    | 570  | _    | pF    | f = 1.0MHz  |
| C <sub>rss</sub>                    | Reverse Transfer Capacitance                  |      | 81   |      |       |   |

### **Source-Drain Diode Ratings and Characteristics**

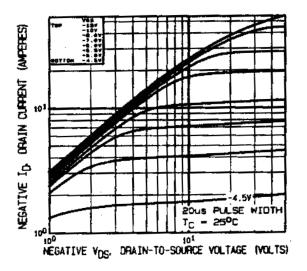
|                 | Parameter                    |  | Min | Тур | Max  | Units | Test Conditions   |
|-----------------|------------------------------|--|-----|-----|------|-------|---|
| IS              | Continuous Source Current (E | Body Diode)  |     | _   | -9.4 | ۸     |   |
| ISM             | Pulse Source Current (Body [ | Diode) ①   | -   | _   | -36  | Α     |   |
| VSD             | Diode Forward Voltage        |  | -   | _   | -4.6 | V     | $T_j = 25^{\circ}C$ , $I_S = -9.4A$ , $V_{GS} = 0V$ ④         |
| t <sub>rr</sub> | Reverse Recovery Time        |  |     | _   | 440  | nS    | $T_j = 25^{\circ}C$ , $I_F = -9.4A$ , $di/dt \le -100A/\mu s$ |
| QRR             | Reverse Recovery Charge      |  |     | _   | 7.2  | μC    | V <sub>DD</sub> ≤ -50V ④                                      |
| ton             | Forward Turn-On Time         | Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_S + L_D$ . |     |     |      |       |   |

## **Thermal Resistance**

|                    | Parameter           | Min | Тур  | Max  | Units | Test Conditions      |
|--------------------|---------------------|-----|------|------|-------|----------------------|
| RthJC              | Junction-to-Case    | _   | _    | 1.25 |       |                      |
| RthCS              | Case-to-sink        | _   | 0.21 | _    | °C/W  |                      |
| R <sub>th</sub> JA | Junction-to-Ambient | _   | _    | 80   |       | Typical socket mount |

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page



COUR PLLSE MIDTH
TC ~ 150°C
NEGATIVE VDS. DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Fig 1. Typical Output Characteristics



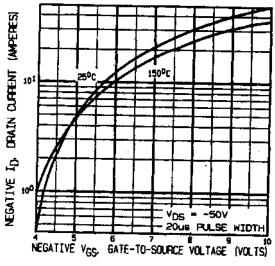
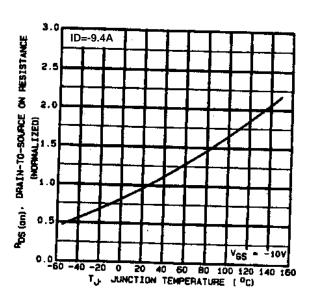
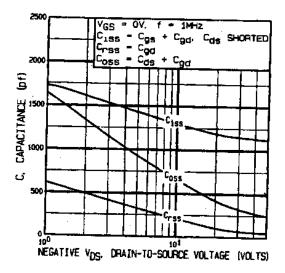


Fig 3. Typical Transfer Characteristics

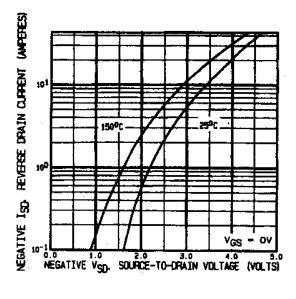


**Fig 4.** Normalized On-Resistance Vs. Temperature



**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage

**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



**Fig 7.** Typical Source-Drain Diode Forward Voltage

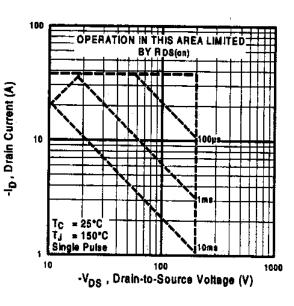
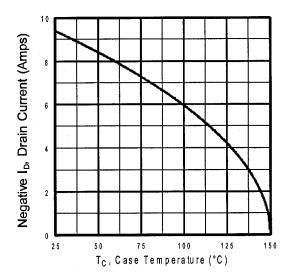


Fig 8. Maximum Safe Operating Area



**Fig 9.** Maximum Drain Current Vs. Case Temperature

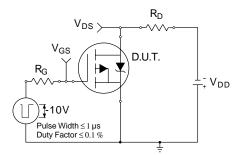


Fig 10a. Switching Time Test Circuit

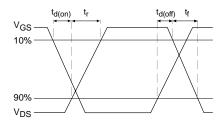


Fig 10b. Switching Time Waveforms

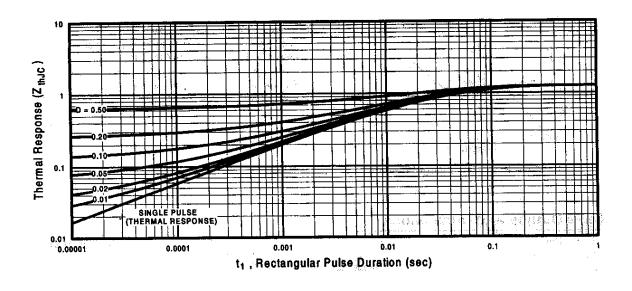


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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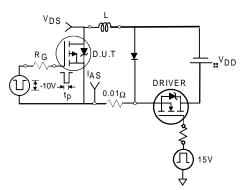


Fig 12a. Unclamped Inductive Test Circuit

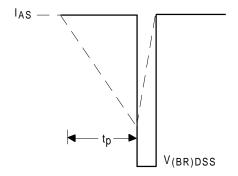


Fig 12b. Unclamped Inductive Waveforms

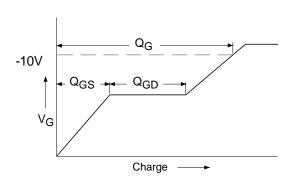


Fig 13a. Basic Gate Charge Waveform

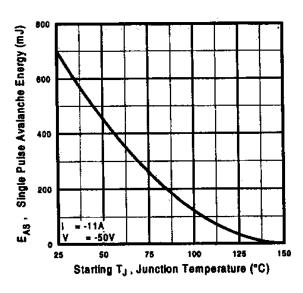


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

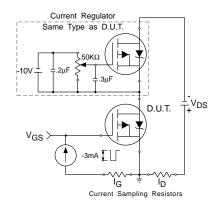


Fig 13b. Gate Charge Test Circuit

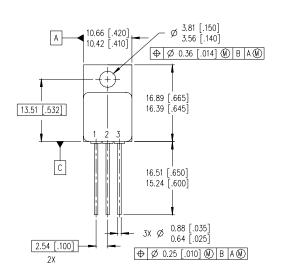


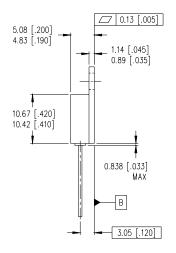
#### **Foot Notes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ②  $V_{DD} = -50V$ , starting  $T_{J} = 25$ °C, L = 15mH Peak  $I_{L} = -9.4$ A,  $V_{GS} = -10V$

- ③ ISD ≤ -9.4A, di/dt ≤ -150A/ $\mu$ s, VDD ≤ -200V, TJ ≤ 150°C
- ④ Pulse width ≤ 300  $\mu$ s; Duty Cycle ≤ 2%

#### Case Outline and Dimensions — TO-257AA



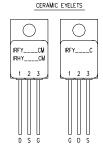


#### NOTES:

- 1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1994.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- 4. OUTLINE CONFORMS TO JEDEC OUTLINE TO-257AA.

<u>LEGEND</u> D – DRAIN

S - SOURCE G - GATE





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