

FRED

HFB50HC20

Ultrafast, Soft Recovery Diode

Features

- Reduced RFI and EMI
- Reduced Snubbing
- Extensive Characterization of Recovery Parameters
- Hermetic

$V_R = 200V$
$I_{F(AV)} = 50A$
$t_{rr} = 35ns$


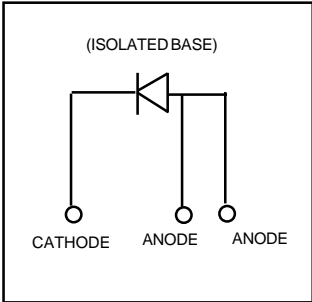
Description

These Ultrafast, soft recovery diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and di/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

Absolute Maximum Ratings

	Parameter	Max.	Units
V_R	Cathode to Anode Voltage	200	V
$I_{F(AV)}$	Continuous Forward Current, ① $T_C = 87^\circ C$	50	A
I_{FSM}	Single Pulse Forward Current, ② $T_C = 25^\circ C$	450	
$P_D @ T_C = 25^\circ C$	Maximum Power Dissipation	167	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ C$

Note: ① D.C. = 50% rect. wave
 ② 1/2 sine wave, 60 Hz , P.W. = 8.33 ms

<p>CASE STYLE</p>  <p>TO-258AA</p>	<p>(ISOLATED BASE)</p>  <p>CATHODE ANODE ANODE</p>
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Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V_{BR}	Cathode Anode Breakdown Voltage	200	—	—	V	$I_R = 100\mu\text{A}$
V_F	Forward Voltage See Fig. 1 ③	—	—	1.34	V	$I_F = 50\text{A}$, $T_J = -55^\circ\text{C}$ ④
		—	—	1.28		$I_F = 50\text{A}$, $T_J = 25^\circ\text{C}$ ④
		—	—	1.7		$I_F = 100\text{A}$, $T_J = 25^\circ\text{C}$ ④
		—	—	1.69		$I_F = 100\text{A}$, $T_J = 125^\circ\text{C}$ ④
I_R	Reverse Leakage Current See Fig. 2 ③	—	—	10	μA	$V_R = V_R$ Rated
		—	—	200	μA	$V_R = V_R$ Rated, $T_J = 125^\circ\text{C}$
C_T	Junction Capacitance, See Fig. 3	—	—	360	pF	$V_R = 200\text{V}$
L_S	Series Inductance	—	8.7	—	nH	Measured from anode lead to cathode lead, 6 mm (0.025 in) from package

Dynamic Recovery Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t_{rr}	Reverse Recovery Time	—	—	35	ns	$I_F = 0.5\text{A}$, $V_R = 30\text{V}$, $di/dt = 300\text{A}/\mu\text{s}$
t_{rr1}	Reverse Recovery Time	—	42	—	ns	$T_J = 25^\circ\text{C}$ See Fig. 5
t_{rr2}		—	69	—		$T_J = 125^\circ\text{C}$
I_{RRM1}	Peak Recovery Current	—	4.4	—	A	$T_J = 25^\circ\text{C}$ See Fig. 6
I_{RRM2}		—	8.7	—		$T_J = 125^\circ\text{C}$
Q_{rr1}	Reverse Recovery Charge	—	108	—	nC	$T_J = 25^\circ\text{C}$ See Fig. 7
Q_{rr2}		—	314	—		$T_J = 125^\circ\text{C}$
$di_{(rec)M}/dt1$	Peak Rate of Fall of Recovery Current During t_b	—	390	—	A/ μs	$T_J = 25^\circ\text{C}$ See Fig. 8
$di_{(rec)M}/dt2$		—	570	—		$T_J = 125^\circ\text{C}$

Thermal - Mechanical Characteristics

	Parameter	Typ.	Max.	Units
R_{thJC}	Junction-to-Case	—	0.75	$^\circ\text{C}/\text{W}$
Wt	Weight	10.9	—	g

Note:

③ Pulse Width < 300 μs , Duty Cycle < 2%

④ Pins 2 and 3 externally tied together

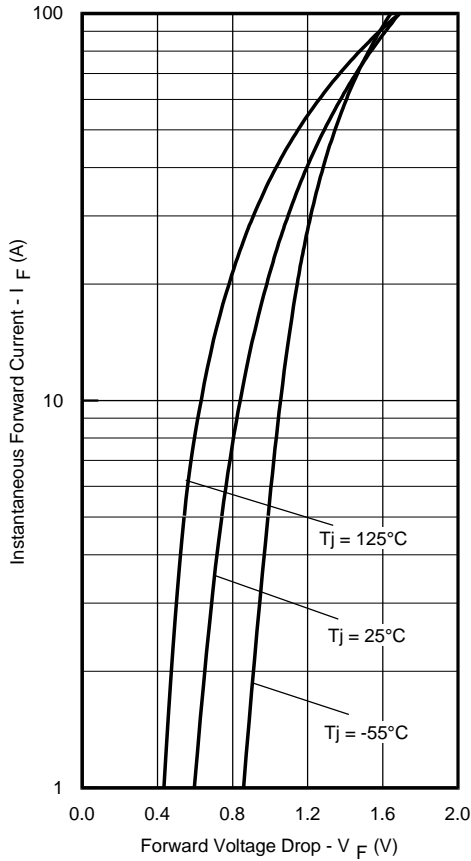


Fig. 1 - Maximum Forward Voltage Drop Vs. Instantaneous Forward Current

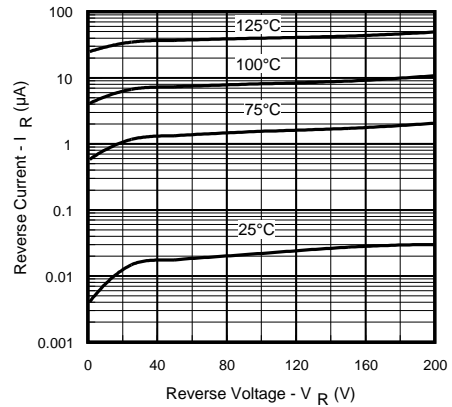


Fig. 2 - Typical Reverse Current Vs. Reverse Voltage

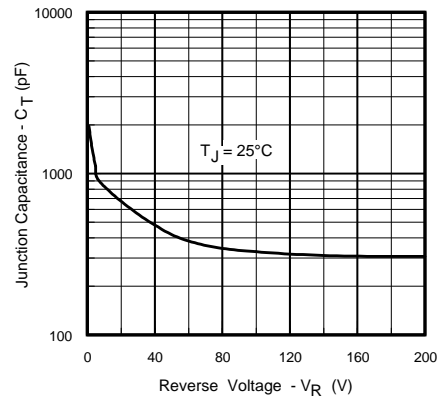


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

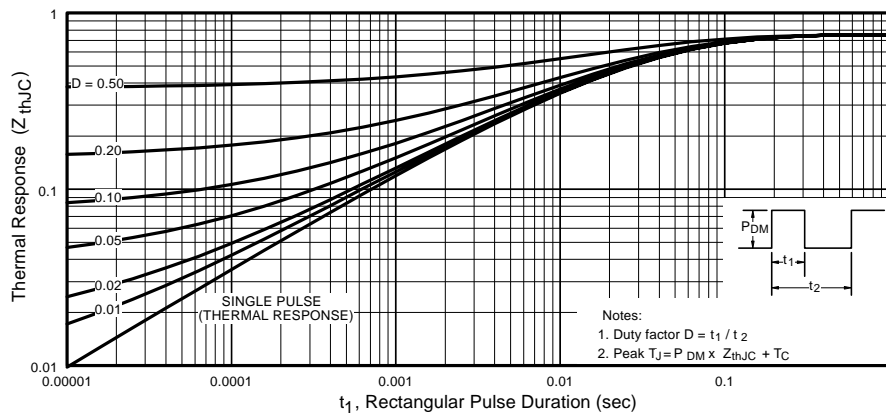


Fig. 4 - Maximum Thermal Impedance Z_{thjC} Characteristics

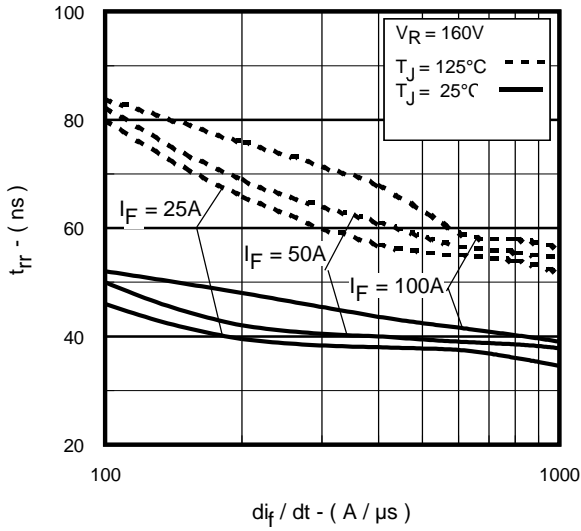


Fig. 5 - Typical Reverse Recovery Vs. di_f/dt ,

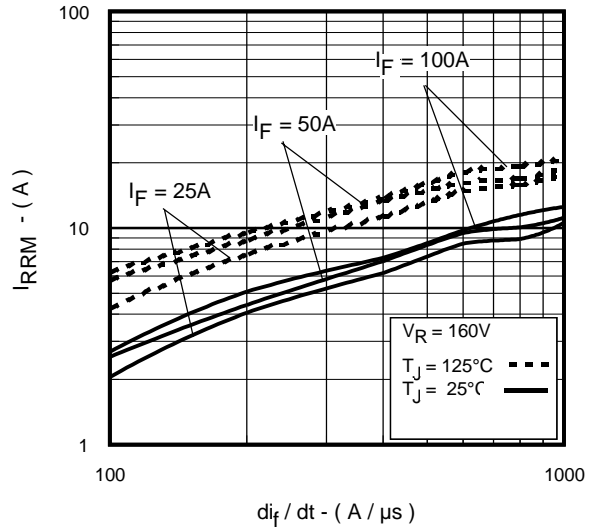


Fig. 6 - Typical Recovery Current Vs. di_f/dt ,

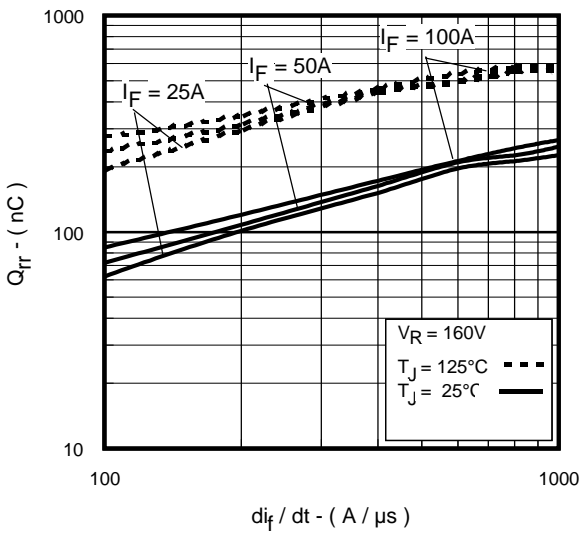


Fig. 7 - Typical Stored Charge Vs. di_f/dt

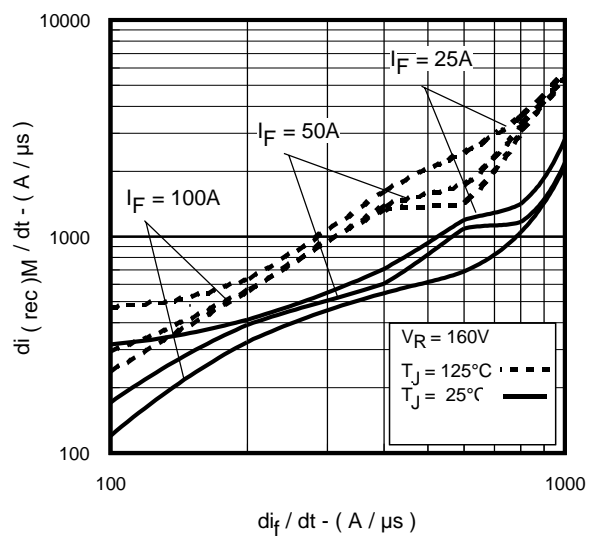
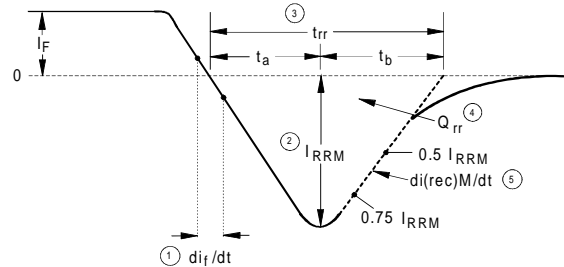
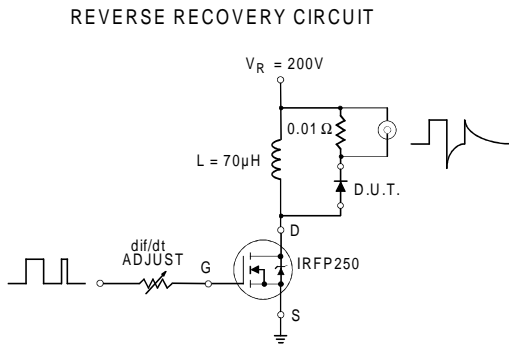


Fig. 8 - Typical $di_{(rec)M}/dt$ Vs. di_f/dt



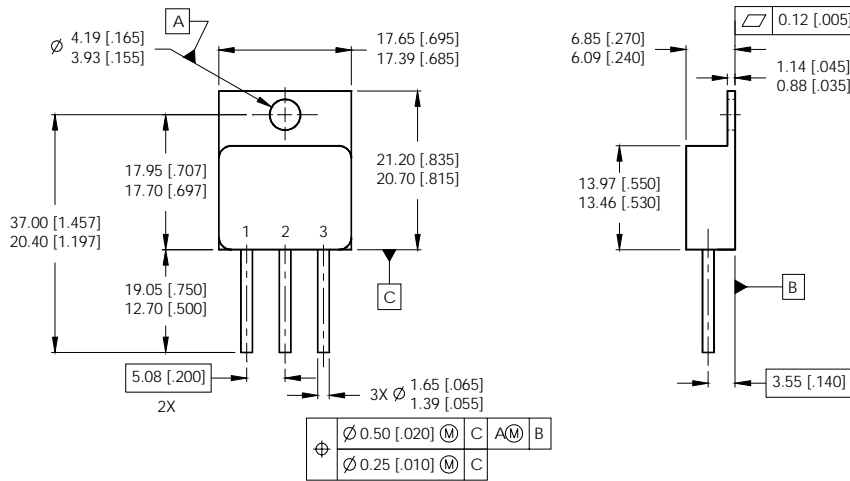
1. di/dt - Rate of change of current through zero crossing
2. I_{RRM} - Peak reverse recovery current
3. t_{rr} - Reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.5 I_{RRM}$ extrapolated to zero current
4. Q_{rr} - Area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$
5. $di_{(rec)M}/dt$ - Peak rate of change of current during t_b portion of t_{rr}

Fig. 9 - Reverse Recovery Parameter Test Circuit

Fig. 10 - Reverse Recovery Waveform and Definitions

Case Outline and Dimensions — TO-258AA



NOTES:

1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-258AA.

PIN ASSIGNMENTS

- 1 = CATHODE
- 2 = ANODE
- 3 = ANODE

单击下面可查看定价，库存，交付和生命周期等信息

[>>Infineon Technologies\(英飞凌\)](#)