

## **Diode EMCON 4 High Power Chip**

### FEATURES:

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1200V EMCON 4 technology •

small temperature coefficient

soft, fast switching • low reverse recovery charge

### This chip is used for:

medium / high power modules •



### **Applications:**

medium / high power drives •

Chip Type	V <sub>R</sub>	I <sub>F</sub>	Die Size	Package
IDC73D120T6H	1200V	150A	8.15 x 9.00 mm <sup>2</sup>	sawn on foil

### **MECHANICAL PARAMETER:**

Raster size	8.15 x 9.00				
Area total / active	73.35 / 59.89				
Anode pad size	7.196 x 8.046				
Thickness	120	μm			
Wafer size	150	mm			
Flat position	180	deg			
Max. possible chips per wafer	187 pcs				
Passivation frontside	Photoimide				
Pad metall	3200 nm AlSiCu				
Backside metall	Ni Ag –system suitable for epoxy and soft solder die bondi				
Die bond	electrically conductive glue or solder				
Wire bond	AI, ≤500µm				
Reject ink dot size	Ø 0.65mm; max 1.2mm				
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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### **Maximum Ratings**

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V <sub>RRM</sub>		1200	V
Continuous forward current limited by	1_		1)	
T <sub>jmax</sub>	1 <sub>F</sub>			А
Maximum repetitive forward current	1		300	~
limited by T <sub>jmax</sub>	I <sub>FRM</sub>		300	
Maximum junction and storage temperature	T <sub>vj,max</sub> , T <sub>stg</sub>		-40+175	°C
Reverse bias safe operating area <sup>2)</sup> (RBSOA)	$I_{\rm F,max} = 30$	00A, $V_{\rm R,max}$ = 1200V, $T_{\rm vj,op} \le 150^{\circ}$	$PC, P_{max} = $ tbd kV	V

<sup>1)</sup> depending on thermal properties of assembly

<sup>2)</sup> not subject to production test - verified by design/characterisation

## Static Electrical Characteristics (tested on wafer), $T_i=25$ °C

Parameter	Symbol	Condi	Value			Unit	
Falametei	Symbol	Cond	litons	min.	Тур.	max.	Unit
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =1200V	<i>T<sub>j</sub></i> =25 ° <i>C</i>			26	μΑ
Cathode-Anode breakdown Voltage	V <sub>Br</sub>	I <sub>R</sub> =0.25mA	<i>T<sub>j</sub></i> =25°C	1200			V
Forward voltage drop	V <sub>F</sub>	I <sub>F</sub> =150A	<i>T<sub>j</sub></i> =25 ° <i>C</i>	1.55	1.9	2.25	V

## **Dynamic Electrical Characteristics** inductive load (not subject to production test - verified by design/characterization)

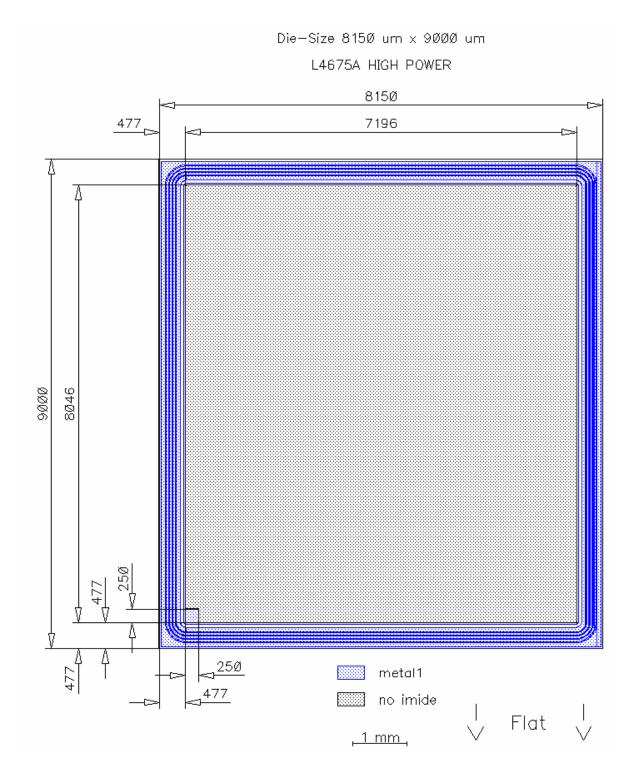
Parameter	Symbol	Conditions		Value <sup>2)</sup>			Unit
Falallelel	Symbol			min. Typ. r		max.	
Peak reverse recovery current	/ <sub>RM</sub>	$I_{F}=A$ di/dt=A/ms $V_{R}=V$ $V_{GE}=-15V$	$T_j = 25 \ ^{\circ}C$ $T_j = 125 \ ^{\circ}C$ $T_j = 150 \ ^{\circ}C$		tbd		A
Reverse recovery charge	Qr	$I_{F}=A$ di/dt=A/ms $V_{R}=V$ $V_{GE}=-15V$	$T_j = 25 \ ^{\circ}C$ $T_j = 125 \ ^{\circ}C$ $T_j = 150 \ ^{\circ}C$		tbd		μC
Reverse recovery energy	E <sub>rec</sub>	$I_F = A$ di/dt = A/ms $V_R = V$ $V_{GE} = -15V$	$T_j = 25 \ ^{\circ}C$ $T_j = 125 \ ^{\circ}C$ $T_j = 150 \ ^{\circ}C$		tbd		mJ

<sup>2)</sup> values also influenced by parasitic L- and C- in measurement and package.

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**CHIP DRAWING:** 



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### FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet

tbd

### **Description:**

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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