

Low Drop Voltage Tracker

TLE 4251



Features

- Output tracking tolerance $\leq \pm 0.2\%$
- 400 mA output current capability
- Enable Function
- Very low current consumption in OFF mode
- Wide operation range: up to 40 V
- Wide temperature range: -40 °C $\leq T_{i} \leq$ 150 °C
- Output protected against short circuit
- Overtemperature protection
- Reverse polarity proof
- Green Product (RoHS compliant)
- AEC Qualified

Functional Description

The **TLE 4251** is a monolithic integrated low drop voltage tracker in the very small SMD package PG-TO252-5-1. It is designed to supply e.g. sensors under the severe

conditions of automotive applications. Therefore the

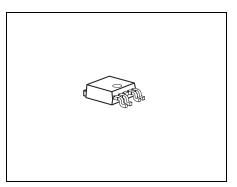
device is equipped with additional protection functions against overload, short circuit and reverse polarity.

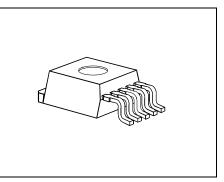
Supply voltages up to 40 V are tracked to a reference voltage given to the adjust input via an external resistor.

The output is able to drive loads up to 400 mA while it follows e.g. the 5 V output of a main voltage regulator within an accuracy of 0.5%. For loads up to 300 mA the tracking accuracy is 0.2%.

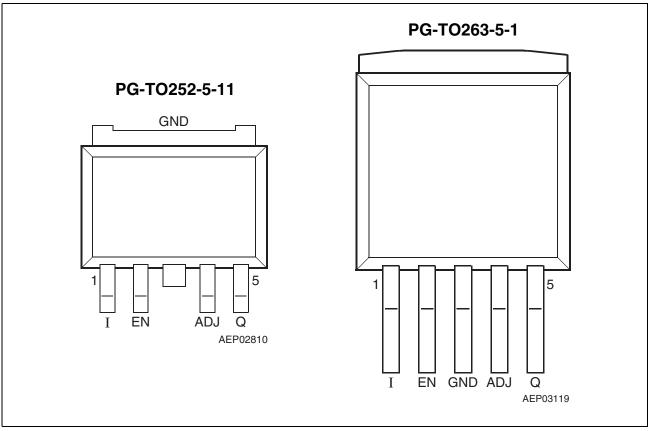
The **TLE 4251** can be switched in stand-by mode via the enable EN input which causes the current consumption to drop to very low values. This feature makes the IC suitable for low power battery applications.

| Туре | Package |
|------------|---------------|
| TLE 4251 D | PG-TO252-5-11 |
| TLE 4251 G | PG-TO263-5-1 |









| Figure 1 | Pin Configuration (top view) |
|----------|------------------------------|
|----------|------------------------------|

Table 1Pin Definitions and Functions

| Pin No. | Symbol | Function |
|---------|--------|--|
| 1 | I | Input voltage |
| 2 | EN | Enable; high-active input |
| 3 | GND | Ground |
| 4 | ADJ | Adjust; connect directly to the reference or with a voltage divider to the reference (for reference-proportional output voltages, VQ <vref)< td=""></vref)<> |
| 5 | Q | Output voltage; must be blocked by a capacitor $C_Q \ge 22 \ \mu\text{F}$, ESR $\le 3 \ \Omega$ to GND |





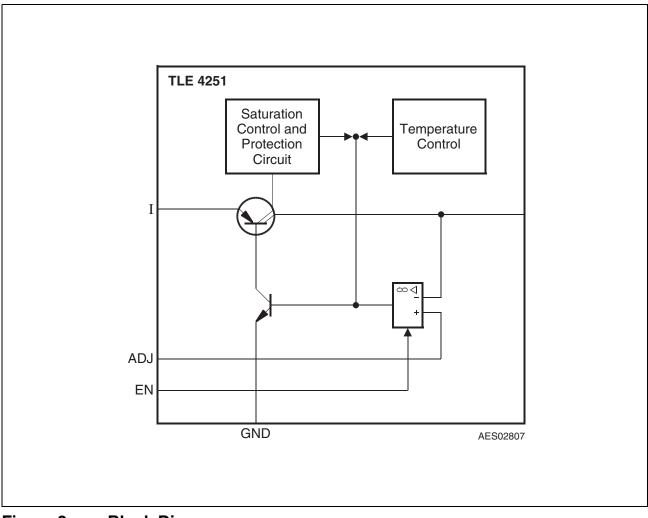


Figure 2 Block Diagram



Table 2 Absolute Maximum Ratings

-40 °C < $T_{\rm j}$ < 150 °C

| Parameter | Symbol | Limit Values | | Unit | Remarks |
|----------------------|-------------------|--------------|------|------|--------------------------|
| | | Min. | Max. | | |
| Input | | | | | |
| Voltage | V_1 | -42 | 45 | V | - |
| Current | I | _ | _ | mA | internally limited |
| Output | | | | | |
| Voltage | V_{Q} | -2 | 45 | V | - |
| Current | IQ | - | - | mA | internally limited |
| Adjust | · | • | | · | • |
| Voltage | V_{ADJ} | -42 | 45 | V | - |
| Current | I _{ADJ} | - | - | μA | internally limited |
| Enable | · | | | | · |
| Voltage | $V_{\sf EN}$ | -42 | 45 | V | - |
| Current | I _{EN} | - | _ | μA | internally limited |
| Temperatures | · | | | | · |
| Junction temperature | Tj | -40 | 150 | °C | - |
| Storage temperature | T _{stg} | -50 | 150 | °C | - |
| Thermal Resistances | · | • | | · | • |
| Junction case | $R_{ m thjc}$ | _ | 4 | K/W | TLE 4251 D |
| Junction ambient | $R_{ m thja}$ | - | 78 | K/W | TLE 4251 D ¹⁾ |
| Junction case | R _{thjc} | _ | 3 | K/W | TLE 4251 G |
| Junction ambient | $R_{ m thja}$ | _ | 52 | K/W | TLE 4251 G ¹⁾ |

1) Worst case, regarding peak temperature; zero airflow; mounted an a PCB $80 \times 80 \times 1.5 \text{ mm}^3$, heat sink area 300 mm².

Note: Maximum ratings are absolute ratings; exceeding any one of these values may cause irreversible damage to the integrated circuit.



Table 3Operating Range

| Parameter | Symbol | Limit | Values | Unit | Remarks |
|----------------------|----------------|-----------------|--------|------|--|
| | | Min. | Max. | | |
| Input voltage | V_{I} | 4 ¹⁾ | 40 | V | - |
| Adjust input voltage | V_{ADJ} | 2.5 | 40 | V | - |
| Adjust input voltage | V_{ADJ} | 0 | 2.5 | V | $V_{\rm Q} \le V_{\rm ADJ} + \Delta V_{\rm Q}$ |
| Enable input voltage | V_{EN} | 0 | 40 | V | - |
| Junction temperature | T _j | -40 | 150 | °C | - |

1) $V_{\rm I} > V_{\rm ADJ} + V_{\rm DR}$



Table 4 Electrical Characteristics

 $V_{\rm I}$ = 13.5 V; 2.5 V $\leq V_{\rm ADJ} \leq V_{\rm I}$ - 0.5 V; -40 °C < $T_{\rm j}$ < 150 °C; unless otherwise specified

| Parameter | Symbol | Limit Values | | | Unit | Test Condition |
|---|------------------|--------------|------|------|------|---|
| | | Min. | Тур. | Max. | | |
| Output | L | 1 | | | 1 | |
| Output voltage tracking accuracy $\Delta V_{\rm Q} = V_{\rm ADJ} - V_{\rm Q}$ | ΔV_{Q} | -10 | _ | 10 | mV | $V_{\rm I}$ < 13.5 V; -40 °C < $T_{\rm j}$ < 125 °C; 1 mA < $I_{\rm Q}$ < 300 mA |
| Output voltage tracking accuracy | ΔV_{Q} | -10 | - | 10 | mV | 6 V < V _I < 40 V; 5 mA < I _Q < 200 mA |
| Output voltage tracking accuracy | ΔV_{Q} | -25 | - | 25 | mV | 6 V < V _I < 28 V; 1 mA < I _Q < 300 mA |
| Drop voltage | V _{dr} | - | 280 | 520 | mV | $I_{\rm Q}$ = 300 mA; $V_{\rm ADJ}$ > 4 V; Enable ON ¹⁾ |
| Output current | IQ | 400 | 450 | 800 | mA | $T_{\rm j} \le 125 \ {}^{\circ}{ m C}^{1)}$ |
| Output capacitor | CQ | 22 | - | - | μF | ESR \leq 3 Ω at 10 kHz |
| Current consumption $I_q = I_1 - I_Q$ | Iq | - | 10 | 20 | mA | <i>I</i> _Q = 300 mA |
| Current consumption $I_q = I_1 - I_Q$ | Iq | - | 230 | 300 | μA | $I_{\rm Q}$ < 1 mA; $T_{\rm j}$ < 85 °C; $V_{\rm EN}$ in ON state |
| Quiescent current(stand-by) $I_q = I_1 - I_Q$ | Iq | - | 0 | 2 | μA | $V_{\rm EN}$ = 0 V; $T_{\rm j}$ < 85 °C |
| Regulator Performance |) | | | | | |
| Load regulation | ΔV_{Q} | -35 | ±5 | 35 | mV | $5 \text{ mA} < I_{\text{Q}} < 300 \text{ mA};$ $V_{\text{I}} = 6 \text{ V}; V_{\text{ADJ}} = 5 \text{ V}$ |
| Line regulation | ΔV_{Q} | -25 | ±10 | 25 | mV | 12 V < V_1 < 32 V; I_Q = 5 mA |
| Power Supply Ripple Rejection | PSRR | 60 | - | _ | dB | $f_{\rm r}$ = 100 Hz; $V_{\rm r}$ = 0.5 Vpp; $V_{\rm ADJ}$ = 5 V; $C_{\rm Q}$ = 22 µF Tantalum |
| Adjust Input | | | | | | _ |
| Input biasing current | I _{ADJ} | _ | 0.1 | 0.5 | μA | $V_{ADJ} = 5 \text{ V}$ |



Table 4Electrical Characteristics (cont'd)

 $V_{\rm I}$ = 13.5 V; 2.5 V $\leq V_{\rm ADJ} \leq V_{\rm I}$ - 0.5 V; -40 °C < $T_{\rm j}$ < 150 °C; unless otherwise specified

| Parameter | Symbol | Limit Values | | | Unit | Test Condition |
|--------------------------|-------------------|--------------|------|------|------|------------------------------|
| | | Min. | Тур. | Max. | | |
| Enable | • | | | | | |
| Enable on voltage range | $V_{\rm EN ON}$ | 2 | _ | _ | V | V _Q ON |
| Enable off voltage range | $V_{\rm EN OFF}$ | _ | _ | 0.5 | V | $V_{\rm Q} \le 0.1 \ { m V}$ |
| Input current | I _{EN} | 5 | 40 | 70 | μA | $V_{\rm EN}$ = 5 V |

1) Measured when the output voltage $V_{\rm Q}$ has dropped 100 mV from the nominal value.

Application Information

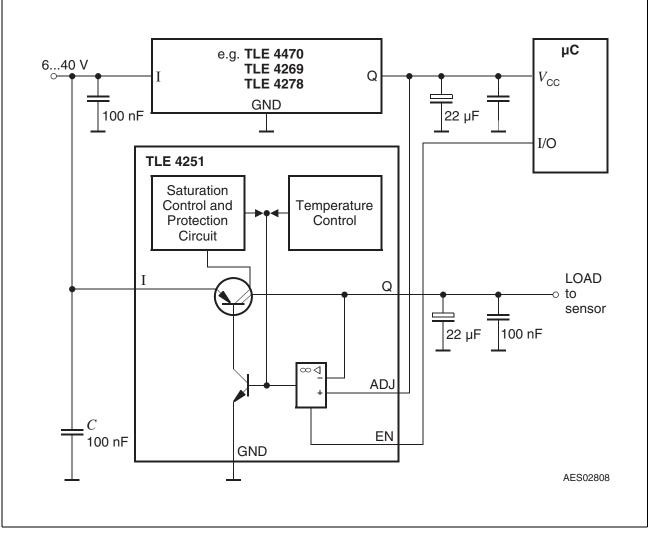
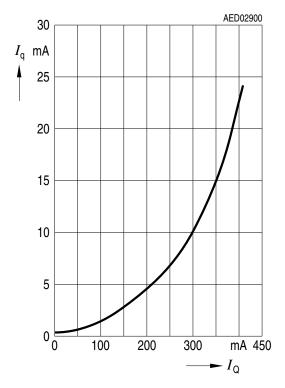


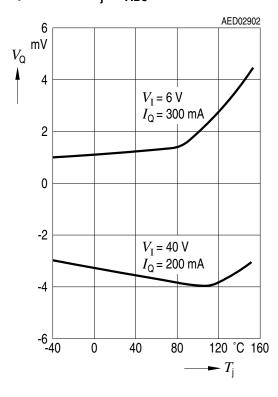
Figure 3 Application Circuit



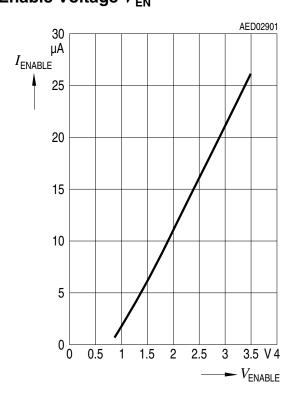
Quiescent Current I_q versus Output Current I_Q



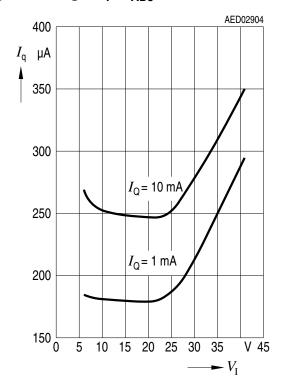
Tracking Accuracy ΔV_{Q} versus Temperature T_{j} , V_{ADJ} = 5 V



Enable Current $I_{\rm EN}$ versus Enable Voltage $V_{\rm EN}$

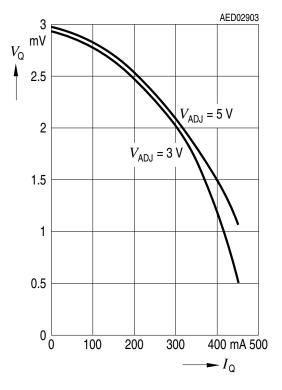


Current Consumption I_q versus Input Voltage V_l , $V_{ADJ} = 5$ V



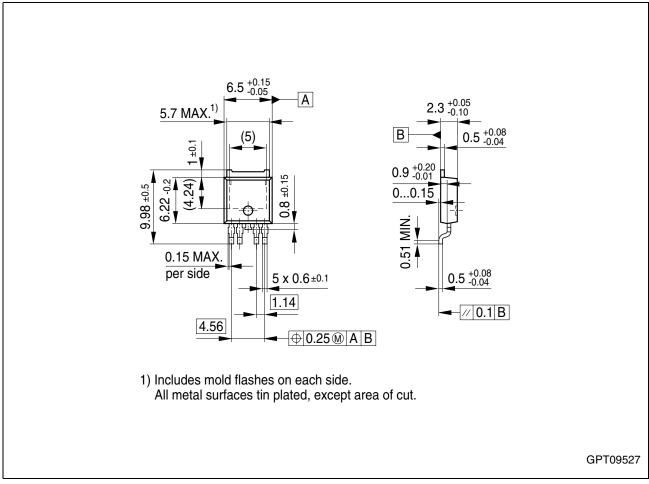


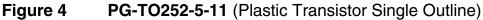
Tracking Accuracy $\Delta V_{\rm Q}$ versus Output Current $I_{\rm Q}$





Package Outlines





Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

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SMD = Surface Mounted Device

Dimensions in mm



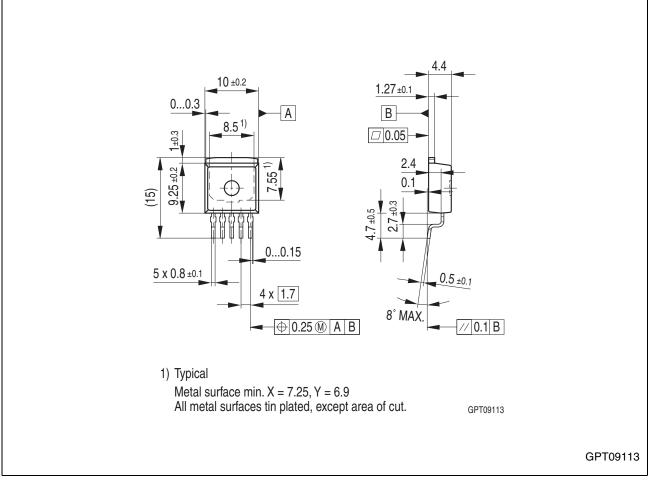


Figure 5 PG-TO263-5-1 (Plastic Transistor Single Outline)

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Revision History

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| Version | Date | Changes |
|----------|------------|---|
| Rev. 2.9 | 2007-03-20 | Initial version of RoHS-compliant derivate of TLE 4251 Page 1: AEC certified statement added Page 1 and Page 10: RoHS compliance statement and Green product feature added Page 1 and Page 10: Package changed to RoHS compliant version Legal Disclaimer updated |
| | | |

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