

AUIRFN7107

Features

- Advanced Process Technology
- Ultra Low On-Resistance
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified *

Description

Specifically designed for Automotive applications, this HEXFET[®] Power MOSFET utilizes the latest processing techniques to achieve extremely low on-resistance per silicon are. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this product an extremely efficient and reliable device for use in Automotive and wide variety of other applications.

Applications

- Injection
- Heavy Loads
- DC-DC Converter

Base Part Number	Packago Typo	Standard	Complete Part Number	
Base Fait Nulliber	Part Number Package Type Fo		Quantity	Complete Fait Number
AUIRFN7107	PQFN 5mm x 6mm	Tape and Reel	4000	AUIRFN7107TR

Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature (TA) is 25°C, unless otherwise specified.

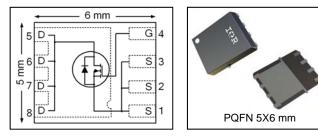
	Parameter	Max.	Units	
V _{DS}	Drain-to-Source Voltage	75	V	
I _D @ T _A = 25°C	Continuous Drain Current, V _{GS} @ 10V	14		
I _D @ T _A = 70°C	Continuous Drain Current, V _{GS} @ 10V	12		
I _D @ T _{C(Bottom)} = 25°C	Continuous Drain Current, V _{GS} @ 10V	756	А	
I _D @ T _{C(Bottom)} = 100°C	Continuous Drain Current, V _{GS} @ 10V	536		
I _{DM}	Pulsed Drain Current ①	300		
P _D @T _A = 25°C	Power Dissipation	4.4	10/	
$P_D @T_{C(Bottom)} = 25^{\circ}C$	Power Dissipation	125	- W	
	Linear Derating Factor	0.029	W/°C	
V _{GS}	Gate-to-Source Voltage	± 20	V	
E _{AS}	Single Pulse Avalanche Energy ②	123	mJ	
I _{AR}	Avalanche Current ①	45	A	
TJ	Operating Junction and	-55 to + 175	°C	
T _{STG}	Storage Temperature Range		°C	

HEXFET® is a registered trademark of Infineon.

*Qualification standards can be found at <u>www.infineon.com</u>

HEXFET[®] POWER MOSFET

V _{DSS}	75V
R_{DS(on)} max (@V _{GS} = 10V)	8.5mΩ
Q _{G (typical)}	51nC
Ι _D (@Τ _{C (Bottom)} = 25°C)	75A



G	D	S
Gate	Drain	Source



Thermal Resistance

Symbol	Parameter	Тур.	Max.	Units
R _{0JC} (Bottom)	Junction-to-Case ④		1.2	
R _{θJC} (Top)	Junction-to-Case ④		27	0 0 AA
$R_{ ext{ heta}JA}$	Junction-to-Ambient (5)		34	°C/W
R _{0JA} (<10s)	Junction-to-Ambient (5)		22	

Static Electrical Characteristics $@T_J = 25^{\circ}C$ (unless otherwise specified)

Parameter	Min.	Тур.	Max.	Units	Conditions
Drain-to-Source Breakdown Voltage	75			V	$V_{GS} = 0V, I_{D} = 250\mu A$
Breakdown Voltage Temp. Coefficient		0.074		V/°C	Reference to 25°C, $I_D = 1.0$ mA
Static Drain-to-Source On-Resistance		6.9	8.5	mΩ	V _{GS} = 10V, I _D = 45A ③
Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}, I_D = 100 \mu A$
Internal Gate Resistance		0.82		Ω	
Forward Transconductance	73			S	$V_{DS} = 25V, I_{D} = 45A$
Drain to Source Leokere Current			20		$V_{DS} = 75V, V_{GS} = 0V$
Drain-to-Source Leakage Current			250	μΑ	$V_{DS} = 75V, V_{GS} = 0V, T_{J} = 125^{\circ}C$
Gate-to-Source Forward Leakage			100		$V_{GS} = 20V$
<u> </u>			-100	nA	V _{GS} = -20V
	Drain-to-Source Breakdown Voltage Breakdown Voltage Temp. Coefficient Static Drain-to-Source On-Resistance Gate Threshold Voltage Internal Gate Resistance Forward Transconductance Drain-to-Source Leakage Current	Drain-to-Source Breakdown Voltage 75 Breakdown Voltage Temp. Coefficient — Static Drain-to-Source On-Resistance — Gate Threshold Voltage 2.0 Internal Gate Resistance — Forward Transconductance 73 Drain-to-Source Leakage Current — Gate-to-Source Forward Leakage —	Drain-to-Source Breakdown Voltage75Breakdown Voltage Temp. CoefficientStatic Drain-to-Source On-ResistanceGate Threshold Voltage2.0Internal Gate ResistanceForward Transconductance73Drain-to-Source Leakage CurrentGate-to-Source Forward Leakage	Drain-to-Source Breakdown Voltage75—Breakdown Voltage Temp. Coefficient—0.074Static Drain-to-Source On-Resistance—6.98.5Gate Threshold Voltage2.0—4.0Internal Gate Resistance—0.82—Forward Transconductance73—20Drain-to-Source Leakage Current—20Gate-to-Source Forward Leakage—100	Drain-to-Source Breakdown Voltage75—VBreakdown Voltage Temp. Coefficient— 0.074 — $V/^{\circ}C$ Static Drain-to-Source On-Resistance— 6.9 8.5 $m\Omega$ Gate Threshold Voltage 2.0 — 4.0 VInternal Gate Resistance— 0.82 — Ω Forward Transconductance73—SDrain-to-Source Leakage Current— 250 μA Gate-to-Source Forward Leakage—— 100

Dynamic Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Q_{g}	Total Gate Charge		51	77		I _D = 45A
Q_{gs}	Gate-to-Source Charge		15			$V_{DS} = 38V$
Q_{gd}	Gate-to-Drain ("Miller") Charge		14		nC	$V_{GS} = 10V$
Q _{sync}	Total Gate Charge Sync. (Q _g - Q _{gd})		37			$I_{D} = 45A, V_{DS} = 0V, V_{GS} = 10V$
t _{d(on)}	Turn-On Delay Time		8.0			$V_{DD} = 75V$
t _r	Rise Time		12		20	I _D = 45A
t _{d(off)}	Turn-Off Delay Time		19		ns	$R_{G} = 1.8\Omega$
t _f	Fall Time		7.0			V _{GS} = 10V ③
C _{iss}	Input Capacitance		3001			$V_{GS} = 0V$
C _{oss}	Output Capacitance		371		pF	$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		151			<i>f</i> = 1.0 MHz

Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
1	Continuous Source Current			75	۸	MOSFET symbol
I _S	(Body Diode)			75	A	showing the
	Pulsed Source Current			200	۸	integral reverse 식다
I _{SM}	(Body Diode) ①			— 300	A	p-n junction diode.
V _{SD}	Diode Forward Voltage		0.85	1.3	V	$T_J = 25^{\circ}C, I_S = 45A, V_{GS} = 0V$ (3)
t _{rr}	Reverse Recovery Time		28		ns	$T_J = 25^{\circ}C, I_F = 45A, V_{DD} = 38V$
Q _{rr}	Reverse Recovery Charge		145		nC	di/dt = 500A/µs

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^{\circ}C$, L =0.12mH, $R_G = 50\Omega$, $I_{AS} = 45A$.
- 3 Pulse width \leq 400µs; duty cycle \leq 2%.
- ④ R_{θ} is measured at TJ of approximately 90°C.
- S When mounted on 1" square PCB (FR-4 or G-10 Material). For recommended footprint and soldering techniques refer to application note #AN-994: http://www.irf.com/technical-info/appnotes/an-994.pdf
- © Calculated continuous current based on maximum allowable junction temperature.

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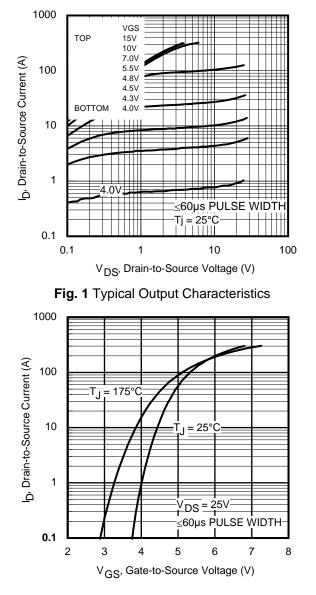


Fig. 3 Typical Transfer Characteristics

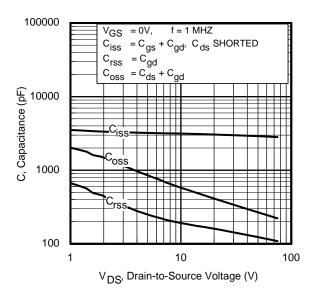


Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

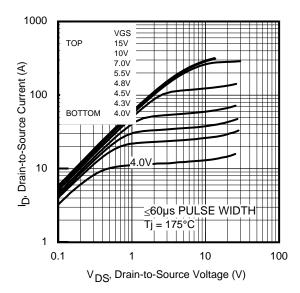


Fig. 2 Typical Output Characteristics

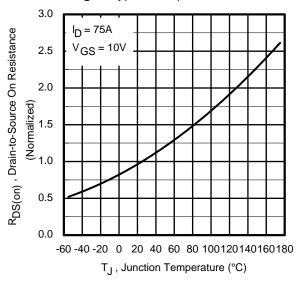
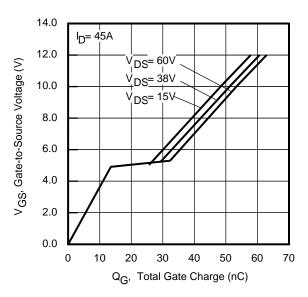
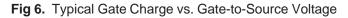


Fig. 4 Normalized On-Resistance vs. Temperature







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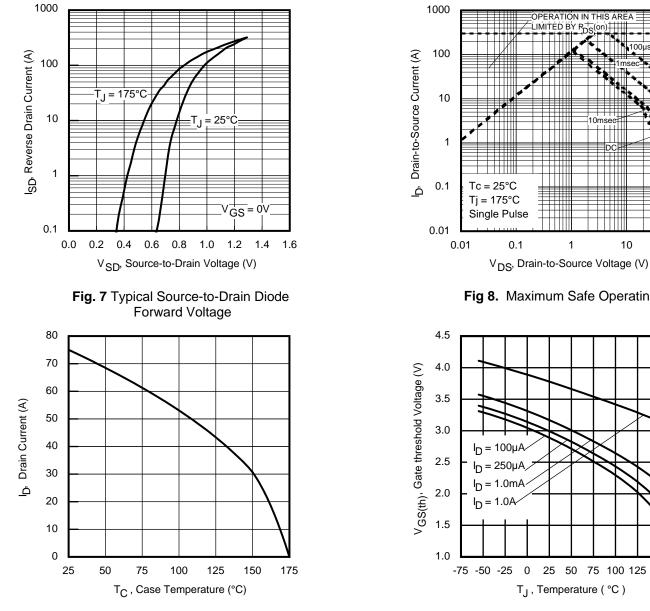
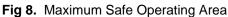


Fig 9. Maximum Drain Current vs. Case Temperature



1

0mse

≣DC

10

100

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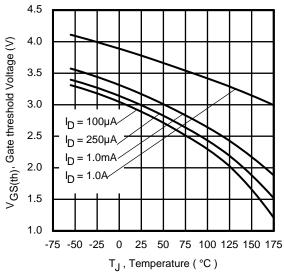


Fig 10. Threshold Voltage vs. Temperature

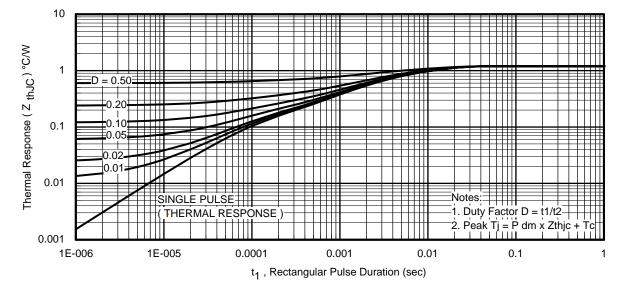


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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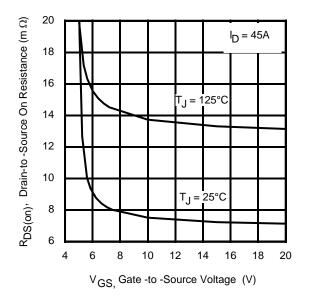


Fig 12. Typical On-Resistance vs. Gate Voltage

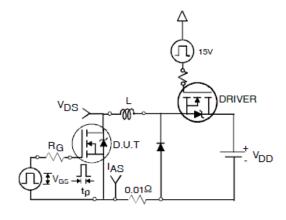


Fig 14a. Unclamped Inductive Test Circuit

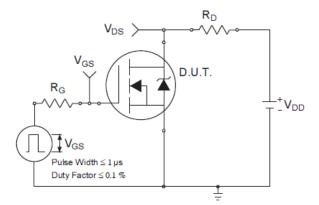


Fig 15a. Switching Time Test Circuit

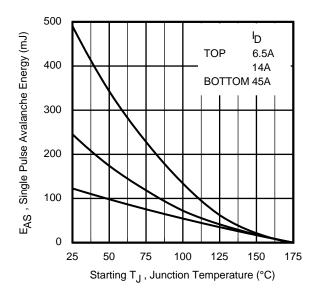


Fig 13. Maximum Avalanche Energy vs. Drain Current

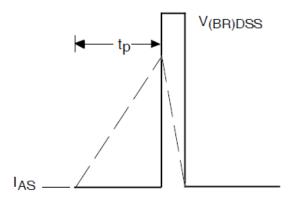


Fig 14b. Unclamped Inductive Waveforms

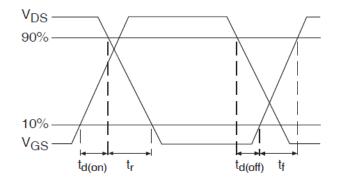


Fig 15b. Switching Time Waveforms

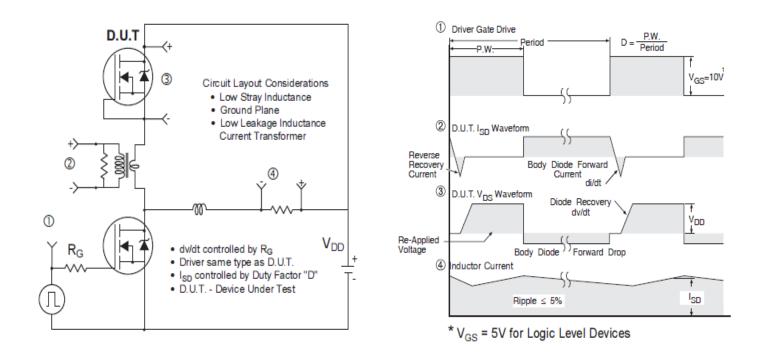


Fig 16. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs

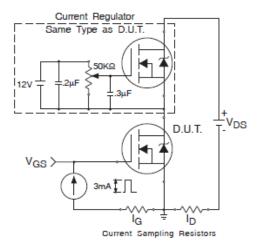


Fig 17a. Gate Charge Test Circuit

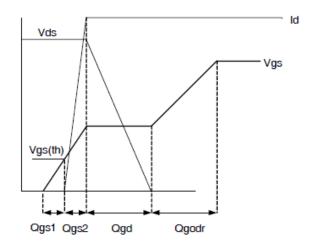
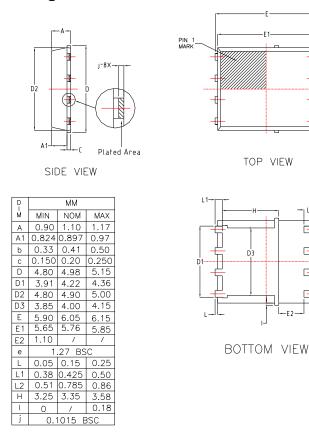


Fig 17b. Gate Charge Waveform

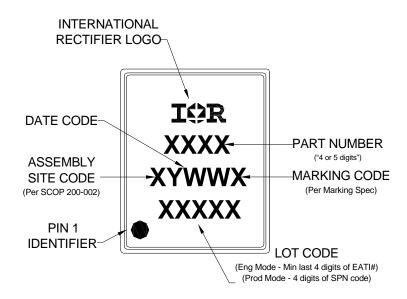
PQFN 5x6 Outline "E" Package Details



For more information on board mounting, including footprint and stencil recommendation, please refer to application note AN-1136: <u>http://www.irf.com/technical-info/appnotes/an-1136.pdf</u>

For more information on package inspection techniques, please refer to application note AN-1154: <u>http://www.irf.com/technical-info/appnotes/an-1154.pdf</u>

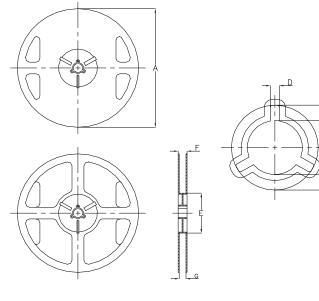
PQFN 5x6 Outline "E" Part Marking



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

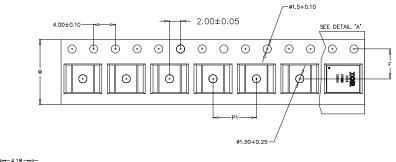


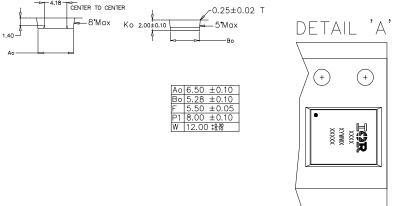
PQFN 5x6 Outline "E" Tape and Reel



NOTE: Controlling dimensions in mm Std reel quantity is 4000 parts.

	RE	EL DIME	NSIONS	
S	TANDAR	D OPTIO	N (QTY 40	100) TR
	M	ETRIC	IMP	ERIAL
CODE	MIN	MAX	MIN	MAX
A	329.5	330.5	12.972	13.011
В	20.9	21.5	0.823	0.846
С	12.8	13.5	0.504	0.532
D	1.7	2.3	0.067	0.091
Е	97	99	3.819	3.898
F	Ref	17.4		·
G	13	14.5	0.512	0.571





Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

Qualification Information

		Automotive				
		(per AEC-Q101)				
Qualification Level		Comments: This part number(s) passed Automotive qualification. Infineon's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.				
Moisture Sensitivity Level		PQFN 5x6 MSL1				
	Human Body Model	Class H1C (+/- 2000V) [†]				
		AEC-Q101-001				
ESD	Charged Device Model	Class C5 (+/- 2000V) [†]				
		AEC-Q101-005				
RoHS Compliant		Yes				

† Highest passing voltage.

Revision History

Date	Comments
10/12/2015	Updated datasheet with corporate template

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