

6th Generation CoolSiC™

650V SiC Schottky Diode

The CoolSiCTM generation 6 (G6) is the leading edge technology from Infineon for the SiC Schottky barrier diodes. The Infineon proprietary innovative G5 technology was enhanced in G6 by introducing further advancements like a novel Schottky metal system. The result is a family of products with improved efficiency over all load conditions, resulting from a lower figure of merit ($Q_c \times V_F$). The CoolSiCTM Schottky diode 650 V G6 has been designed to complement our 600 V and 650 V CoolMOSTM 7 families, meeting the most stringent application requirements in this voltage range.

Table 1 Key performance parameters

Parameter	Value	Unit				
V_{RRM}	650	V				
$\overline{Q_C (V_R = 400 \text{ V})}$	6.9	nC				
$\overline{E_C (V_R = 400 \text{ V})}$	1.1	μJ				
$I_F (T_C \le 150 ^{\circ}\text{C}, D = 1)$	4	Α				
$V_F (I_F = 4 \text{ A}, T_j = 25 \text{ °C})$	1.25	V				

Table 2 Package info	rmation
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Type / ordering Code	Package	Marking
IDH04G65C6	PG-TO220-2	D0465C6

PG-TO220-2 CASE 1) Cathode 2) Anode 1 O O CASE

Features

- Best in class forward voltage (1.25 V)
- Best in class figure of merit $(Q_c \times V_F)$
- High dv/dt ruggedness (150 V/ns)

Benefits

- System efficiency improvement
- System cost and size savings due to the reduced cooling requirements
- Enabling higher frequency and increased power density

Potential Applications

- · Power factor correction in SMPS
- Solar inverter
- Uninterruptible power supply

Product Validation

Qualified for industrial applications according to the relevant tests of JEDEC (J-STD20 and JESD22)



Please read the Important Notice and Warnings at the end of this document

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IDH04G65C6



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1 Maximum ratings

Table 3 Maximum ratings

Davamatar	Complete	Values			l lmit	N /	
Parameter	Symbol	Min.	Тур. Мах.		Unit	Note/Test condition	
		_	_	4		$T_C \le 150 ^{\circ}\text{C}, D = 1$	
Continuous forward current	I_F	-	_	7		$T_C \le 125 ^{\circ}\text{C}, D = 1$	
		_	-	12		$T_C \le 25 ^{\circ}\text{C}, D = 1$	
Surge-repetitive forward current, sine halfwave ¹	$I_{F,RM}$	-	-	18	A	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Surge non-repetitive forward	,	_	_	29		$T_C = 25$ °C, $t_p = 10$ ms	
current, sine halfwave	$I_{F,SM}$	_	_	23		$T_C = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Non-repetitive peak forward current	I _{F,max}	-	-	250		T_C = 25 °C, t_ρ = 10 μs	
:24 1	∫i²dt	_	_	4.3	A 2 -	$T_C = 25$ °C, $t_p = 10$ ms	
i ² t value		_	_	2.7	H A ² s	$T_C = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}$	
Repetitive peak reverse voltage	V_{RRM}	_	_	650	٧	<i>T_C</i> = 25 °C	
Diode dv/dt ruggedness	dv/dt	-	_	150	V/ns	V _R = 0480 V	
Power dissipation	P _{tot}	-	_	45	W	$T_C = 25$ °C, $R_{thJC,max}$	
Operating and storage temperature	T_j T_{stg}	-55	-	175	°C	-	
Mounting torque	_	_	_	70	Ncm	M3 screw	

2 Thermal characteristics

Table 4 Thermal characteristics (PG-TO-220-2)

Parameter	Cymphol	Values			l lm!s	Note/Test son dition
	Symbol	Min.	Тур.	Max.	Unit	Note/Test condition
Thermal resistance, junction- case	R_{thJC}	-	2.0	3.4	12 /\AI	_
Thermal resistance, junctionambient	R_{thJA}	_	_	62	K/W	leaded
Soldering temperature, wavesoldering only allowed at leads	T_{sold}	-	-	260	°C	1.6 mm (0.063 in.) from case for 10 s

¹ The surge-repetitive forward current test was performed with 1000 pulses (half-wave rectified sine with the 10 ms period).

Final Datasheet

3 Rev. 2.0, 2017-05-23



3 Electrical characteristics

3.1 Static characteristics

 Table 5
 Static characteristics

Parameter	Symbol	Values			l los!4	Nata/Tast association
		Min.	Тур.	Max.	Unit	Note/Test condition
DC blocking voltage	V_{DC}	650	_	_		<i>T_j</i> = 25 °C
Diode forward voltage	V_F	_	1.25	1.35	V	$I_F = 4 \text{ A}, T_j = 25 ^{\circ}\text{C}$
		_	1.5	_		$I_F = 4 \text{ A}, T_j = 150 \text{ °C}$
Reverse current	I_R	_	0.4	14	μΑ	$V_R = 420 \text{ V}, T_j = 25 \text{ °C}$
		_	13	_		V_R = 420 V, T_j = 125 °C
		_	31	_		$V_R = 420 \text{ V}, T_j = 150 \text{ °C}$

3.2 AC characteristics

Table 6 AC characteristics

Parameter	Symbol	Values			l lmit	Note/Test Condition
		Min.	Тур.	Max.	Unit	Note/Test Condition
Total capacitive charge	Q_c	-	6.9	-	nC	$V_R = 400 \text{ V}, T_j = 150 \text{ °C},$ di/dt = 200 A/µs, I _F ≤ I _{F,MAX}
Total capacitance	С	_	205	_	pF	$V_R = 1 \text{ V, } f = 1 \text{ MHz,}$ $T_j = 25 \text{ °C}$
		_	12	_		V_R = 300 V, f = 1 MHz, T_j = 25 °C
		_	12	-		V_R = 600 V, f = 1 MHz, T_j = 25 °C



4 Diagrams

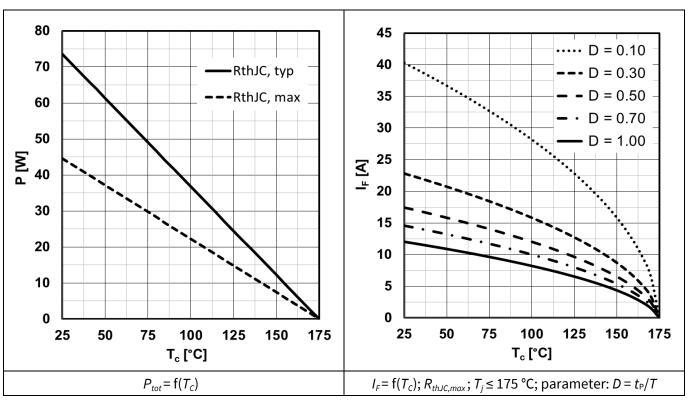


Figure 1 Power dissipation

Figure 2 Max. forward current

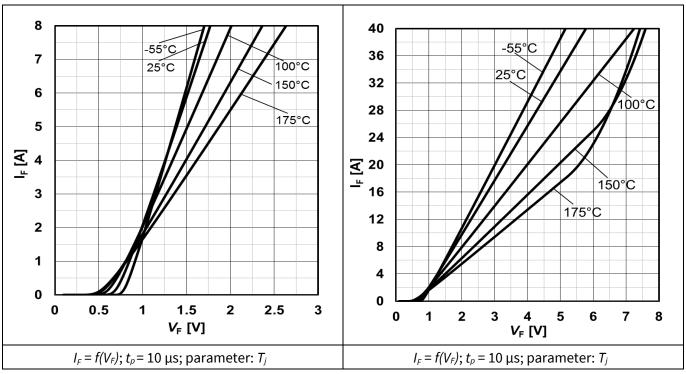


Figure 3 Typ. forward characteristics

Figure 4 Typ. forward characteristics in surge current



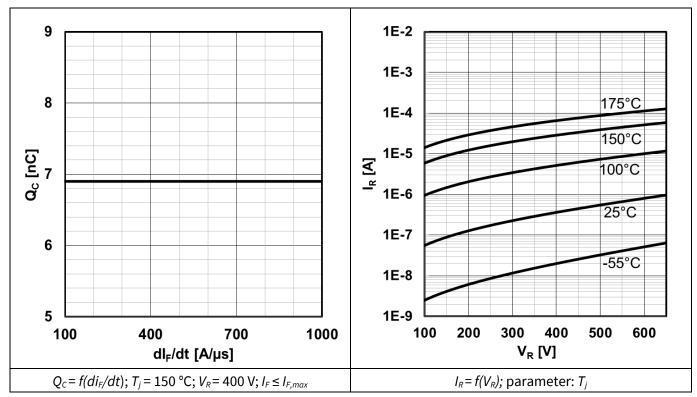


Figure 5 Typ. cap. charge vs. current slope

Figure 6 Typ. reverse current vs. reverse voltage

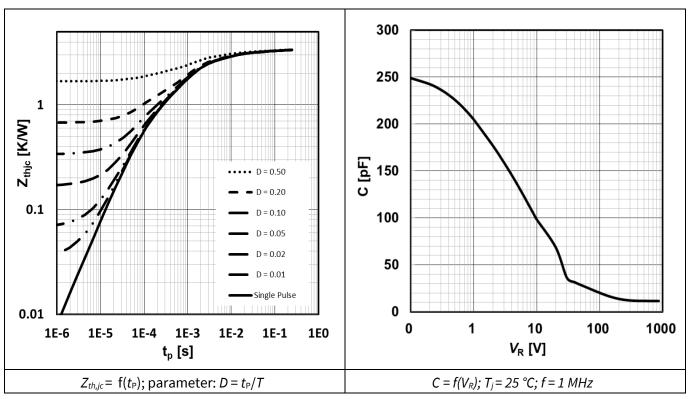


Figure 7 Max. transient thermal impedance

Figure 8 Typ. capacitance vs. reverse voltage



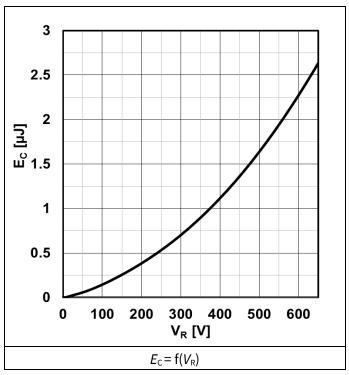


Figure 9 Typ. capacitance stored energy

5 Simplified forward characteristic

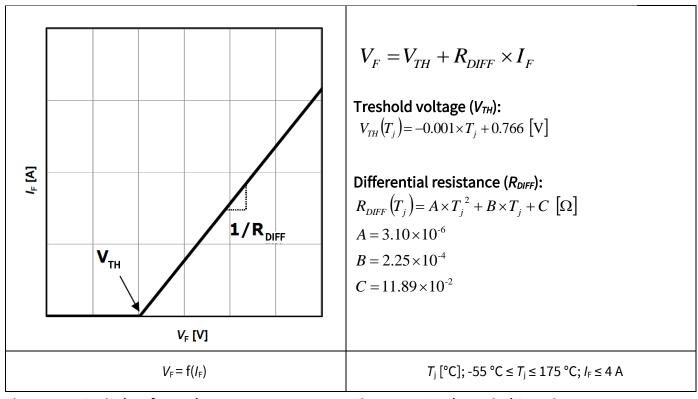


Figure 10 Equivalent forward current curve

Figure 11 Mathematical Equation



6 Package outlines

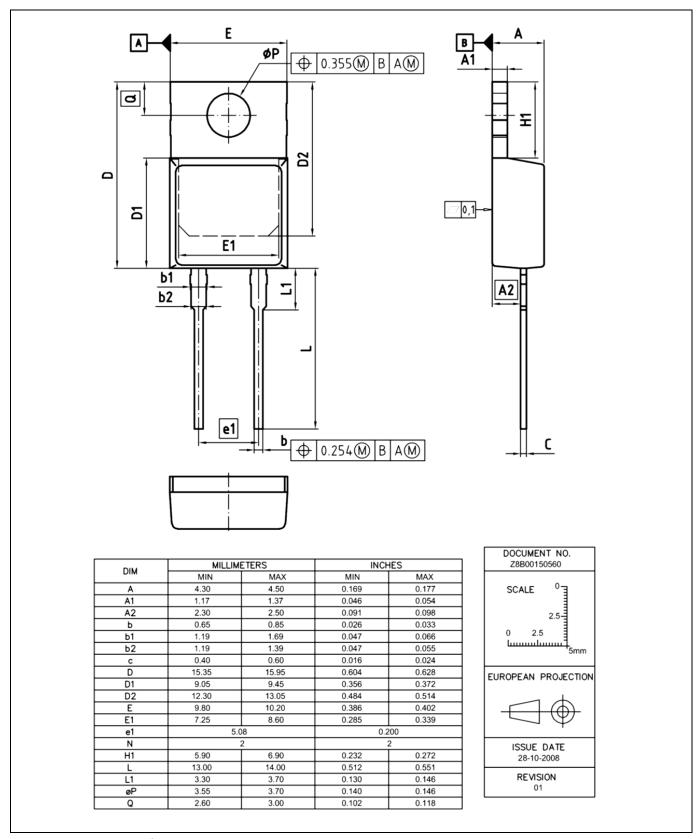


Figure 12 Outlines of the package PG-TO220-2, dimensions in mm/inches

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IDH04G65C6



Revision History

Major changes since the last revision

Revision	Date	Subject (major changes since last revision)
2.0	2017-05-23	Release of final version

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Document reference

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