



1.0A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

**FEATURES:**

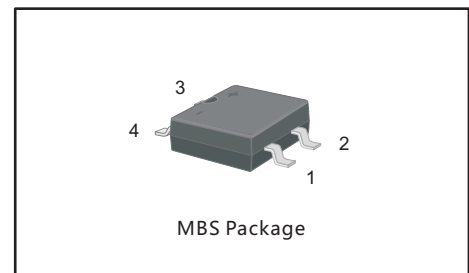
- Glass Passivated Chip Junction
- Reverse Voltage - 100 to 1000 V
- Forward Current - 1.0 A
- Fast reverse recovery time
- Designed for Surface Mount Application

**MECHANICAL DATA**

- Case: MBS
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 100mg / 0.0035oz

**PINNING**

PIN	DESCRIPTION
1	Input Pin ( ~ )
2	Input Pin ( ~ )
3	Output Anode ( + )
4	Output Cathode ( - )



**Maximum Ratings and Electrical characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	UMB1S-10	UMB2S-10	UMB4S-10	UMB6S-10	UMB8S-10	UMB10S-10	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{DC}$	100	200	400	600	800	1000	V
Average Rectified Output Current at $T_c = 125\text{ }^\circ\text{C}$	$I_O$	1.0						A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	$I_{FSM}$	35						A
Maximum Forward Voltage at 1.0 A	$V_F$	1.0	1.3	1.5			V	
Maximum DC Reverse Current $T_a = 25\text{ }^\circ\text{C}$ at Rated DC Blocking Voltage $T_a = 125\text{ }^\circ\text{C}$	$I_R$	5.0			100			$\mu\text{A}$
Typical Junction Capacitance <sup>1)</sup>	$C_j$	18						pF
Maximum Reverse Recovery Time <sup>2)</sup>	$t_{rr}$	50			75			ns
Typical Thermal Resistance <sup>3)</sup>	$R_{\theta JA}$ $R_{\theta JC}$	80			30			$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150						$^\circ\text{C}$

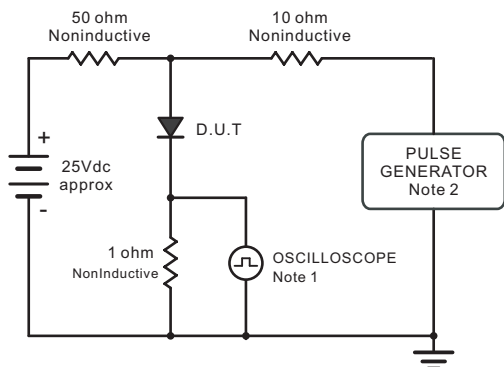
Note: 1. Measured at 1MHz and applied reverse voltage of 4 V D.C.

2. Measured with  $I_F = 0.5\text{ A}$ ,  $I_R = 1\text{ A}$ ,  $I_{rr} = 0.25\text{ A}$ .

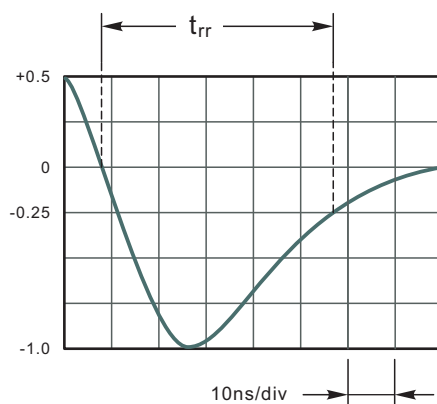
3. Mounted on glass epoxy PC board with  $4 \times 1.5'' \times 1.5''$  (  $3.81 \times 3.81\text{ cm}$  ) copper pad.



Fig.1 Reverse Recovery Time Characteristic And Test Circuit Diagram



Note: 1. Rise Time = 7ns, max.  
Input Impedance = 1megohm, 22pF.  
2. Rise Time = 10ns, max.  
Source Impedance = 50 ohms.



Set time Base for 10ns/div

Fig.2 Maximum Average Forward Current Rating

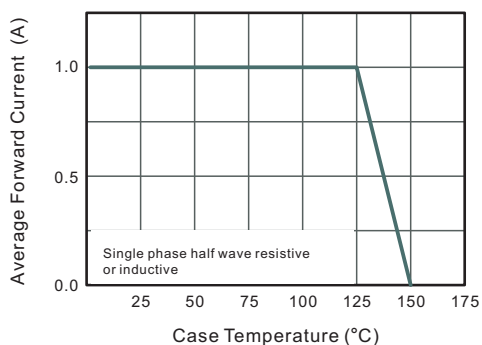


Fig.3 Typical Reverse Characteristics

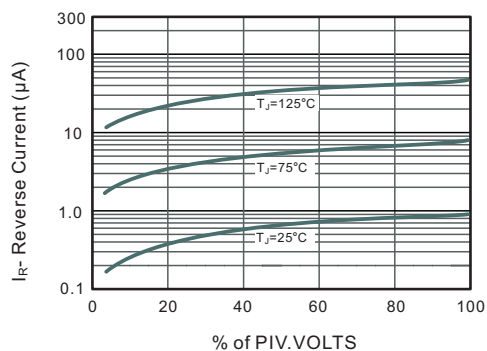


Fig.3 Typical Instaneous Forward Characteristics

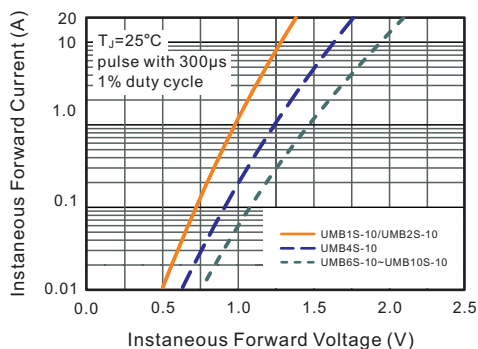
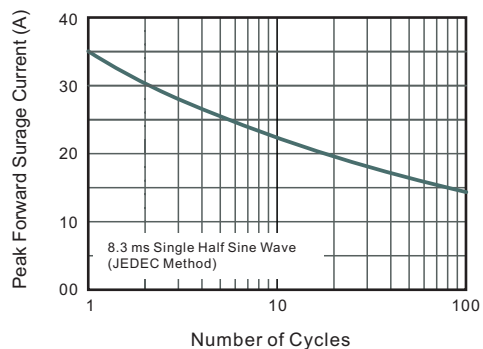


Fig.4 Maximum Non-Repetitive Peak Forward Surge Current

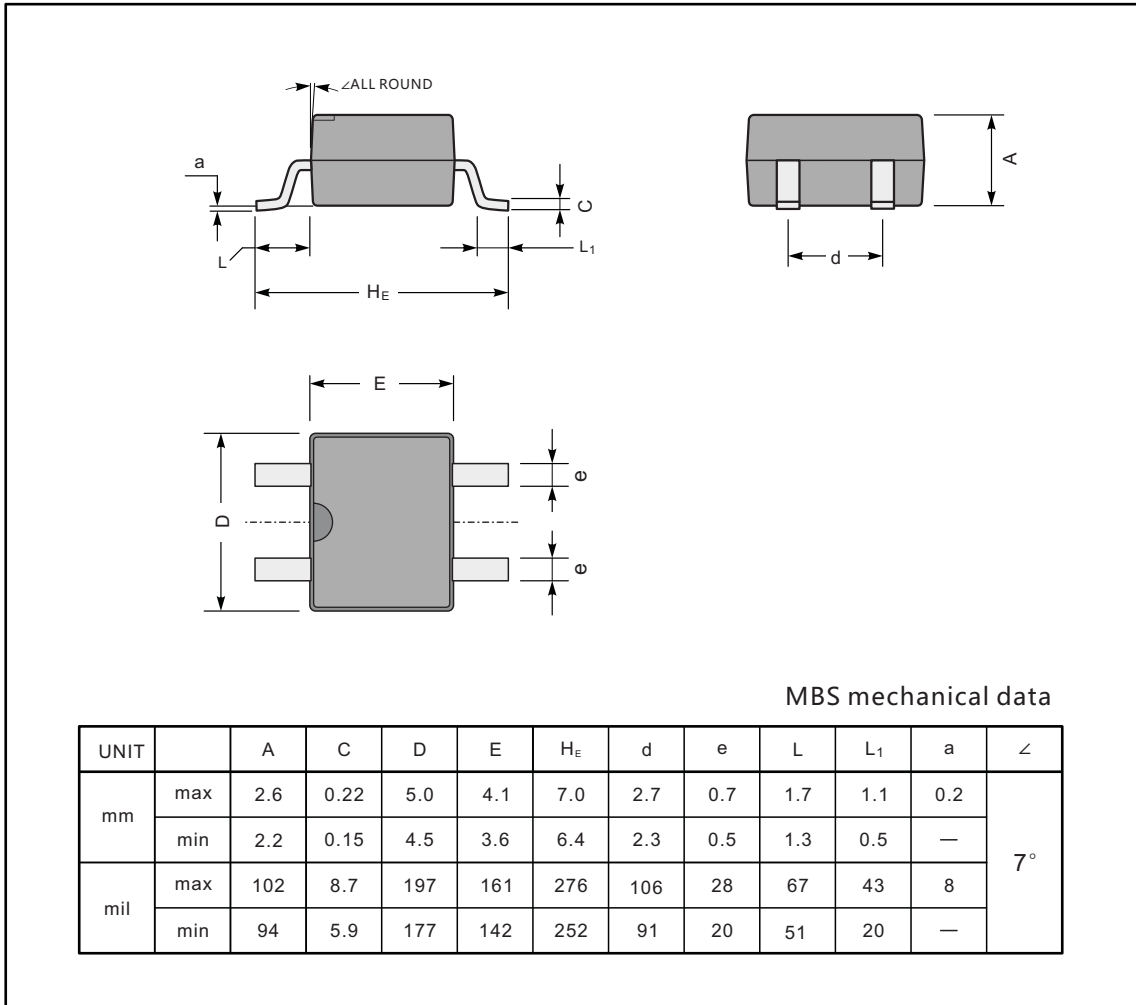




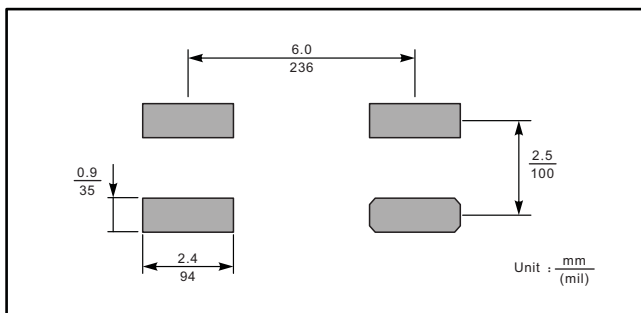
PACKAGE OUTLINE

Plastic surface mounted package; 4 leads

MBS



The recommended mounting pad size



Marking

Type number	Marking code
UMB1S-10	U10S1
UMB2S-10	U10S2
UMB4S-10	U10S4
UMB6S-10	U10S6
UMB8S-10	U10S8
UMB10S-10	U10S10

单击下面可查看定价，库存，交付和生命周期等信息

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