



## Description

### JMT P-channel Enhancement Mode Power MOSFET

#### Features

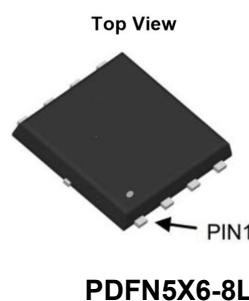
- $V_{DS} = -30V$ ,  $I_D = -50A$   
 $R_{DS(ON)} < 7.8m\Omega$  @  $V_{GS} = -10V$   
 $R_{DS(ON)} < 12.6m\Omega$  @  $V_{GS} = -4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

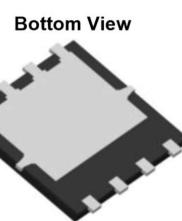
- PWM Applications
- Load Switch
- Power Management



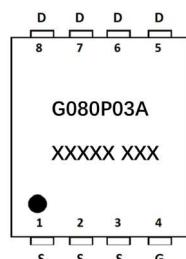
100% UIS TESTED!  
100%  $\Delta V_{ds}$  TESTED!



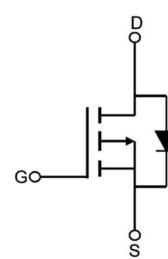
PDFN5X6-8L



Bottom View



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
G080P03A	JMTG080P03A	TAPING	PDFN5X6-8L	13inch	2500	25000

## Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		-30	V
$V_{GSS}$	Gate-Source Voltage		$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ C$	-50	A
		$T_C = 100^\circ C$	-33	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		-200	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>note2</sup>		127.7	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ C$	33	W
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.8	$^\circ C/W$
$T_J$ , $T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ C$

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D = -250\mu\text{A}$	-30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D = -250\mu\text{A}$	-1.0	-1.5	-2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS} = -10\text{V}$ , $I_D = -20\text{A}$	-	6	7.8	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}$ , $I_D = -10\text{A}$	-	9	12.6	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = -15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	4650	-	pF
$C_{oss}$	Output Capacitance		-	550	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	486	-	pF
$Q_g$	Total Gate Charge	$V_{DS} = -15\text{V}$ , $I_D = -20\text{A}$ , $V_{GS} = -10\text{V}$	-	45	-	nC
$Q_{gs}$	Gate-Source Charge		-	8	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	12	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -15\text{V}$ , $I_D = -20\text{A}$ , $V_{GS} = -10\text{V}$ , $R_{\text{GEN}} = 2.5\Omega$	-	19	-	ns
$t_r$	Turn-on Rise Time		-	15	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	65	-	ns
$t_f$	Turn-off Fall Time		-	36	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-50	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	200	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s = -30\text{A}$	-	-0.8	-1.2	V

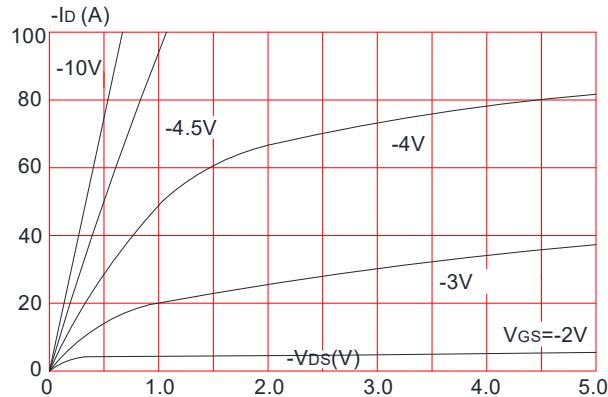
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. E<sub>AS</sub> condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD} = -20\text{V}$ ,  $V_G = -10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 0.5\text{mH}$ ,  $I_{AS} = -22.6\text{A}$

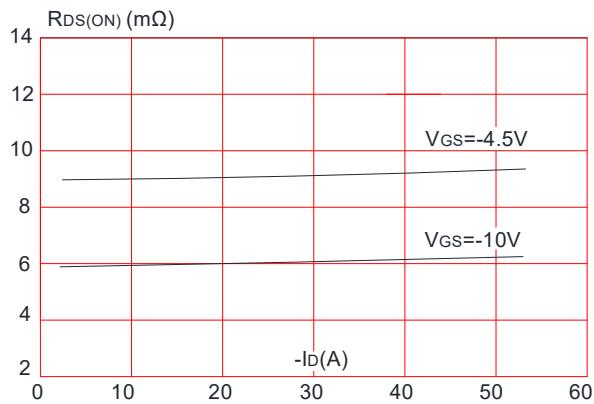
3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

## Typical Performance Characteristics

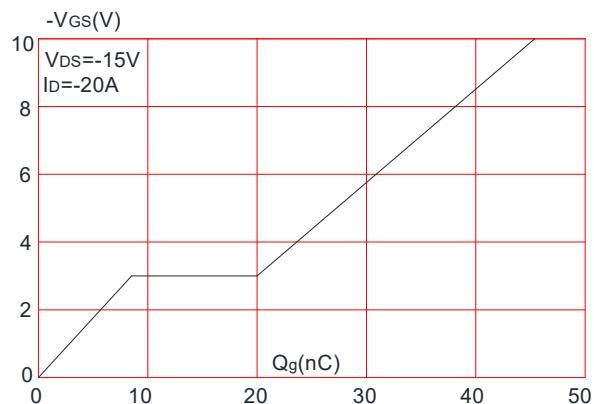
**Figure 1:** Output Characteristics



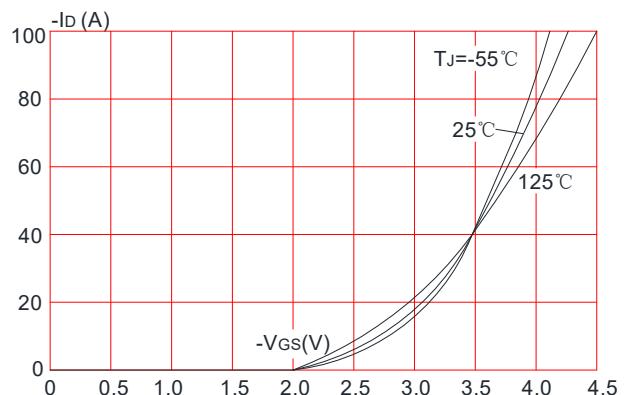
**Figure 3:** On-resistance vs. Drain Current



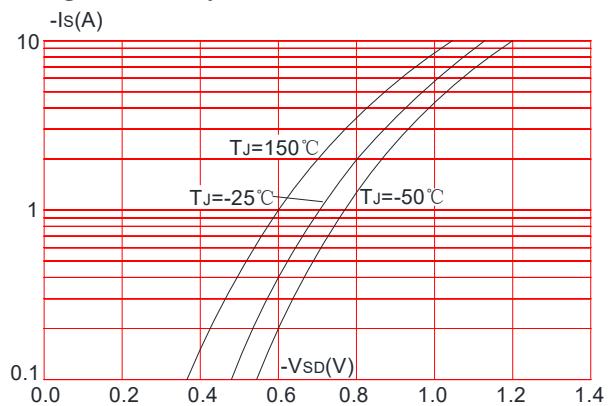
**Figure 5:** Gate Charge Characteristics



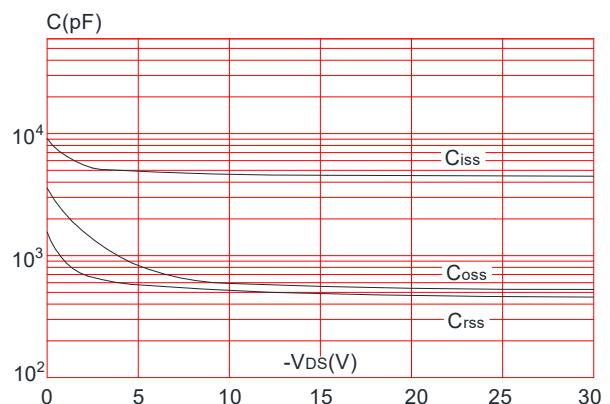
**Figure 2:** Typical Transfer Characteristics



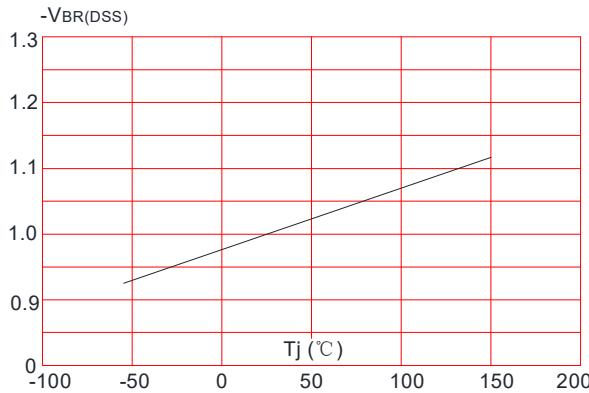
**Figure 4:** Body Diode Characteristics



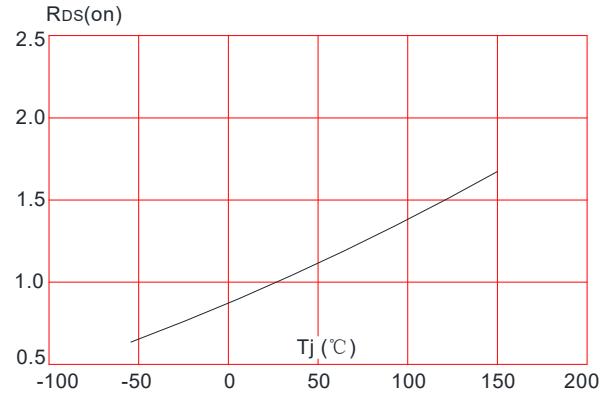
**Figure 6:** Capacitance Characteristics



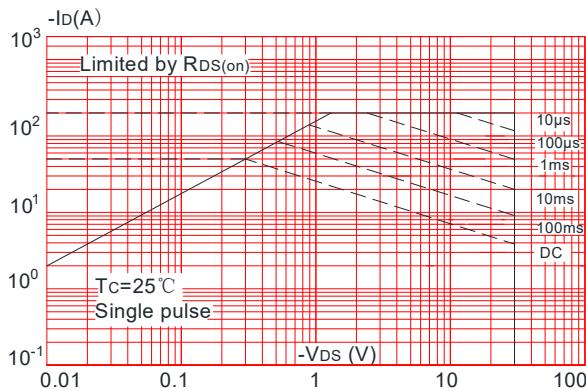
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



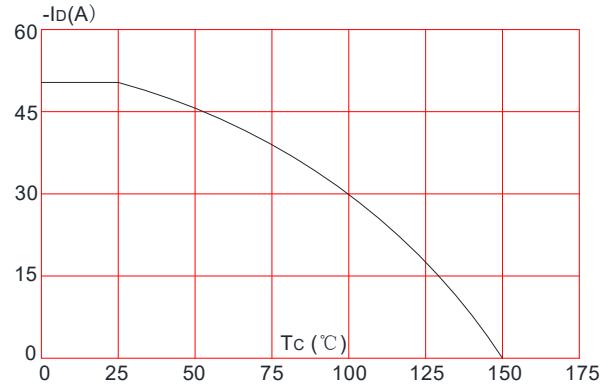
**Figure 8:** Normalized on Resistance vs. Junction Temperature



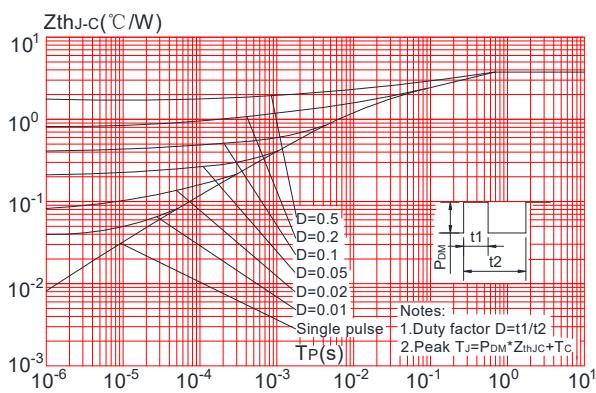
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature

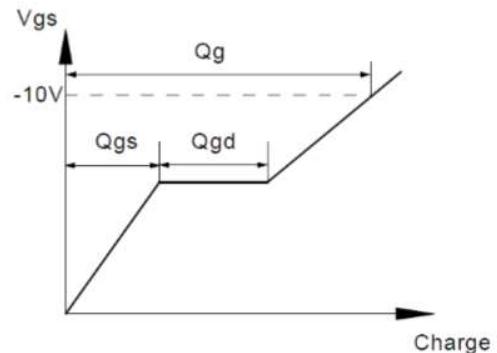
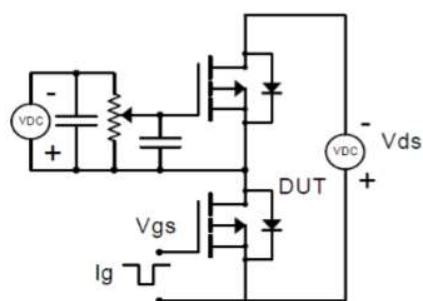


**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case

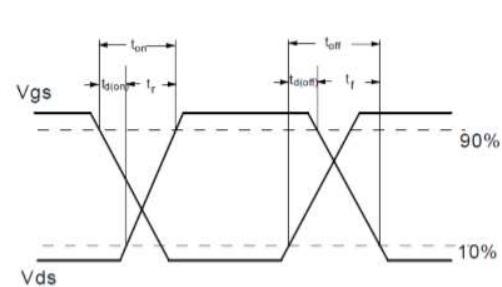
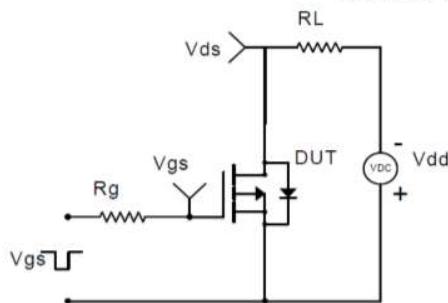


## Test Circuit

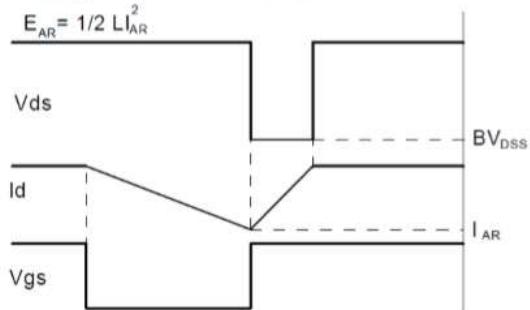
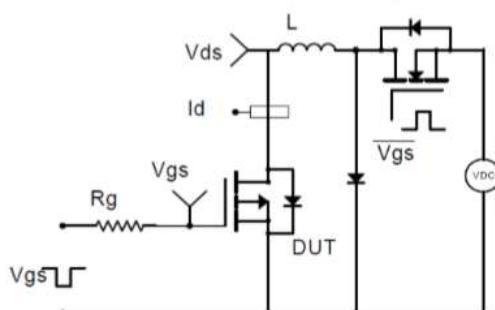
Gate Charge Test Circuit & Waveform



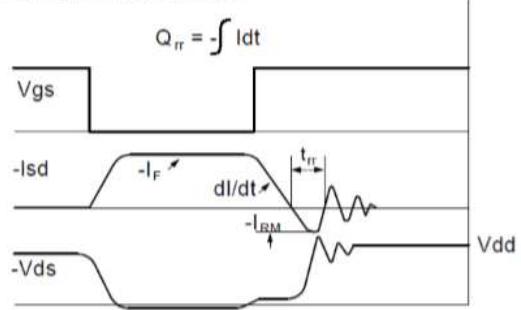
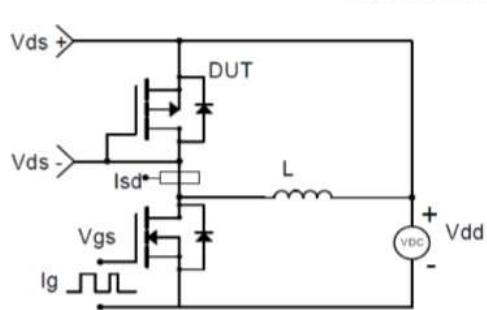
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

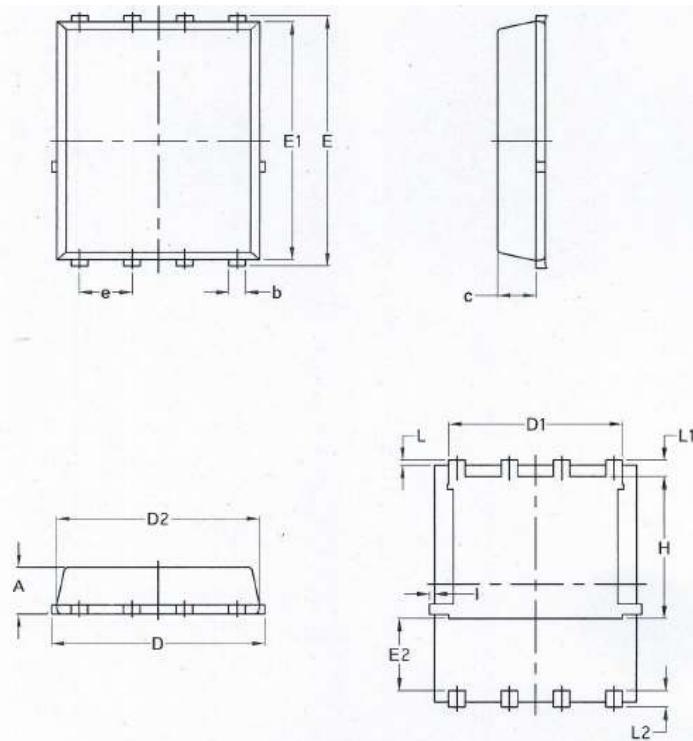


Diode Recovery Test Circuit & Waveforms





## Package Mechanical Data- PDFN5X6-8L



SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.970	0.0324	0.0382
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	—	0.0630	—
e	1.27	BSC	0.05	BSC
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	—	0.18	—	0.0070

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2020 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.

单击下面可查看定价，库存，交付和生命周期等信息

[>>JJW\(捷捷微\)](#)