

JSKT280/JSKH280

Description:

- 1) A package of series of two chips.
- 2) With high thermal conductivity DBC as the insulation.
- 3) Welding by vacuum welding technology, which provide high reliability.



Typical Application:

DC motor control, temperature control and light control system.

Absolute Maximum Ratings (Packaged into modules, unless otherwise specified, $T_{CASE}=25^{\circ}C$)

Parameter	Test Conditions	Symbol	Values			Unit
			12	16	18	
Operating junction temperature range		T_j	-40~125			$^{\circ}C$
Storage temperature range		T_{stg}	-40~125			$^{\circ}C$
Repetitive peak off-state voltage	$T_j=25^{\circ}C$	V_{DRM}	1200	1600	1600	V
Repetitive peak reverse voltage	$T_j=25^{\circ}C$	V_{RRM}	1200	1600	1600	V
Non-repetitive peak off-state voltage	$T_j=25^{\circ}C$	V_{DSM}	1400	1800	2000	V
Non-repetitive peak reverse voltage	$T_j=25^{\circ}C$	V_{RSM}	1400	1800	2000	V
Average on-state current	$T_C=85^{\circ}C$	$I_{T(AV)}/I_{F(AV)}$	280			A
Peak on-state surge current	$t_P=10ms$ $V_R=0.6V_{RRM}$	I_{TSM}/I_{FSM}	5600			A
I^2t value for fusing	$t_P=10ms$ $V_R=0.6V_{RRM}$	I^2t	156800			A^2s
Critical rate of rise of on-state current	$I_G=2 \times I_{GT}$	di/dt	150			$A/\mu s$
Insulation voltage	A.C 50Hz(1s/1min)	V_{ISO}	3600/3000			V

Electrical Characteristics (Packaged into modules, unless otherwise specified, $T_{CASE}=25^{\circ}C$)

Parameter	Test Conditions	Symbol	Values	Unit
Peak on-state voltage	$I_T=840A$ $t_P=380\mu s$	V_{TM}	≤ 1.8	V
Threshold voltage	$T_j=125^{\circ}C$	V_{TO}	≤ 0.77	V
Dynamic resistance	$T_j=125^{\circ}C$	R_d	≤ 0.8	$m\Omega$
Repetitive peak off-state current	$V_D=V_{DRM}$ $T_C=25^{\circ}C$	I_{DRM1}	≤ 100	μA
	$T_C=125^{\circ}C$	I_{DRM2}	≤ 100	mA



Repetitive peak reverse current	$V_R = V_{RRM}$ $T_C = 25^\circ\text{C}$ $T_C = 125^\circ\text{C}$	I_{RRM1} I_{RRM2}	≤ 100 ≤ 100	μA mA
Triggering gate current	$V_D = 12\text{V}$ $R_L = 30\Omega$	I_{GT}	20-150	mA
Holding current	$I_T = 1\text{A}$	I_H	≤ 300	mA
Latching current	$I_G = 1.2 I_{GT}$	I_L	≤ 400	mA
Triggering gate voltage	$V_D = 12\text{V}$ $R_L = 30\Omega$	V_{GT}	≤ 1.8	V
Non triggering gate voltage	$V_D = 0.5V_{DRM}$ $T_j = 125^\circ\text{C}$	V_{GD}	≤ 0.25	V
Critical rate of rise of voltage	$V_D = 2/3V_{DRM}$ $T_j = 125^\circ\text{C}$ Gate Open	dv/dt	≥ 1000	$\text{V}/\mu\text{s}$
Thermal resistance	Junction to case Case to heatsink	$R_{th(j-c)}$ $R_{th(c-s)}$	0.12 0.05	$^\circ\text{C}/\text{W}$

Mechanical Characteristics

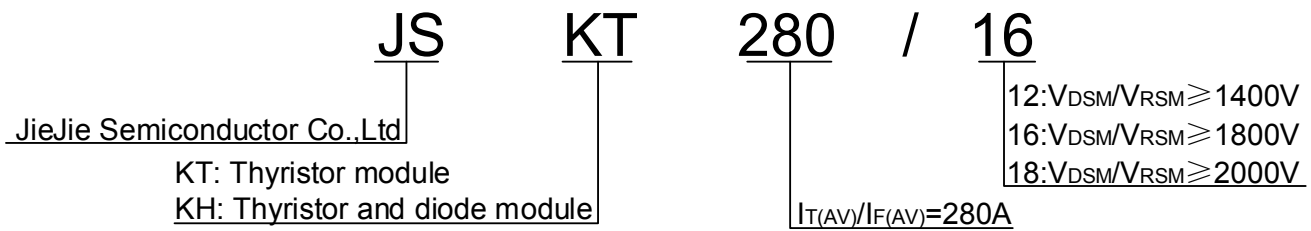
Module size	115mm×50mm
Module height	53mm
Terminal distance of (1)/(2)/(3)	42.5mm/35mm/23.5mm
Mounting torque(M5)	5±15%Nm
Terminal torque(M8)	9±15%Nm
<p>The table contains three main parts: mechanical drawings and two electrical symbols. The top-left drawing is a perspective view showing dimensions: MAX 1.45, 6, 115±0.75, 45.5±0.75, 46±0.75, and 53±0.75. The top-right drawing is a side view showing a height of 32.5±0.75. The bottom-left drawing is a top-down view showing dimensions: 42.5±0.5, 35±0.5, 23.5±0.5, 38±0.5, 3-M8, 80±0.5, 4-Ø5.5, 5.3, 6.7, 5.3, and 50±0.75. The right side of the table contains two electrical symbols. The top symbol is labeled 'JSKT symbol' and shows a thyristor with terminals (1), (2), (3), G1(5), and K1(4). The bottom symbol is labeled 'JSKH symbol' and shows a thyristor with terminals (1), (2), (3), G1(5), and K1(4).</p>	



Instructions and Precautions

- 1) There is no severe vibration and shock in operating environment, and there should be no impurity and atmosphere which may corrode metal and damage the insulation in the air-dielectric.
- 2) The operating condition of the product can't out of range of the above parameters.
- 3) When the product is installed on the radiator, the radiator's surface should be confirmed flat, smooth, wipe clean with alcohol, and coated evenly with a layer of thermal grease which thickness is moderate on the contact surface between product and radiator. When the module is fastened on the surface of the radiator, the M5 or M6 screws and spring washers are used and fastened with 5NM torque. After the module is operated 1 hour, all screws must be refastened.
- 4) The connection with the main electrode of module can use copper, welding, socket and so on. The contact surface should be smooth and flat, which make good contact. While the connection with the control electrode of module is installed, attention should be paid to the corresponding connection of each pin. After the completion of the connection, do not plug and pull out the lead of the control electrode freely.

Ordering Information



Performance Curves

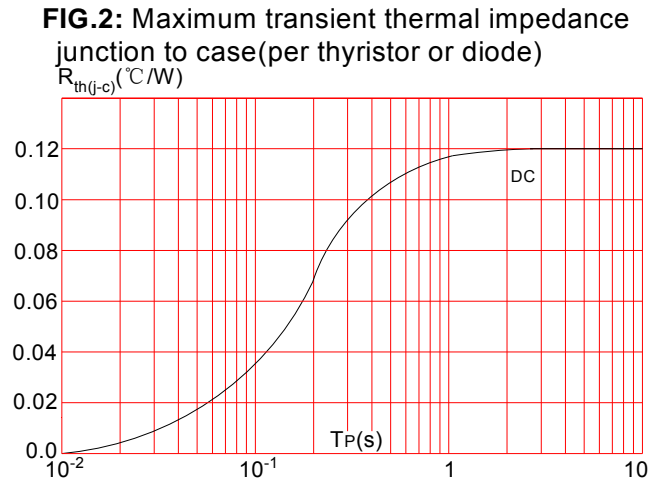
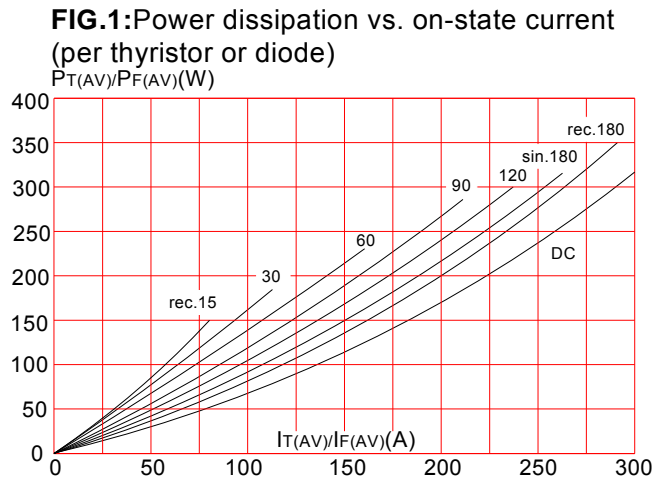




FIG.3:Forward characteristics
(per thyristor or diode)

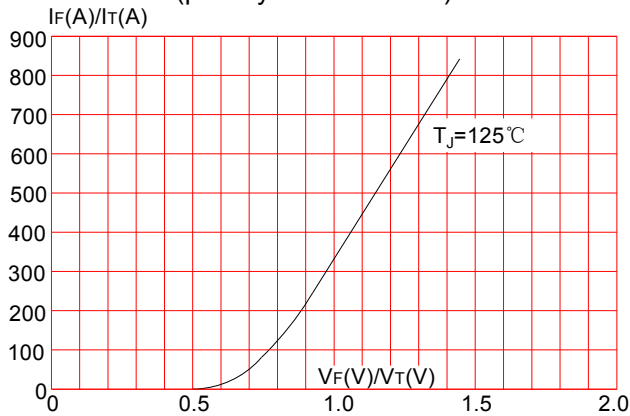
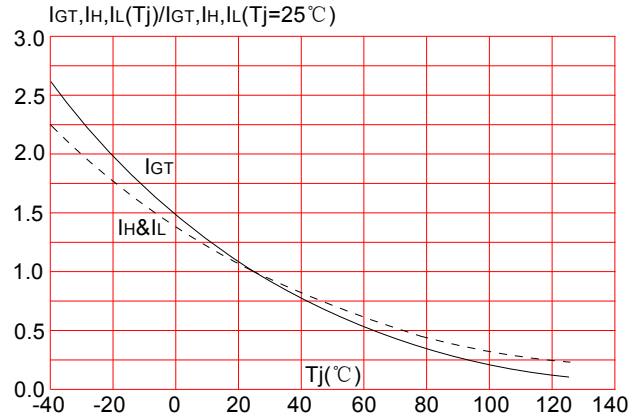


FIG.4: Relative variations of gate trigger current, holding current and latching current versus junction temperature



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TEL: +86-513-83639777

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