



## Description

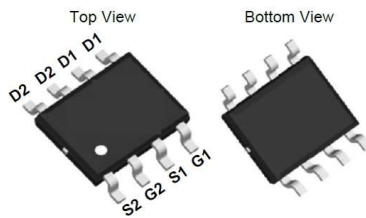
### JMT Dual N-channel Enhancement Mode Power MOSFET

#### Features

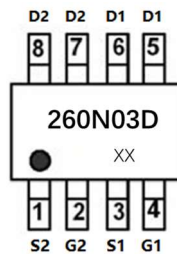
- 30V, 8A  
 $R_{DS(ON)} < 25m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 30m\Omega @ V_{GS} = 4.5V$   
 $R_{DS(ON)} < 40m\Omega @ V_{GS} = 2.5V$
- Advanced Trench Technology
- Provide Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired

#### Application

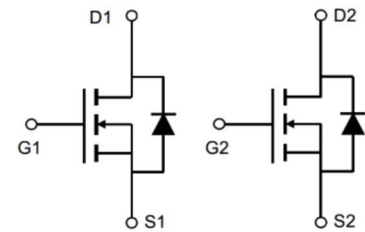
- Load Switch
- PWM Application
- Power management



SOP-8(Dual)



Marking and pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

| Device Marking | Device      | OUTLINE | Device Package | Reel Size | Reel (PCS) | Per Carton (PCS) |
|----------------|-------------|---------|----------------|-----------|------------|------------------|
| 260N03D        | JMTP260N03D | TAPING  | SOP-8          | 13inch    | 4000       | 48000            |

## Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise specified)

| Symbol                            | Parameter                               | Max.                   | Units |
|-----------------------------------|---|------------------------|-------|
| V <sub>DSS</sub>                  | Drain-Source Voltage                    | 30                     | V     |
| V <sub>GSS</sub>                  | Gate-Source Voltage                     | ±12                    | V     |
| I <sub>D</sub>                    | Continuous Drain Current                | T <sub>A</sub> = 25°C  | 8     |
|                                   |   | T <sub>A</sub> = 100°C | 5     |
| I <sub>DM</sub>                   | Pulsed Drain Current <sup>note1</sup>   | 32                     | A     |
| P <sub>D</sub>                    | Power Dissipation                       | 2.5                    | W     |
| R <sub>θJA</sub>                  | Thermal Resistance, Junction to Ambient | 50                     | °C/W  |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range | -55 to +150            | °C    |



## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise specified)

| Symbol  | Parameter   | Test Condition  | Min. | Typ. | Max. | Units |
|---|---|---|------|------|------|-------|
| <b>Off Characteristic</b>                                     |   |   |      |      |      |       |
| V <sub>(BR)DSS</sub>  | Drain-Source Breakdown Voltage                            | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA  | 30   | -    | -    | V     |
| I <sub>DSS</sub>  | Zero Gate Voltage Drain Current                           | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V,  | -    | -    | 1.0  | μA    |
| I <sub>GSS</sub>  | Gate to Body Leakage Current                              | V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V  | -    | -    | ±100 | nA    |
| <b>On Characteristics</b>                                     |   |   |      |      |      |       |
| V <sub>GS(th)</sub>   | Gate Threshold Voltage                                    | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                                    | 0.5  | 0.9  | 1.4  | V     |
| R <sub>DS(on)</sub>   | Static Drain-Source on-Resistance<br><small>note2</small> | V <sub>GS</sub> =10V, I <sub>D</sub> =4.2A  | -    | 18   | 25   | mΩ    |
|   |   | V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A   | -    | 20   | 30   |       |
|   |   | V <sub>GS</sub> =2.5V, I <sub>D</sub> =1A   | -    | 25   | 40   |       |
| <b>Dynamic Characteristics</b>                                |   |   |      |      |      |       |
| C <sub>iss</sub>  | Input Capacitance   | V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,<br>f=1.0MHz                                      | -    | 702  | -    | pF    |
| C <sub>oss</sub>  | Output Capacitance  |   | -    | 66   | -    | pF    |
| C <sub>rss</sub>  | Reverse Transfer Capacitance                              |   | -    | 52   | -    | pF    |
| Q <sub>g</sub>  | Total Gate Charge   | V <sub>DS</sub> =15V, I <sub>D</sub> =4A,<br>V <sub>GS</sub> =4.5V                          | -    | 4.8  | -    | nC    |
| Q <sub>gs</sub>   | Gate-Source Charge  |   | -    | 1.2  | -    | nC    |
| Q <sub>gd</sub>   | Gate-Drain("Miller") Charge                               |   | -    | 1.7  | -    | nC    |
| <b>Switching Characteristics</b>                              |   |   |      |      |      |       |
| t <sub>d(on)</sub>  | Turn-on Delay Time  | V <sub>DS</sub> =15V,<br>I <sub>D</sub> =6A, R <sub>GEN</sub> =3Ω,<br>V <sub>GS</sub> =4.5V | -    | 12   | -    | ns    |
| t <sub>r</sub>  | Turn-on Rise Time   |   | -    | 52   | -    | ns    |
| t <sub>d(off)</sub>   | Turn-off Delay Time                                       |   | -    | 17   | -    | ns    |
| t <sub>f</sub>  | Turn-off Fall Time  |   | -    | 10   | -    | ns    |
| <b>Drain-Source Diode Characteristics and Maximum Ratings</b> |   |   |      |      |      |       |
| I <sub>S</sub>  | Maximum Continuous Drain to Source Diode Forward Current  |   | -    | -    | 8    | A     |
| I <sub>SM</sub>   | Maximum Pulsed Drain to Source Diode Forward Current      |   | -    | -    | 32   | A     |
| V <sub>SD</sub>   | Drain to Source Diode Forward Voltage                     | V <sub>GS</sub> =0V, I <sub>S</sub> =8A   | -    | -    | 1.2  | V     |

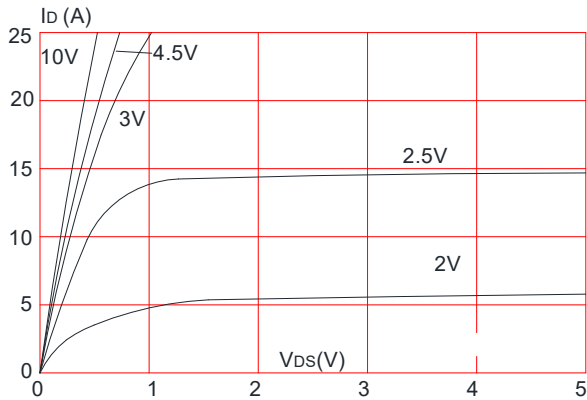
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

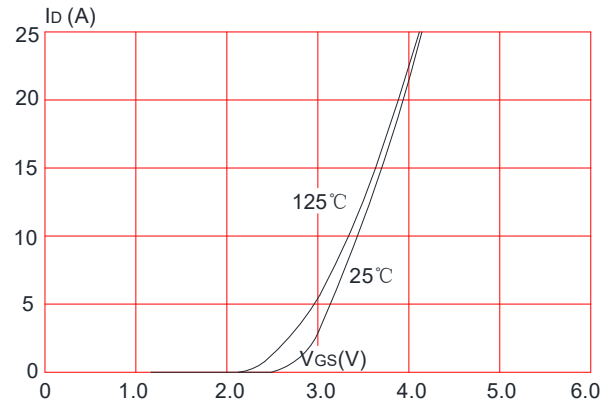


## Typical Performance Characteristics

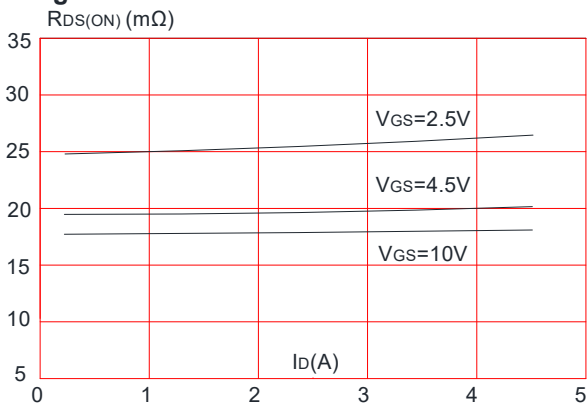
**Figure 1: Output Characteristics**



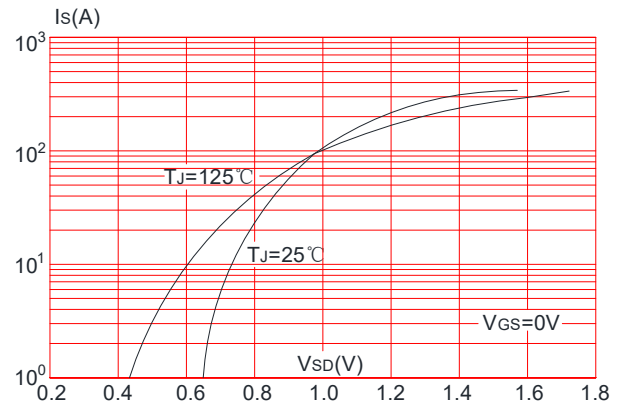
**Figure 2: Typical Transfer Characteristics**



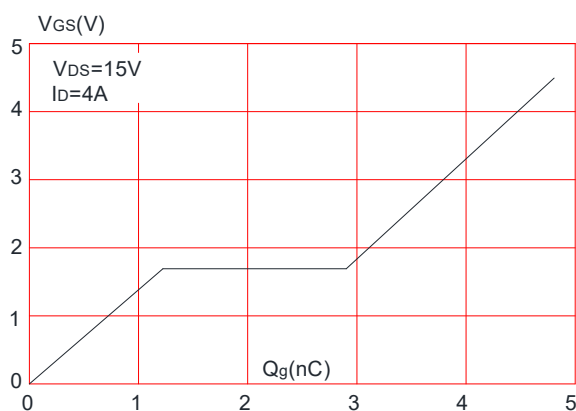
**Figure 3: On-resistance vs. Drain Current**



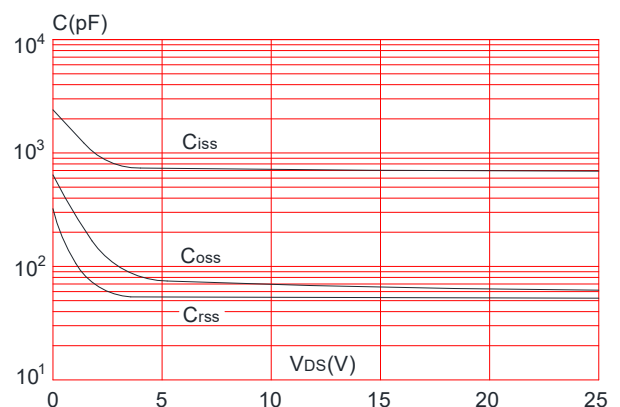
**Figure 4: Body Diode Characteristics**



**Figure 5: Gate Charge Characteristics**

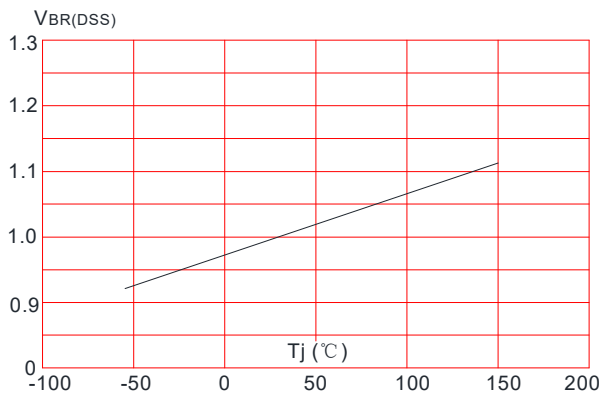


**Figure 6: Capacitance Characteristics**

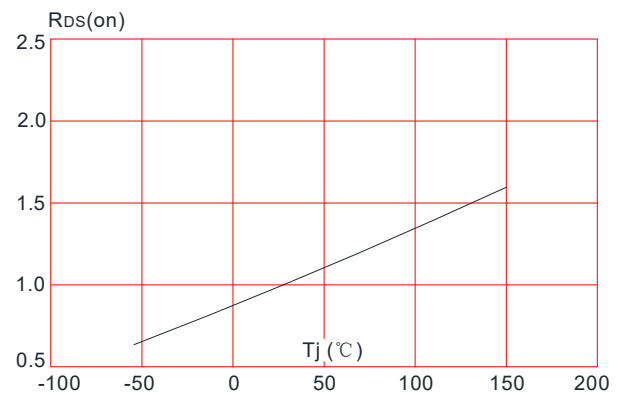




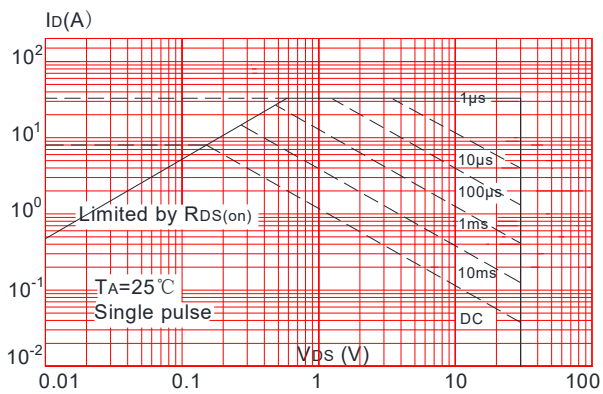
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



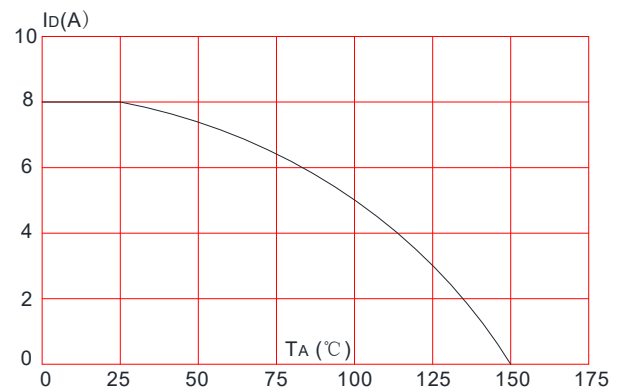
**Figure 8:** Normalized on Resistance vs. Junction Temperature



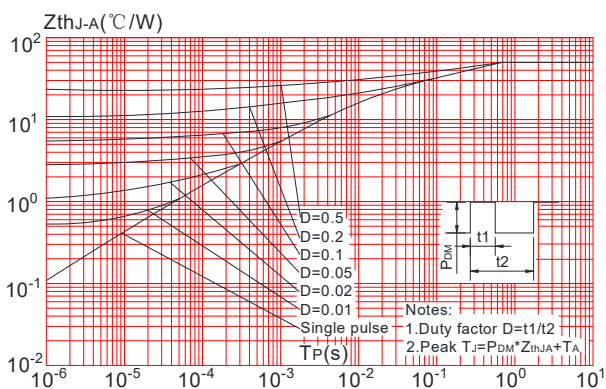
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## Test Circuit

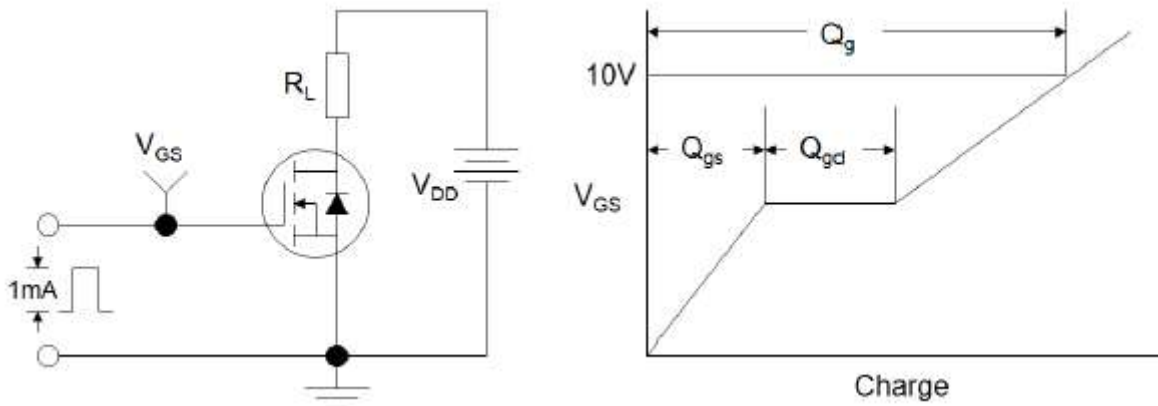


Figure1:Gate Charge Test Circuit & Waveform

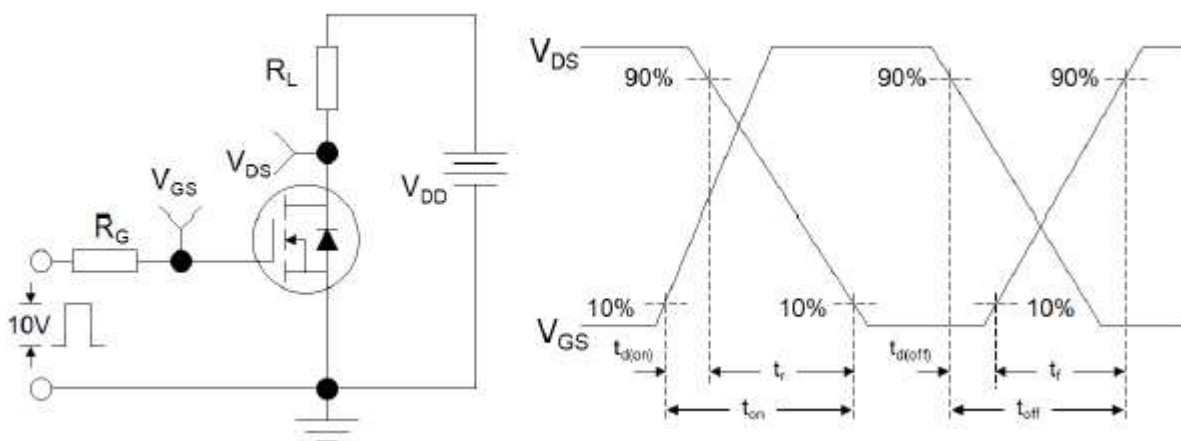


Figure 2: Resistive Switching Test Circuit & Waveforms

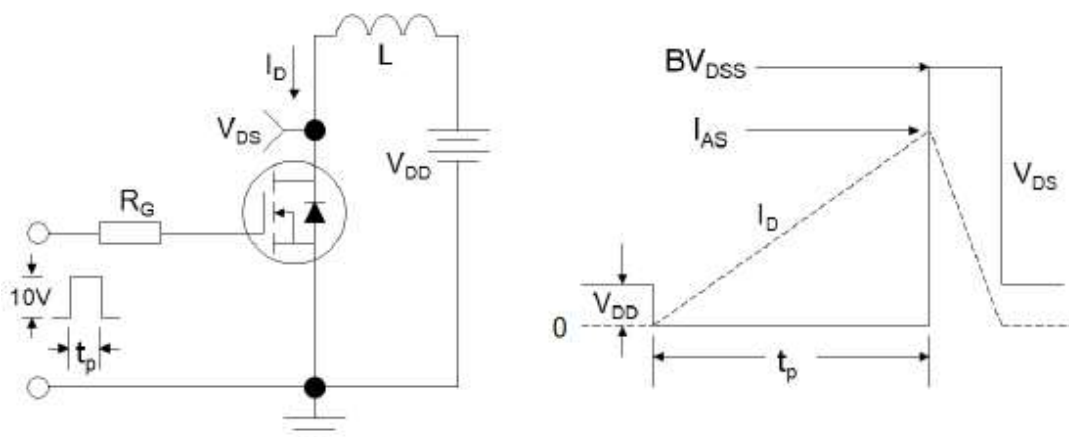
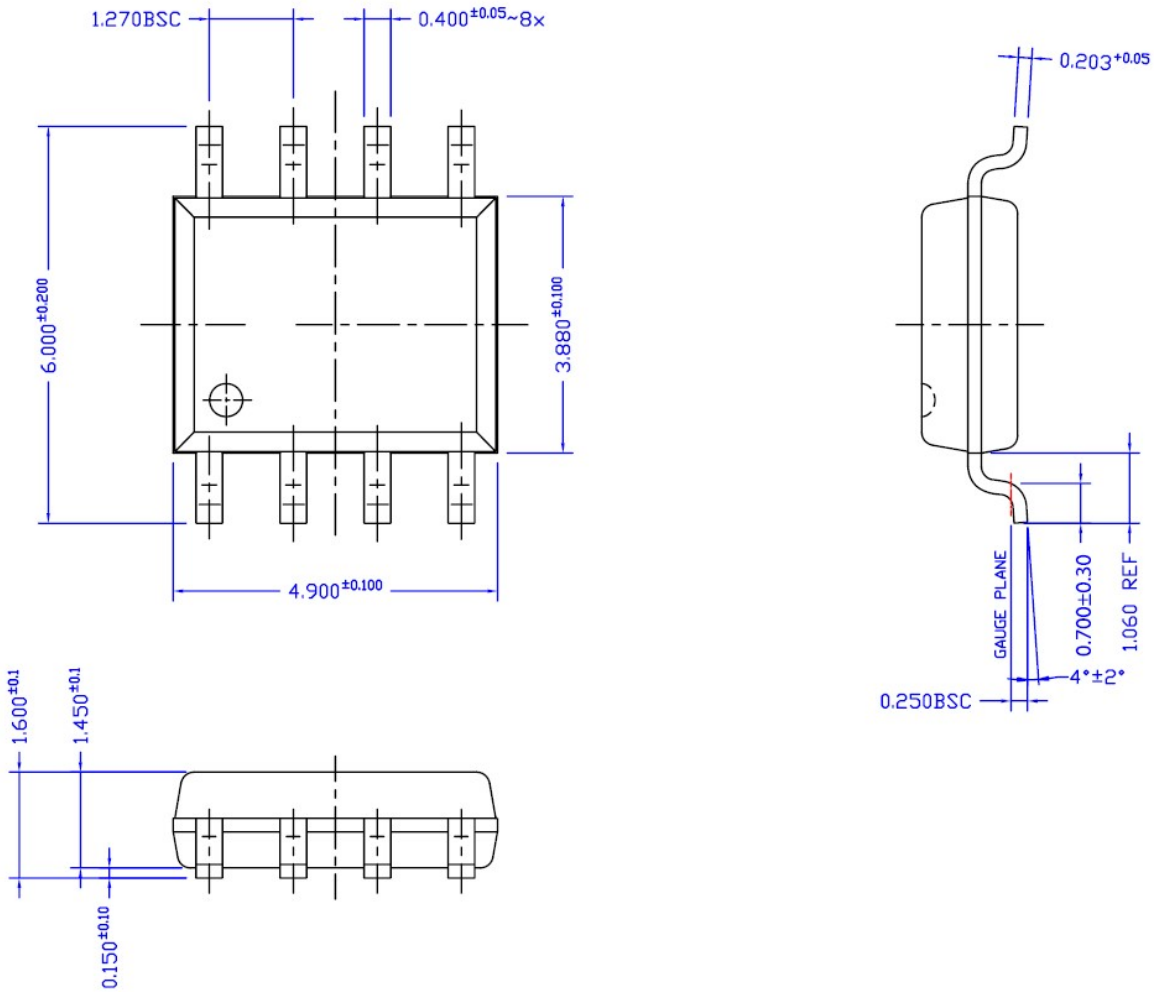


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



## Package Mechanical Data-SOP-8



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