



JR0405K Sensitive gate SCRs

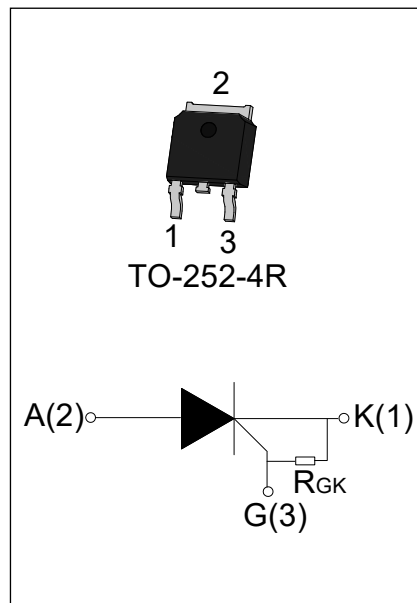
Rev.1

DESCRIPTION:

The JR0405K SCR with the parallel resistor between Gate and Cathode are especially recommended for use on straight hair, igniter, anion generator, etc. Package TO-252-4R is RoHS compliant. (2011/65/EU)

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
I_{GT}	≤ 200	μA
V_{TM}	≤ 1.5	V



ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T_{stg}	-40-150	$^{\circ}C$
Operating junction temperature range		T_j	-40-125 ^①	$^{\circ}C$
Repetitive peak off-state voltage		V_{DRM}	600	V
Repetitive peak reverse voltage		V_{RRM}	600	V
RMS on-state current	TO-252-4R ($T_C=80^{\circ}C$)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (tp=10ms)		I_{TSM}	30	A
I^2t value for fusing (tp=10ms)		I^2t	4.5	A^2s
Critical rate of rise of on-state current		di/dt	50	$A/\mu s$
Peak gate current (tp=20 μs , $T_j=125^{\circ}C$)		I_{GM}	1.2	A
Peak gate power (tp=20 μs , $T_j=125^{\circ}C$)		P_{GM}	2	W
Average gate power dissipation($T_j=125^{\circ}C$)		$P_{G(AV)}$	0.2	W

NOTE 1: When we parallel connect a $\leq 1K\Omega$ resistor between Gate and Cathode, the T_j can reach $125^{\circ}C$; if without this resistor, the T_j only can reach $110^{\circ}C$.

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	-	50	200	μA
V_{GT}		-	0.6	0.8	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$	0.2	-	-	V
I_L	$I_G=1.2 I_{GT}$	-	-	6	mA
I_H	$I_T=0.05\text{A}$	-	-	5	mA
dv/dt	$V_D=2/3V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_{GK}=1\text{K}\Omega$	10	-	-	V/ μs

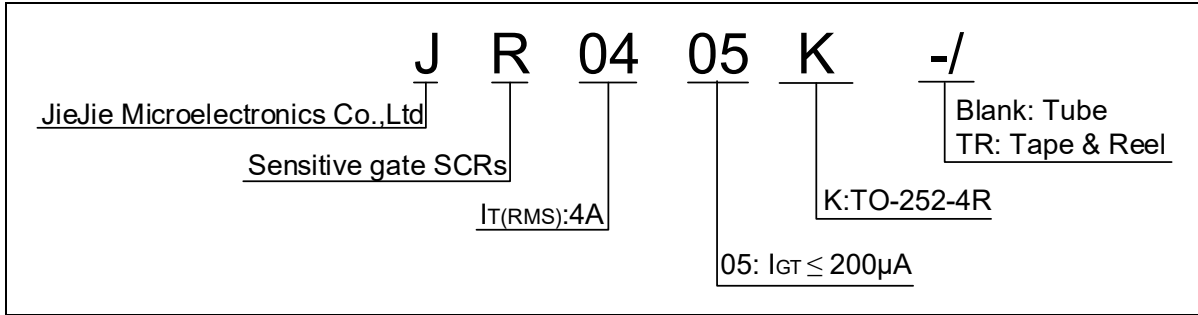
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V_{TM}	$I_{TM}=8\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	200	μA

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
$R_{th(j-c)}$	junction to case	TO-252-4R	6.5	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient		70	

ORDERING INFORMATION



MARKING

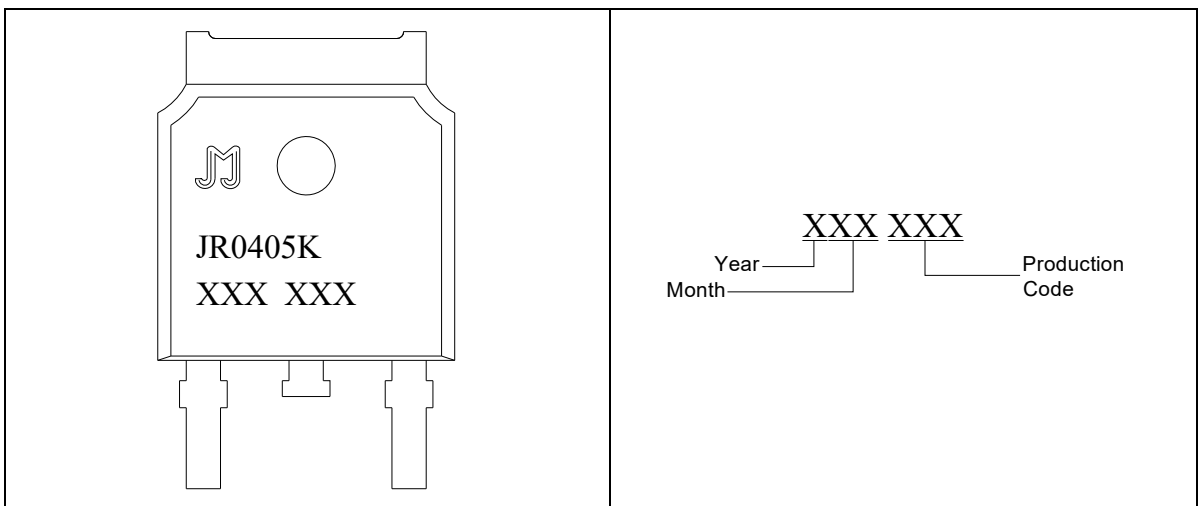


FIG.1: Maximum power dissipation versus RMS on-state current

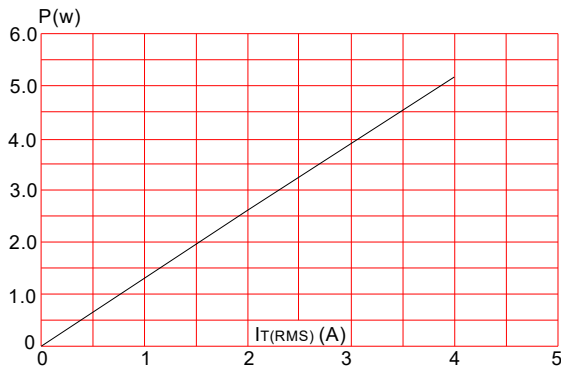


FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness:35μm)(full cycle)

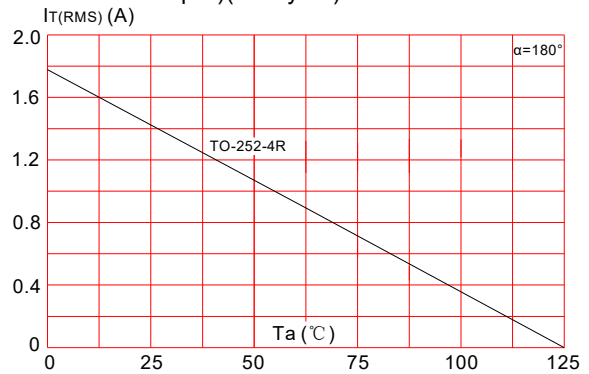


FIG.3: Surge peak on-state current versus number of cycles

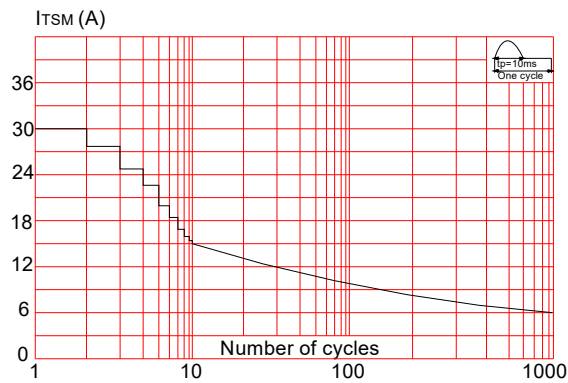


FIG.4: On-state characteristics (maximum values)

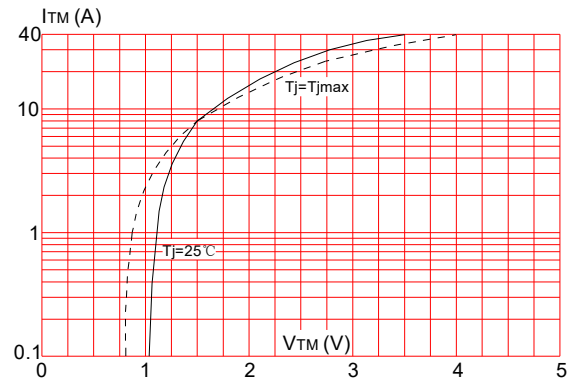


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$, and corresponding value of I^2t ($di/dt < 50\text{A}/\mu\text{s}$)

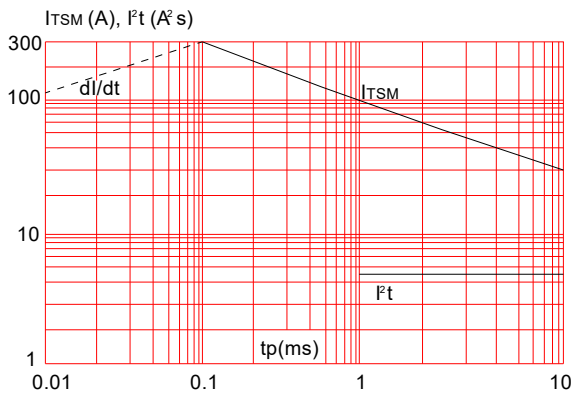
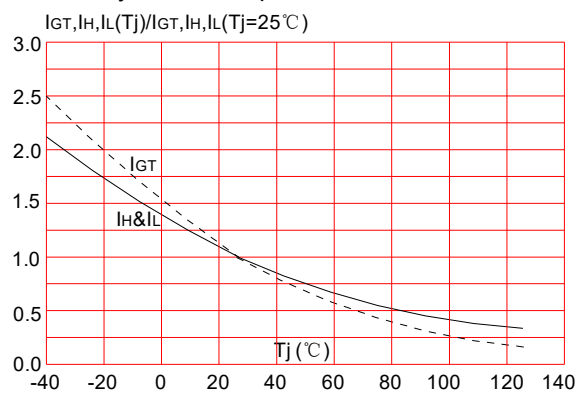
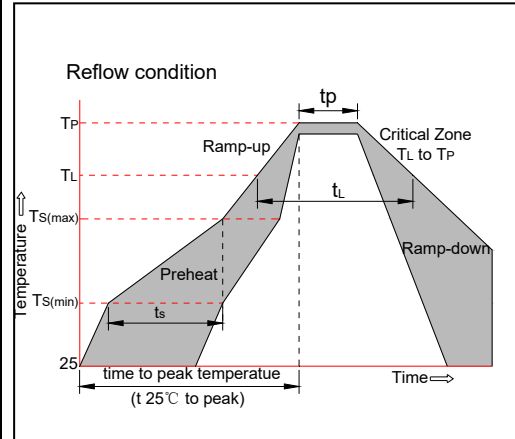


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150°C
	-Temperature Max($T_{s(max)}$)	+200°C
	-Time (Min to Max) (t_s)	60-180 secs.
Average ramp up rate (Liquidus Temp (T_L)to peak)		3°C/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3°C/sec. Max
Reflow	-Temperature(T_L) (Liquidus)	+217°C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_p)		20-40secs.
Ramp-down Rate		6°C/sec. Max
Time 25°C to Peak Temp (T_p)		8 min. Max
Do not exceed		+260°C



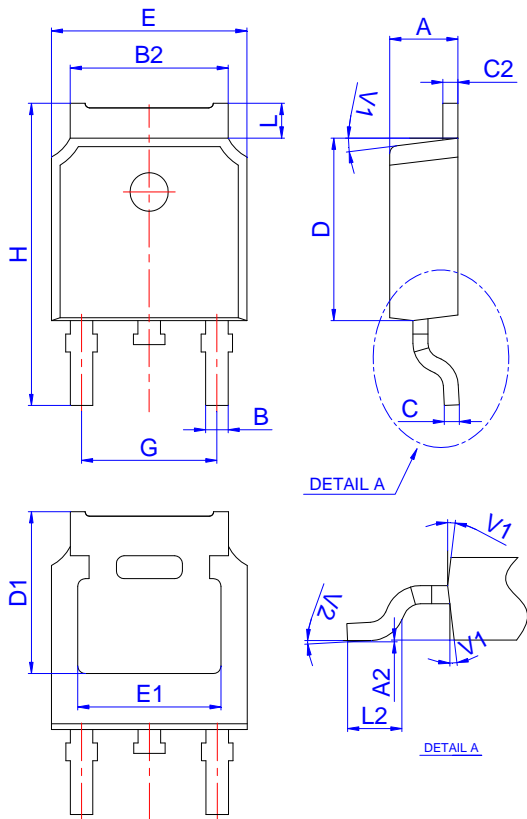
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(μ A)	Package	Base qty. (pcs)	Delivery mode
JR0405K	600	≤ 200	TO-252-4R	80	Tube
				2,500	Tape & Reel

Document Revision History

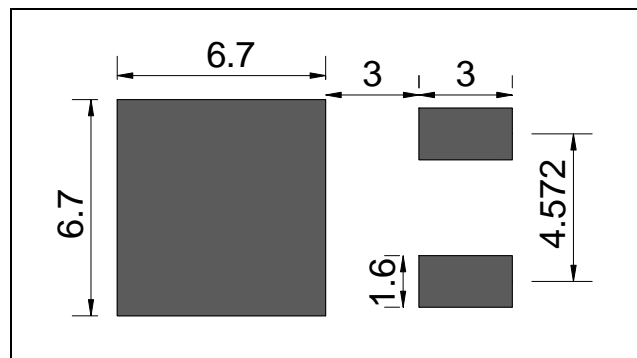
Date	Revision	Changes
Mar 19, 2022	1	Last update

PACKAGE MECHANICAL DATA

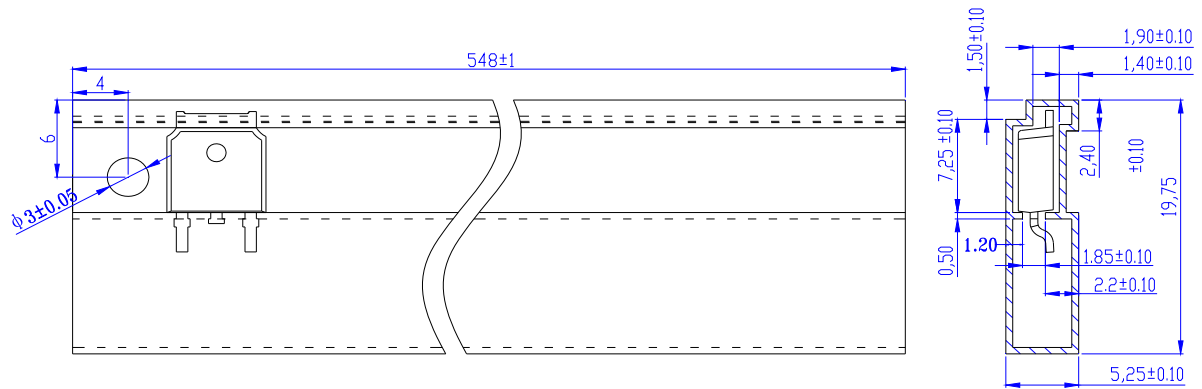


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

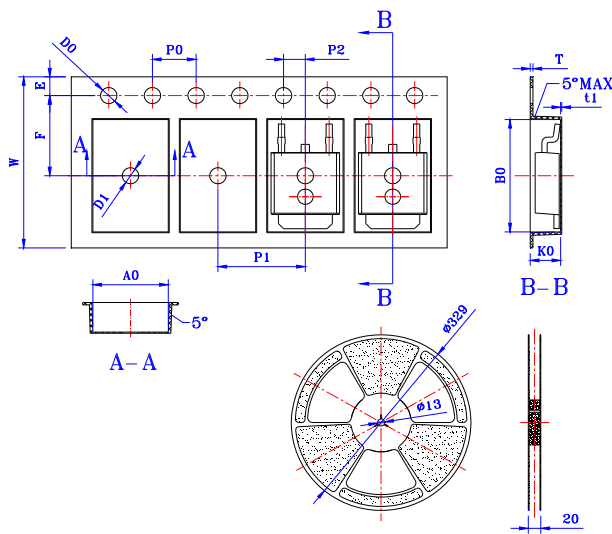
FOOTPRINT-TO-252-4R (dimensions in mm)



DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-252-4R	TUBE	80	4,000	20,000




Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
10P0	39.80	40.00	40.20	1.567	1.575	1.583
A0	6.85	6.90	7.00	0.270	0.272	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.106	0.109	0.113
T	0.24	-	0.27	0.009	-	0.011
ti	0.10	-	-	0.004	-	-

PACKAGE	OUTLINE	REEL (PCS)	PER CARTON (PCS)	TAPE & REEL
TO-252-4R	TAPING	2,500	25,000	13 inch



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

Copyright ©2022 Jiangsu JieJie Microelectronics Co.,Ltd. Printed All rights reserved.

单击下面可查看定价，库存，交付和生命周期等信息

[>>JW\(捷捷微\)](#)