



Description

JMT N And P-Channel Enhancement Mode MOSFET

Features

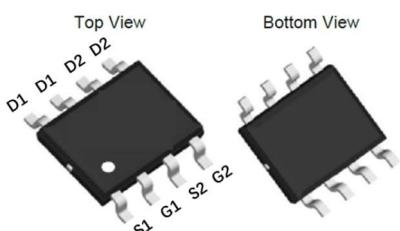
- N-Channel: 30V, 9A
 $R_{DS(ON)} < 21m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(ON)} < 33m\Omega$ @ $V_{GS} = 4.5V$
- P-Channel: -30V, -7A
 $R_{DS(ON)} < 35m\Omega$ @ $V_{GS} = -10V$
 $R_{DS(ON)} < 54m\Omega$ @ $V_{GS} = -4.5V$
- Excellent Gate Charge x $R_{DS(ON)}$ Product(FOM)
- Very Low On-resistance $R_{DS(ON)}$
- Fast Switching Speed

Application

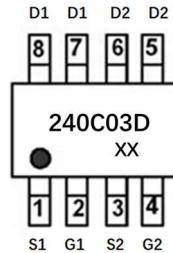
- Battery Protection
- Load Switch
- Power Management



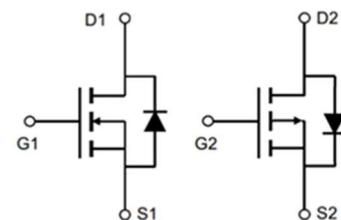
100% UIS TESTED!
100% ΔV_{ds} TESTED!



SOP-8(Dual)



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
240C03D	JMTP240C03D	TAPING	SOP-8	13inch	4000	64000

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise specified)

Symbol	Parameter		Max. N-Channel	Max. P-Channel	Units
V_{DSS}	Drain-Source Voltage		30	-30	V
V_{GSS}	Gate-Source Voltage		± 20	± 20	V
I_D	Continuous Drain Current		$T_A = 25^\circ C$	9	-7
			$T_A = 100^\circ C$	6	-4.6
I_{DM}	Pulsed Drain Current ^{note1}		36	-28	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}		12	30	mJ
P_D	Power Dissipation	$T_A = 25^\circ C$	2.7	2.5	W
R_{eJA}	Thermal Resistance, Junction to Ambient		46.3	50	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150		$^\circ C$

**N-Channel Electrical Characteristics** ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$	-	-	1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS}=10\text{V}$, $I_D=5\text{A}$	-	16	21	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=3\text{A}$	-	24	33	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	490	-	pF
C_{oss}	Output Capacitance		-	79	-	pF
C_{rss}	Reverse Transfer Capacitance		-	61	-	pF
Q_g	Total Gate Charge	$V_{DS}=15\text{V}$, $I_D=5\text{A}$, $V_{GS}=10\text{V}$	-	5.2	-	nC
Q_{gs}	Gate-Source Charge		-	0.9	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	1.3	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=15\text{V}$, $I_D=3\text{A}$, $V_{GS}=10\text{V}$, $R_{REN}=3\Omega$	-	4.5	-	ns
t_r	Turn-on Rise Time		-	2.5	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	14.5	-	ns
t_f	Turn-off Fall Time		-	3.5	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	9	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	36	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=9\text{A}$	-	0.8	1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=15\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{AS}=7\text{A}$

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

**P-Channel Electrical Characteristics** ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D = -250\mu\text{A}$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}$, $V_{GS}=0\text{V}$,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D = -250\mu\text{A}$	-1.0	-1.5	-2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS} = -10\text{V}$, $I_D = -7\text{A}$	-	27	35	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}$, $I_D = -4\text{A}$	-	38	54	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -15\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	982	-	pF
C_{oss}	Output Capacitance		-	135	-	pF
C_{rss}	Reverse Transfer Capacitance		-	109	-	pF
Q_g	Total Gate Charge	$V_{DS} = -15\text{V}$, $I_D = -4\text{A}$, $V_{GS} = -10\text{V}$	-	10	-	nC
Q_{gs}	Gate-Source Charge		-	2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	2.7	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -15\text{V}$, $I_D = -7\text{A}$, $V_{GS} = -10\text{V}$, $R_{GEN}=2.5\Omega$	-	11	-	ns
t_r	Turn-on Rise Time		-	19	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	45	-	ns
t_f	Turn-off Fall Time		-	26	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	-7	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-28	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s = -7\text{A}$	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{DD} = -15\text{V}$, $V_G = -10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{AS} = -11\text{A}$ 3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics-N

Figure 1: Output Characteristics

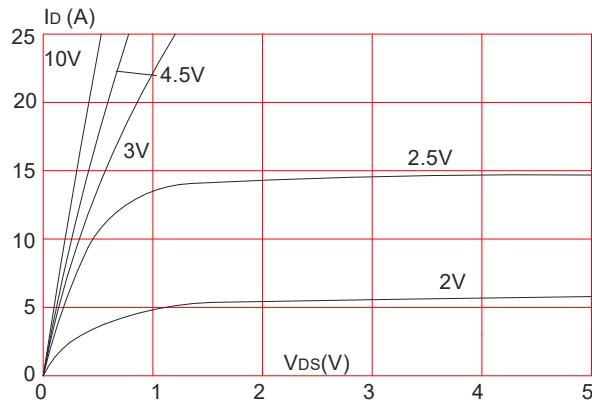


Figure 2: Typical Transfer Characteristics

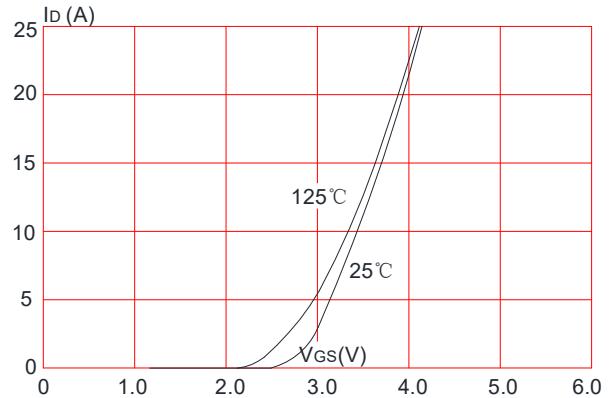


Figure 3: On-resistance vs. Drain Current

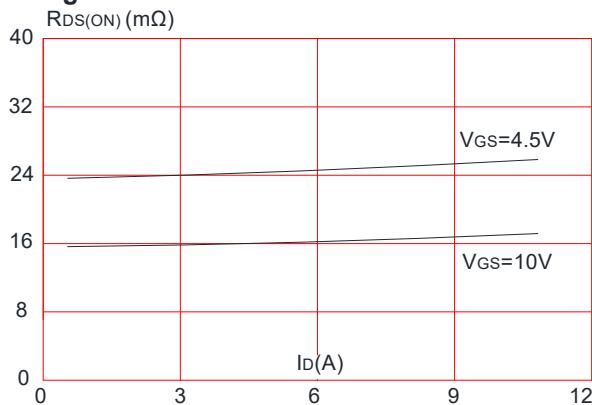


Figure 5: Gate Charge Characteristics

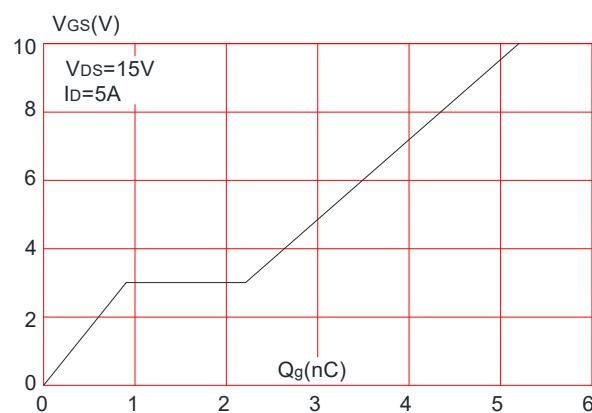


Figure 4: Body Diode Characteristics

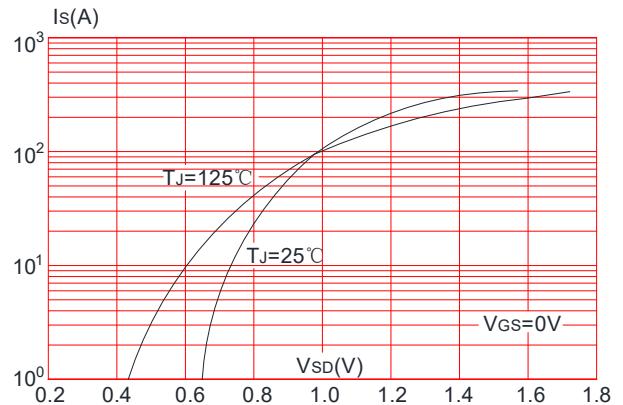


Figure 6: Capacitance Characteristics

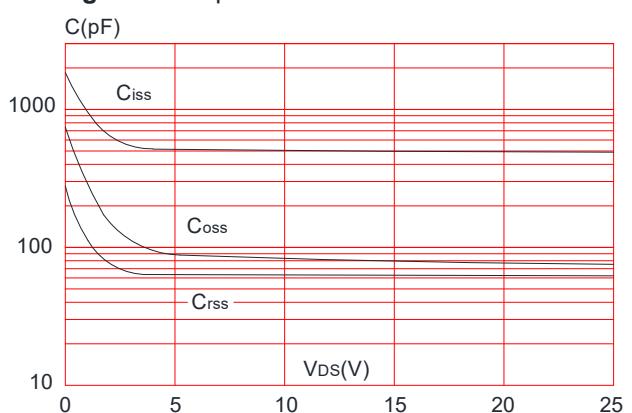


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

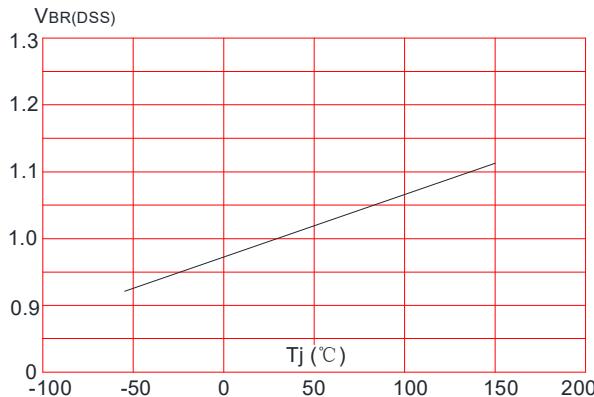


Figure 9: Maximum Safe Operating Area

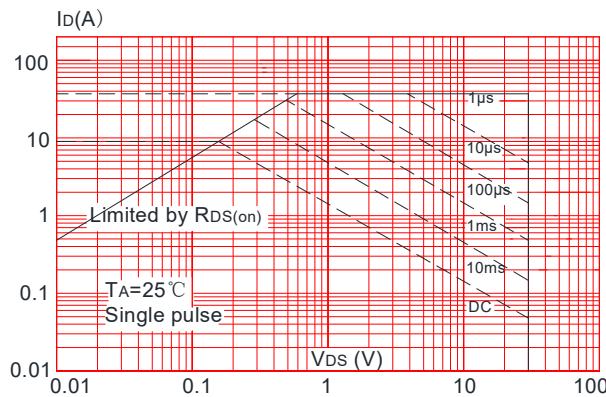


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

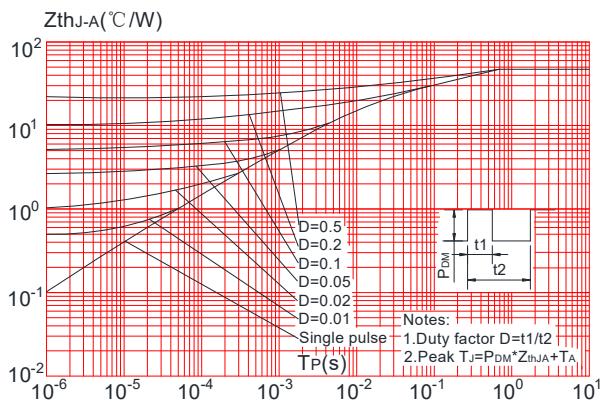


Figure 8: Normalized on Resistance vs. Junction Temperature

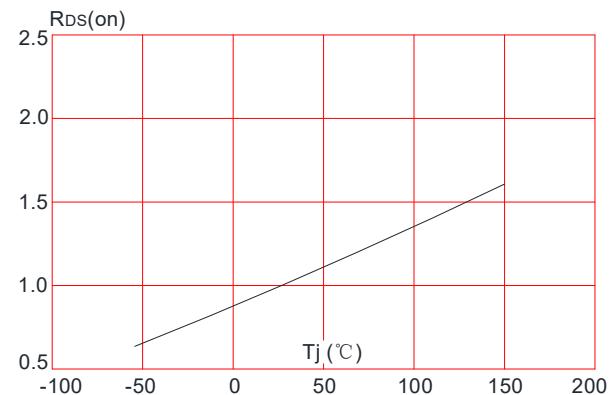
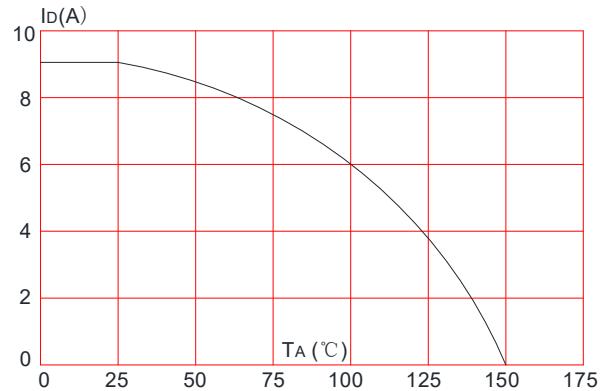


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature



Test Circuit-N

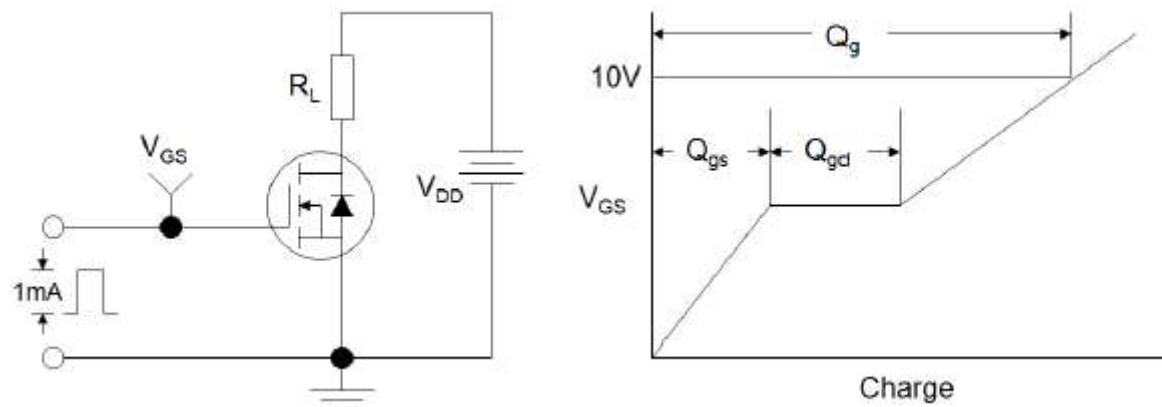


Figure1:Gate Charge Test Circuit & Waveform

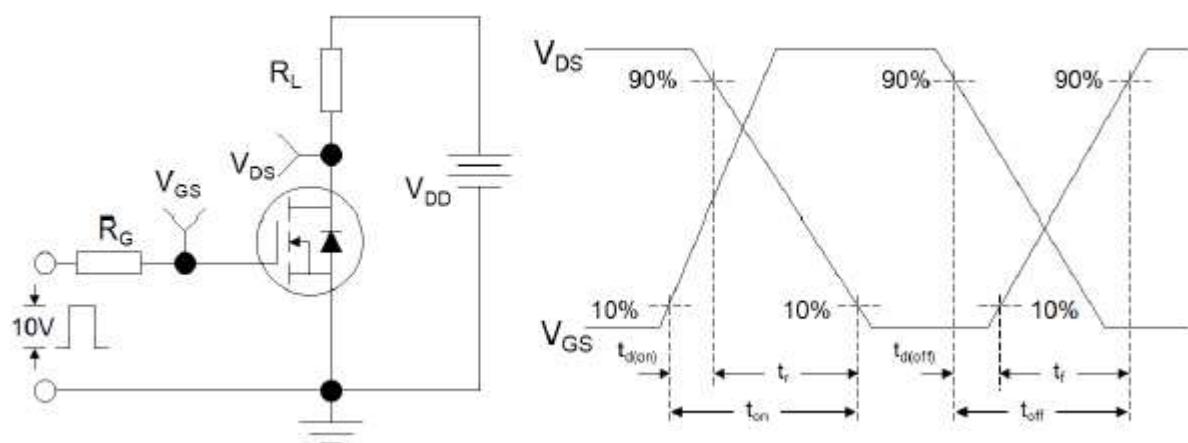


Figure 2: Resistive Switching Test Circuit & Waveforms

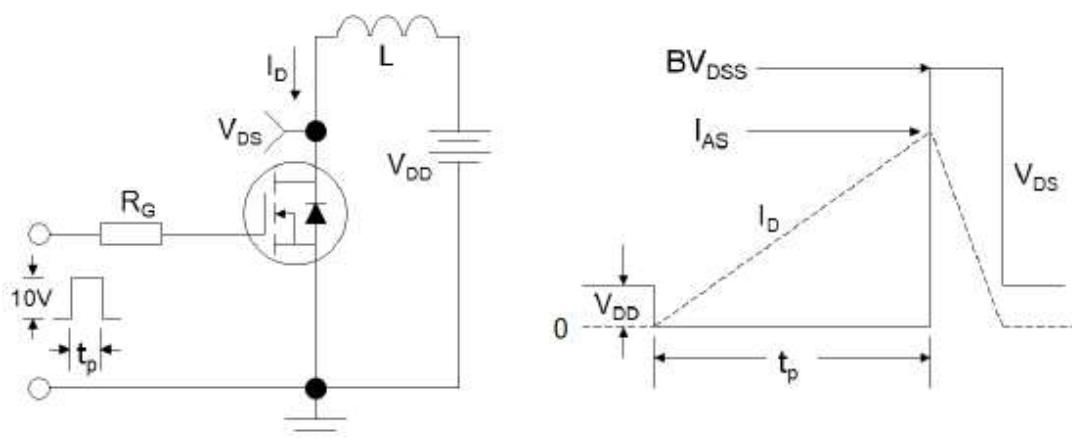


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Typical Performance Characteristics-P

Figure 1: Output Characteristics

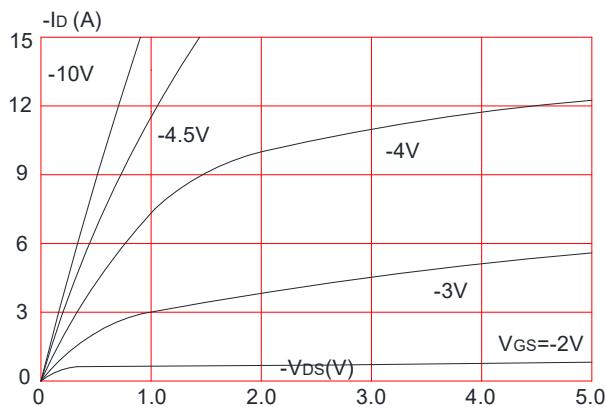


Figure 2: Typical Transfer Characteristics

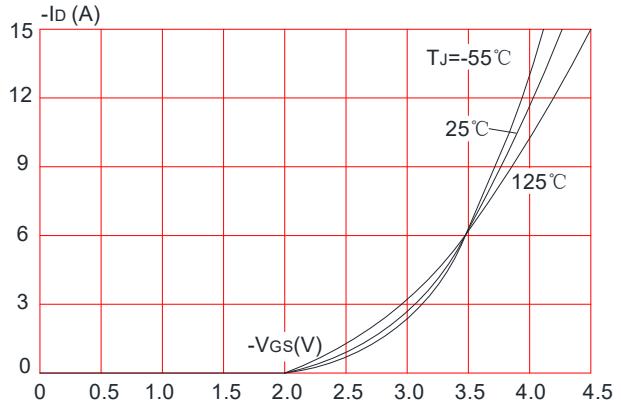


Figure 3: On-resistance vs. Drain Current

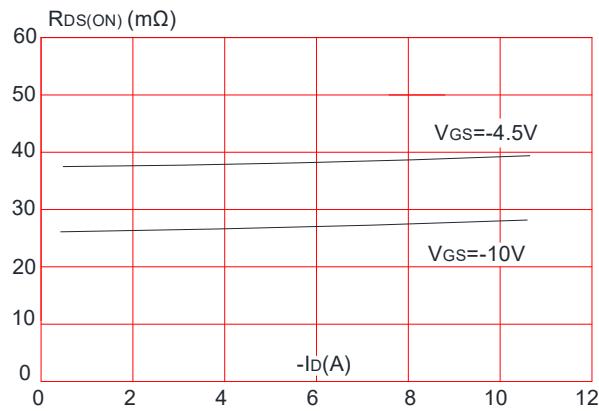


Figure 5: Gate Charge Characteristics

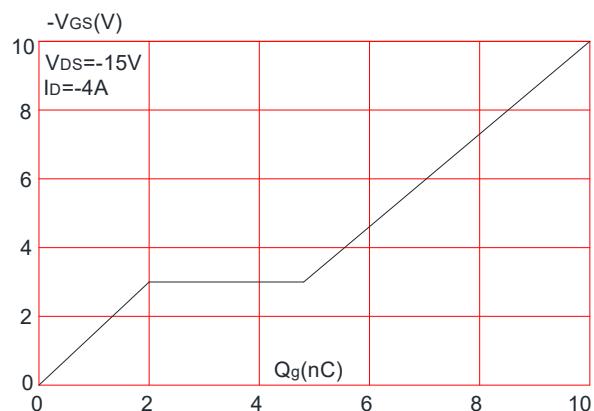


Figure 4: Body Diode Characteristics

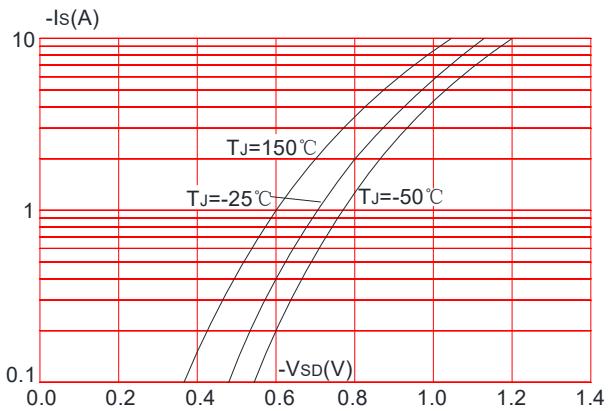


Figure 6: Capacitance Characteristics

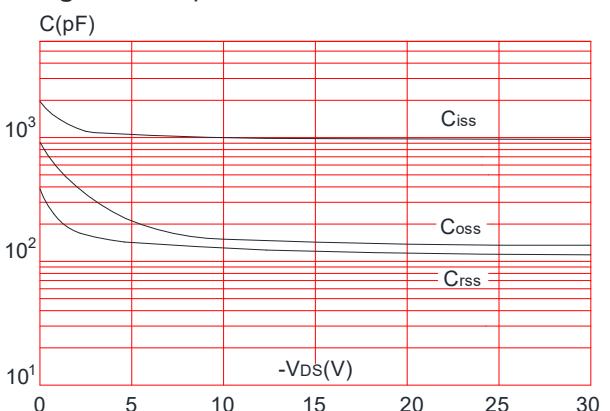


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

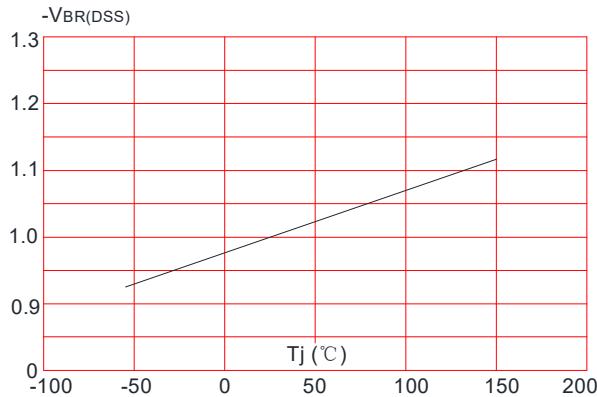


Figure 8: Normalized on Resistance vs. Junction Temperature

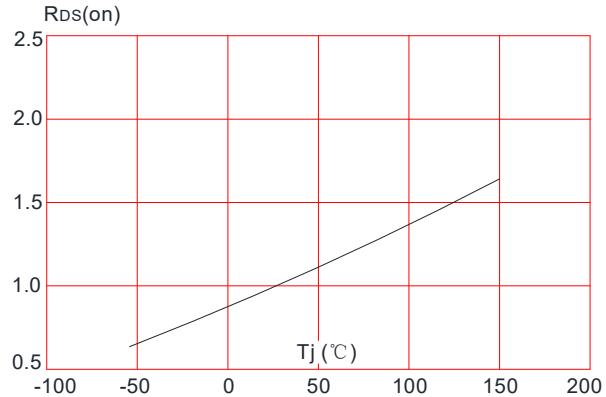


Figure 9: Maximum Safe Operating Area

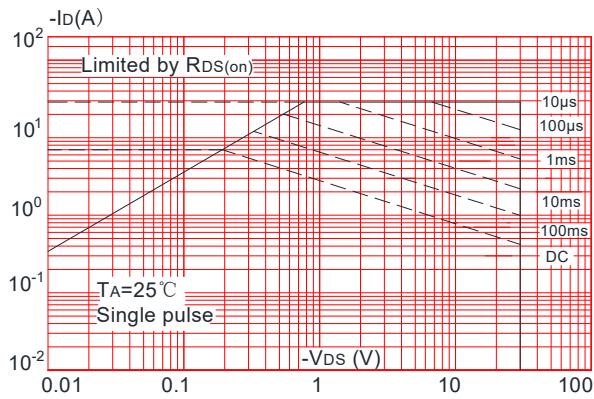


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

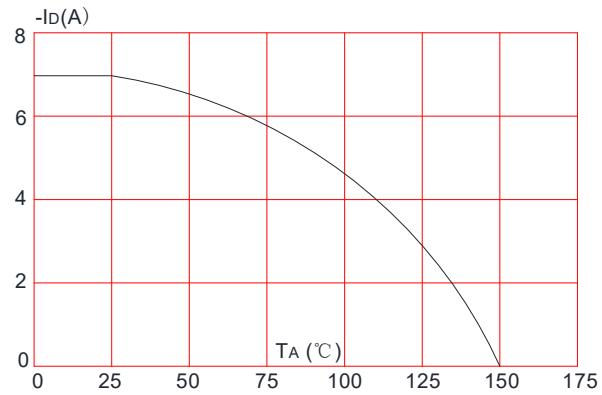
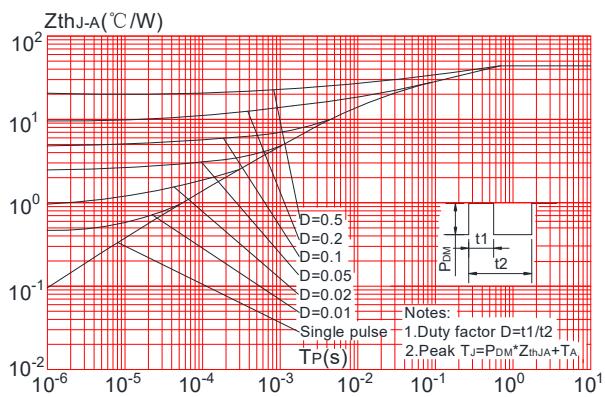
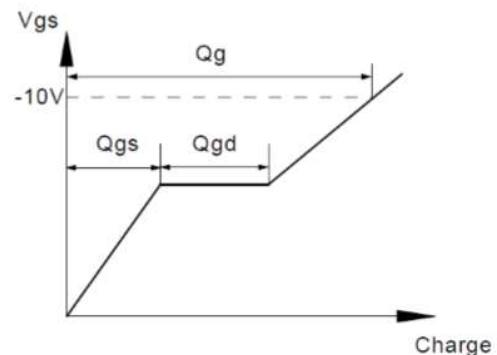
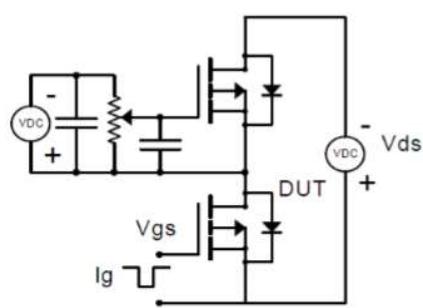


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

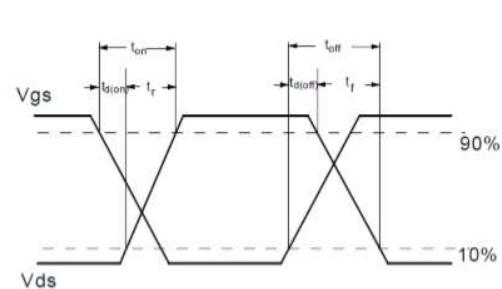
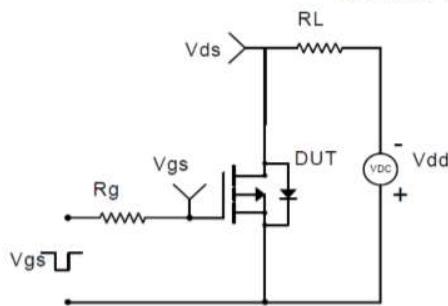


Test Circuit-P

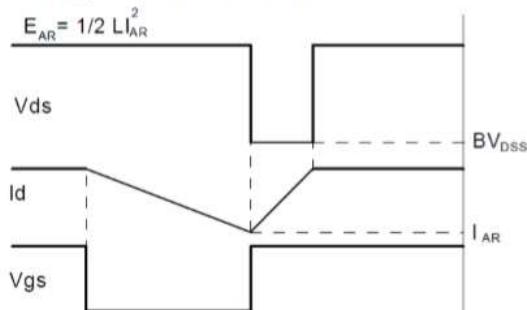
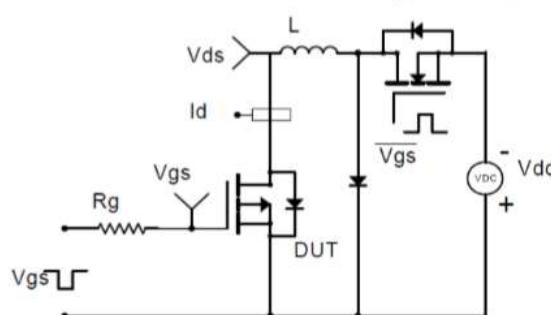
Gate Charge Test Circuit & Waveform



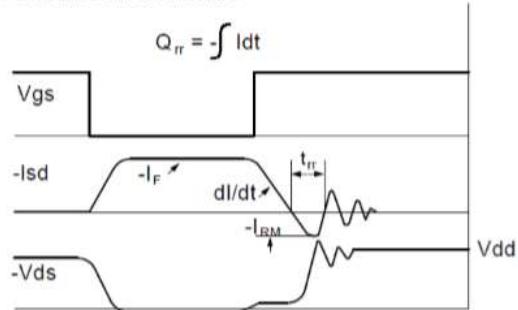
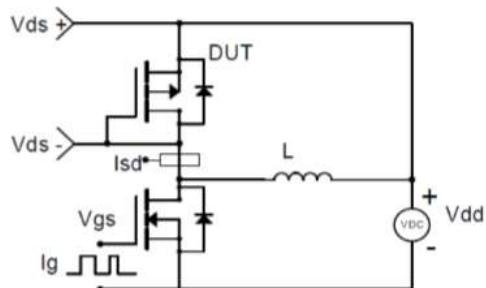
Resistive Switching Test Circuit & Waveforms



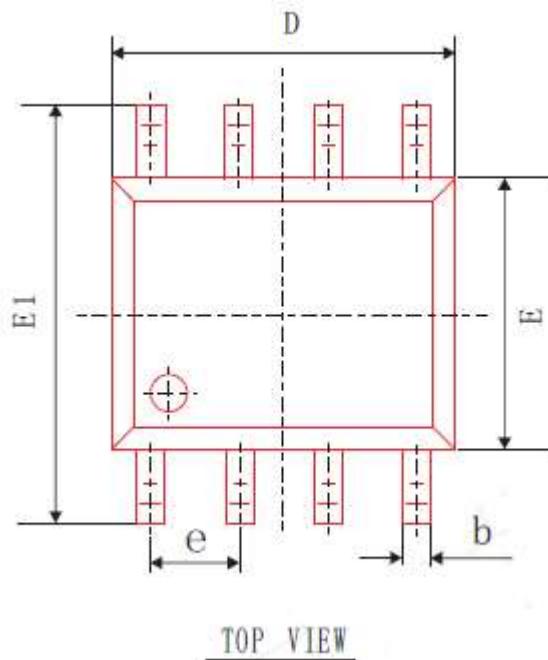
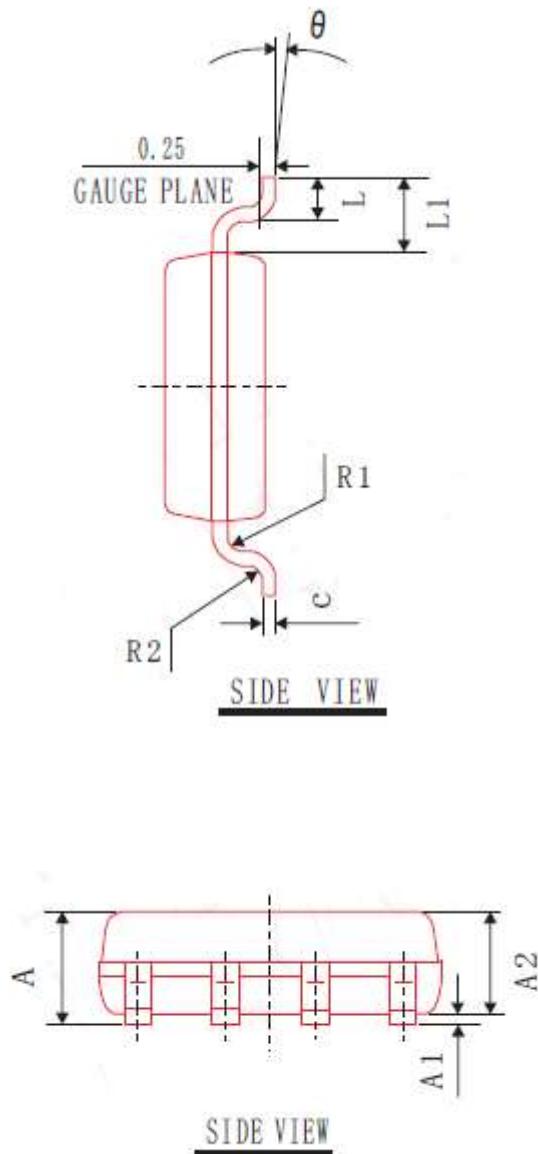
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Mechanical Data-SOP-8



TOP VIEW

SYMBOL	MIN	NOM	MAX
A	1.40	1.60	1.80
A1	0.05	0.15	0.25
A2	1.35	1.45	1.55
b	0.30	0.40	0.50
c	0.153	0.203	0.253
D	4.80	4.90	5.00
E	3.80	3.90	4.00
E1	5.80	6.00	6.20
L	0.45	0.70	1.00
θ	2°	4°	6°
L1		1.04 REF	
e		1.27 BSC	
R1		0.07 TYP	
R2		0.07 TYP	



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