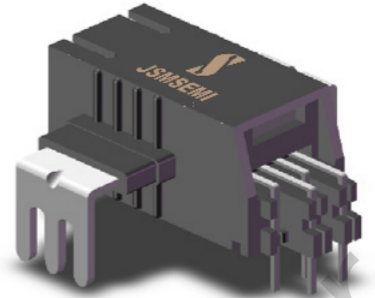


Description:

The SHLSR xx-P series products are open-loop current sensors based High stability, High Precision, Fast Response, Low Noise on the Hall principle. It has advantages such as ultra-low temperature coefficient of 30ppm/K, ultra-low sensitivity error of 0.15%, accuracy @ IPN reaching 0.2%, etc. Suitable for measuring various types and sizes of currents such as DC, AC, pulse, etc.



Features:

- Open-loop current sensor with selectable ranges
- Voltage output
- 5V power supply (SHLSR xx-P)
- Electrical offset voltage $\pm 2\text{mV}$
- Primary conductor and signal terminals electrically isolated
- Temperature coefficient of VOE $\pm 0.05\text{mV}/^\circ\text{C}$
- Gallium Arsenide Packaging Material
- Low power consumption
- Compact design suitable for PCB surface mounting
- Factory calibration
- High bandwidth

Applications:

- Servo motor drives
- DC motor drives
- UPS
- SMPS
- Welding power supplies
- Inverters
- MPPT
- AC variable speed
- Battery supplied applications

Application Circuits

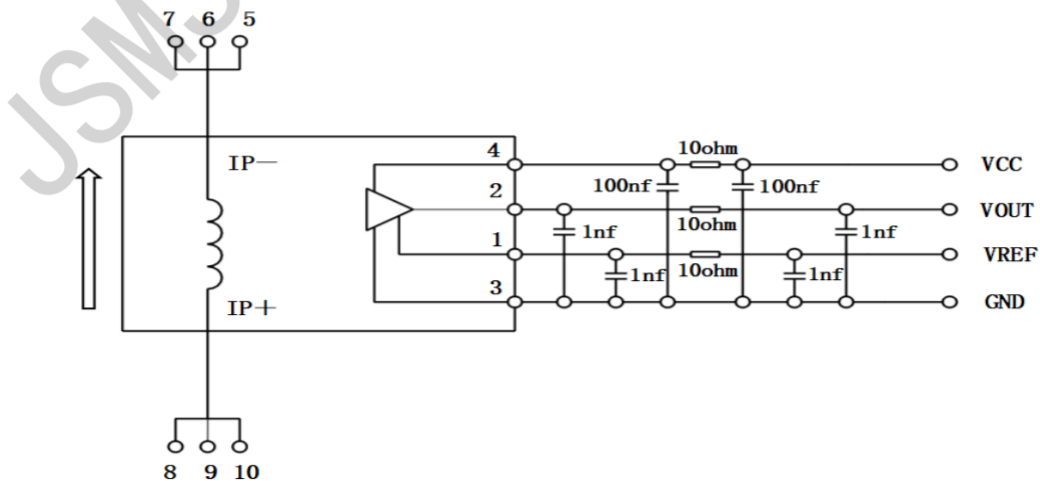


Figure 1. Typical Application Circuit

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Selection Guide:

Part Number	Output	Measuring Current $I_{PR}(A)$	Sensitivity (mV/A)	Operating Temp. Range $T_A (^{\circ}C)$	Packing
			$V_{CC}=5V$		
SHLSR- 10-P	Fixed Output	10	80	-40 to 105°C	Tray
SHLSR- 16-P		16	50		
SHLSR- 20-P		20	40		
SHLSR- 32-P		32	25		
SHLSR- 40-P		40	20		
SHLSR- 50-P		50	16		

1.ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Unit	Comment	Min	Typ	Max
Supply voltage	V_{CC}	V	$T_A=25^{\circ}C$	-0.3	-	6.5
Working current	I_{CC}	mA	$T_A=25^{\circ}C$			40
Ambient Temperature	T_A	$^{\circ}C$		-40		105
Storage temperature	T_s	$^{\circ}C$		-55		150

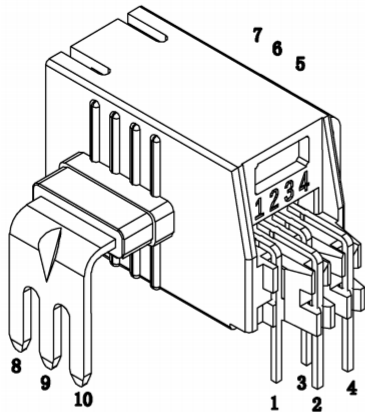
2.ESD CHARACTERISTICS

Parameter	Symbol	Unit	Comment	Min
Manikin	V_{HBM}	kV	ESD between any two pins	± 6

3.ISOLATION CHARACTERISTICS

Parameter	Symbol	Unit	Comment	Min
Isolation and voltage resistance	V_{ISO}	VRMS	Primary and secondary conducting AC voltage 50HZ/1min	4700
Impulse voltage	V_{SURGE}	V	Withstand pulse voltage 1.2/50us	6000
Electrical distance	D_{CL}	mm	The shortest path through air	8
creepage	D_{CR}	mm	Take the shortest path along the device itself	8
Application examples	-	V	Absolute insulation, CAT III, PD 2, compliant with EN 50178 and IEC 61010 in non-uniform scenarios	600
Application examples	-	V	Absolute insulation, CAT III, PD 2, compliant with EN 50178 and IEC 61010 in non-uniform scenarios	1000
Application examples	-	V	CAT III, PD 2, according to UL 508	600

4. TERMINAL LIST & FUNCTIONAL BLOCK



PIN	Pin Definition
1	VREF
2	VOUT
3	GND
4	VCC

Figure 2. Pin diagram

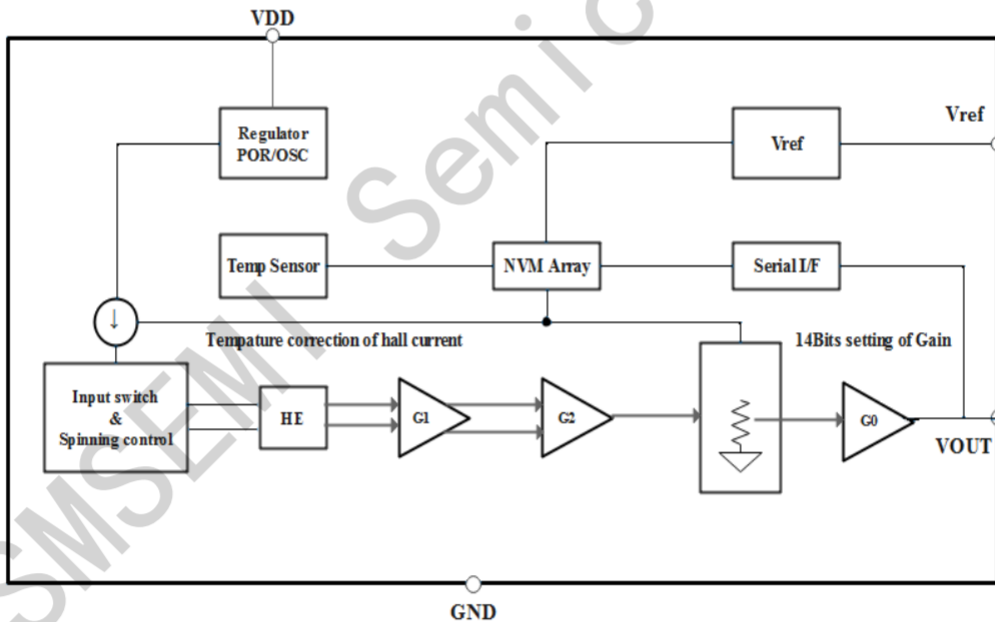


Figure 3. Functional Block Diagram

5.COMMON ELECTRICAL CHARACTERISTICS

SHLSR 10-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		10		
Current measuring range	I_{PM}	A	-25		25	$V_{DD}>4.7V$
Primary conductor turns	N_P			1		
Primary conductive resistance @ $T_A=25^{\circ}C$	R_P	m Ω		0.21		
Primary conductive resistance @ $T_A=105^{\circ}C$	R_P	m Ω		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ $I_P=0$	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-62.5		62.5	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40 $^{\circ}C$ to 105 $^{\circ}C$
Zero temperature drift	TCV_{OE}	mV/K	-0.075		0.075	-40 $^{\circ}C$ to 105 $^{\circ}C$
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-0.94		0.94	-40 $^{\circ}C$ to 105 $^{\circ}C$
Theoretical sensitivity	G_{th}	mV/A		80		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.4		0.4	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/ μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @ $T_A=25^{\circ}C$	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @ $T_A=-40-105^{\circ}C$	X	% of I_{PN}	-2.5		2.5	

SHLSR 16-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		16		
Current measuring range	I_{PM}	A	-40		40	VDD >4.7V
Primary conductor turns	N_P			1		
Primary conductive resistance @ TA=25°C	R_P	mΩ		0.21		
Primary conductive resistance @ TA=105°C	R_P	mΩ		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ IP=0	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-100		100	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40°C to 105°C
Zero temperature drift	TCV_{OE}	mV/K	-0.075		0.075	-40°C to 105°C
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-1.5		1.5	-40°C to 105°C
Theoretical sensitivity	G_{th}	mV/A		50		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.4		0.4	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @TA=25°C	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @TA=-40-105°C	X	% of I_{PN}	-2.5		2.5	

SHLSR 20-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		20		
Current measuring range	I_{PM}	A	-50		50	VDD >4.7V
Primary conductor turns	N_P			1		
Primary conductive resistance @ TA=25 °C	R_P	mΩ		0.21		
Primary conductive resistance @ TA=105 °C	R_P	mΩ		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ IP=0	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-125		125	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40°C to 105°C
Zero temperature drift	TCV_{OE}	mV/K	-0.075		0.075	-40°C to 105°C
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-1.88		1.88	-40°C to 105°C
Theoretical sensitivity	G_{th}	mV/A		40		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.4		0.4	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @TA=25°C	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @TA=-40-105°C	X	% of I_{PN}	-2.5		2.5	

SHLSR 32-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		32		
Current measuring range	I_{PM}	A	-80		80	VDD >4.7V
Primary conductor turns	N_P			1		
Primary conductive resistance @ TA=25 °C	R_P	mΩ		0.21		
Primary conductive resistance @ TA=105 °C	R_P	mΩ		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ $I_P=0$	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-200		200	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40°C to 105°C
Zero temperature drift	TCV_{OE}	mV/K	-0.05		0.05	-40°C to 105°C
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-3		3	-40°C to 105°C
Theoretical sensitivity	G_{th}	mV/A		25		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.4		0.4	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @TA=25°C	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @TA=-40-105°C	X	% of I_{PN}	-2.5		2.5	

SHLSR 40-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		40		
Current measuring range	I_{PM}	A	-100		100	VDD >4.7V
Primary conductor turns	N_P			1		
Primary conductive resistance @ TA=25°C	R_P	mΩ		0.21		
Primary conductive resistance @ TA=105°C	R_P	mΩ		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ IP=0	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-200		200	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40°C to 105°C
Zero temperature drift	TCV_{OE}	mV/K	-0.05		0.05	-40°C to 105°C
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-3		3	-40°C to 105°C
Theoretical sensitivity	G_{th}	mV/A		25		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.4		0.4	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @TA=25°C	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @TA=-40-105°C	X	% of I_{PN}	-2.5		2.5	

SHLSR 50-P Electrical Performance

Parameter	Symbol	Unit	Min	Typ	Max	Comment
Range	I_{PN}	A		50		
Current measuring range	I_{PM}	A	-125		125	VDD >4.7V
Primary conductor turns	N_P			1		
Primary conductive resistance @ TA=25 °C	R_P	mΩ		0.21		
Primary conductive resistance @ TA=105 °C	R_P	mΩ		0.32		
Supply Voltage	V_{DD}	V	4.5	5	5.5	
Drive current	I_C	mA		8	10	
Reference voltage (output)	V_{ref}	V	2.48	2.5	2.52	Internal reference
I_{PM} output voltage range	$V_{out}-V_{ref}$	V	-2		2	
V_{ref} Output resistor	R_{ref}	Ω		1	10	
V_{out} Output resistor	R_{out}	Ω		1	10	
Load Capacitance	C_L	nF		1	10	
Unbalanced voltage @ IP=0	V_{OE}	mV	-5		5	$V_{out}-V_{ref}@V_{ref}=2.5V$
Equivalent offset current	I_{OE}	mA	-313		313	
V_{ref} Temperature drift	TCV_{ref}	ppm/K	-170		170	-40°C to 105°C
Zero temperature drift	TCV_{OE}	mV/K	-0.05		0.05	-40°C to 105°C
I_{OE} Temperature coefficient	TCI_{OE}	mA/K	-3.125		3.125	-40°C to 105°C
Theoretical sensitivity	G_{th}	mV/A		16		800mV@ I_{PN}
Sensitivity error	ϵ_G	%	-0.5		0.5	Factory adjustment
Sensitivity temperature drift	TCG	ppm/K	-200		200	
Linear error 0~ I_{PN}	ϵ_L	% of I_{PN}	-0.4		0.4	
linearity error 0~ I_{PM}	ϵ_L	% of I_{PM}	-0.2		0.2	
Hysteresis equivalent current error	I_{OM}	A	-0.2		0.2	
Response time@90% of I_{PN}	t_r	μs		1.6		@50A/μs
Bandwidth (-3dB)	BW	kHz		250		
Accuracy @ I_{PN} @TA=25°C	X	% of I_{PN}	-1		1	
Accuracy @ I_{PN} @TA=-40-105°C	X	% of I_{PN}	-2.5		2.5	

6. Parameter Description

6.1 Sensitivity S_{Sens}

Definition: The output of a Hall current sensor changes with the I_p passing through the primary conductor. Sensitivity S_{Sens} is the product of magnetic circuit sensitivity (G_s/A ; $1G_s = 0.1mT$) and linear I C sensitivity (mV/G_s).

The gain of linear ICs can be programmed before leaving the factory, ensuring high-precision output of current sensors with different ranges at different operating temperatures.

6.2 Sensitivity error E_{Sens}

Definition: Sensitivity error E_{Sens} is the percentage of deviation between actual sensitivity and ideal sensitivity.

For example, when $V_{CC}=5V$:

$$E_{\text{Sens}} = \frac{S_{\text{Sens Meas}(5V)} - S_{\text{Sens Ideal}(5V)}}{S_{\text{Sens IDEAL}(5V)}} \times 100\%$$

6.3 Sensitivity temperature drift $\Delta S_{\text{Sens TC}}(\%)$

The sensitivity temperature drift within the entire temperature range is defined as:

$$\Delta S_{\text{Sens TC}} = \frac{S_{\text{Sens TA}} - S_{\text{Sens EXPECTED(TA)}}}{S_{\text{Sens EXPECTED(TA)}}} \times 100\%$$

6.4 Saturated output voltage $V_{\text{OUT-SAT(H/L)}}$

Definition: The maximum output of the chip under a positive magnetic field when $V_{\text{OUT-SAT(H)}}$ is $I_{\text{OUT}}=2.0/0.5mA$;

The minimum output of the chip under negative magnetic field when $V_{\text{OUT-SAT(L)}}$ is $I_{\text{OUT}}=2.0/0.5mA$.

6.5 Zero point output voltage $V_{\text{IOUT(Q)}}$

The output voltage $V_{\text{IOUT(Q)}}$ of the sensor when $I_p=0$.

For bidirectional devices, the output voltage $V_{\text{IOUT(Q)}}=V_{CC} \times 0.5$;

For unidirectional devices, the output voltage $V_{\text{IOUT(Q)}}=V_{CC} \times 0.1$.

The variation of $V_{\text{IOUT(Q)}}$ can be adjusted by the built-in IC programming combined with the variation of temperature drift.

6.6 Zero offset voltage V_{OE}

Used to measure the influence of external non-magnetic factors, it refers to the relationship between the actual output voltage and the static voltage.

6.7 Zero point output voltage temperature $\Delta V_{\text{OUT(Q)TC}}(V)$

Due to internal component tolerances and heat dissipation, the static output voltage $V_{\text{OUT(Q)}}$ may deviate by $\Delta V_{\text{OUT(Q)TC}}$ with changes in operating temperature.

It is defined as:

$$\Delta V_{\text{OUT(Q)TC}} = V_{\text{OUT(Q)(TA)}} - V_{\text{OUT(Q)EXPECTED(TA)}}$$

$\Delta V_{\text{OUT(Q)TC}}$ should be calculated using actual test values and expected values, rather than programmed target values.

6.8 Noise V_N

Definition: Noise is the macroscopic sum of internal thermal noise, granular noise, etc. in a current sensor. The minimum current that the device can resolve can be obtained by dividing the noise (mV) by the sensitivity (mV/A).

6.9 Symmetry E_{SYM}

Definition: The relationship between the actual output voltage and the forward half range and reverse half range outputs. Formula:

$$E_{\text{SYM}} = 100\% \left\{ \frac{V_{\text{IOUT+half-scale amperes}} - V_{\text{IOUT(Q)}}}{V_{\text{IOUT(Q)}} - V_{\text{IOUT-half-scale amperes}}} \right\}$$

6.10 Nonlinearity E_{LIN}

The design output of this device shows a linear relationship with the measured current.

Ideally, under the same voltage and temperature conditions, the output sensitivity of the device is the same for two different current levels I_1 and I_2 . However, in reality, there is a difference in sensitivity for measuring two different current levels I_1 and I_2 , and nonlinearity E_{LIN} is a description of this difference.

The definitions of positive current nonlinearity E_{LINPOS} and negative current nonlinearity E_{LINNEG} are as follows:

$$E_{\text{LINPOS}} = 100(\%) \times \{ 1 - (S_{\text{Sens IPOS2}} / S_{\text{Sens IPOS1}}) \}$$

$$E_{\text{LINNEG}} = 100(\%) \times \{ 1 - (S_{\text{Sens INEG2}} / S_{\text{Sens INEG1}}) \}$$

AS

$$S_{\text{Sens Ix}} = (V_{\text{IOUT(Ix)}} - V_{\text{IOUT(Q)}}) / I_x$$

I_{POSx} , I_{NEGx} are positive and negative currents

$$I_{\text{POS2}} = 2 \times I_{\text{POS1}}$$

$$I_{\text{NEG2}} = 2 \times I_{\text{NEG1}}$$

Due to the hysteresis effect of the magnetic core, there is magnetic saturation at high currents. Therefore, when the measured current exceeds 200A, the nonlinear error will increase.

6. Parameter Description (Continued)

6.11 Nonlinearity error ρ [%F.S.] (%)

The ratio of the maximum vertical difference between the B-VOUT curve (fitted by least squares method) and the measured curve to the difference in full-scale output voltage (VH-VL). Calculation formula: $\rho = 100 * MFD / F.S. = 100 * MFD / (V_H - V_L)$

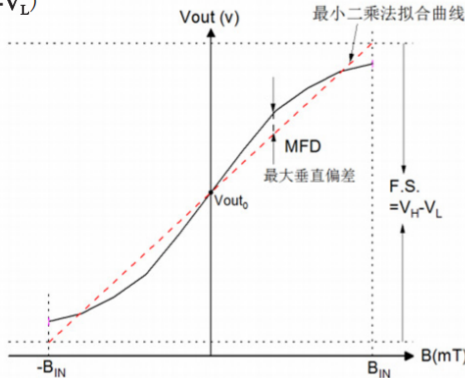


Figure 3. Schematic diagram of linearity calculation

6.12 Magnetic offset error (I_{ERROR})

Magnetic offset is caused by the residual magnetism of the magnetic core material. The magnetic offset error is maximum when the magnetic circuit is saturated, usually when the device is at full range or in a current overload state. The magnetic offset error largely depends on the magnetic core material. Usually, the lower the temperature, the greater the magnetic offset error.

6.13 Total output error (E_{TOT})

Definition: The difference between the test current corresponding to the output and the actual current (I_P) (equivalent to the difference between the ideal output voltage and the actual output voltage), divided by the product of the ideal sensitivity and the primary conductor current:

$$E_{TOT(I_P)} = + \frac{V_{I_{OUT}(I_P)} - V_{I_{OUT}(ideal)(I_P)}}{Sens_{\phi(ideal)} \times I_{PM}} \times 100\%$$

Among them, the total output error E_{TOT} includes all error sources and is a function of I_P

$$V_{I_{OUT}(ideal)(I_P)} = V_{I_{OUT}(Q)} + (Sens_{IDEAL} \times I_P)$$

At relatively high currents, E_{TOT} is mainly sensitivity error, while at relatively low currents, E_{TOT} is mainly bias voltage (V_{OE}). When the I_P approaches zero, calculate E_{TOT} to approach infinity.

6.14 Dynamic response characteristics

6.14.1 Power on delay (T_{POD})

When the power supply rises to the operating voltage, the device needs a limited period of time to supply power to internal components before it can respond to the measured magnetic field. The power on delay T_{POD} is defined as the time required for the output voltage to stabilize within a range of $\pm 10\%$ under the action of an external magnetic field after the power supply reaches its minimum specified operating voltage V_{CC}, as shown in the figure.

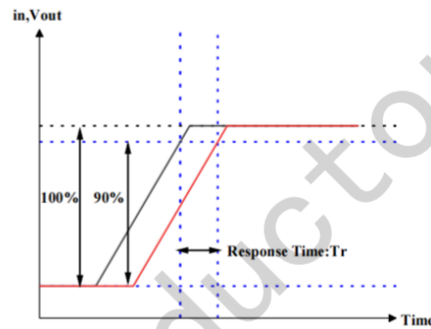


Figure 4. Schematic diagram of time definitions for dynamic corresponding characteristics

6.14.2 Rise time (T_r)

The time interval between the sensor reaching 10% of its full-scale output and 90% of its full-scale value.

6.14.3 Transmission delay (T_{PROP})

The time interval between the measured current reaching 20% of its full value and the sensor output reaching 20% of its full output.

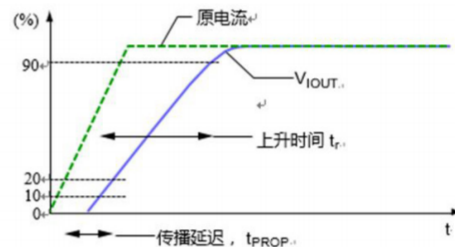


Figure 5. Rise time (Tr) and propagation delay (T_{PROP})

6.14.4 Response time (T_{RESPONSE})

The time interval between when the measured current reaches 90% of its full value and when the sensor reaches its corresponding full output of 90%.

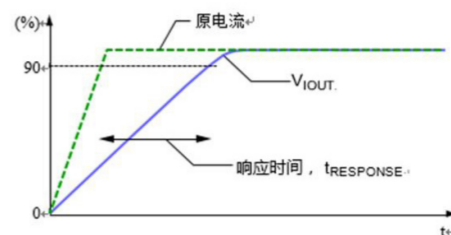


Figure 6. Response time (T_{RESPONSE})

7. Electrical characteristic diagram

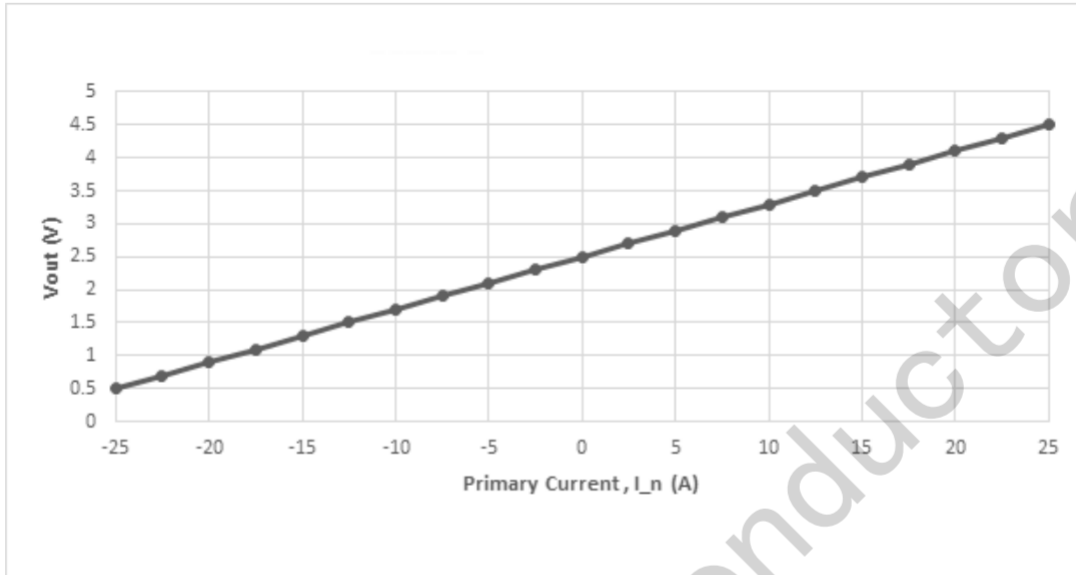


Figure 5. SHLSR 10-P output characteristic curve

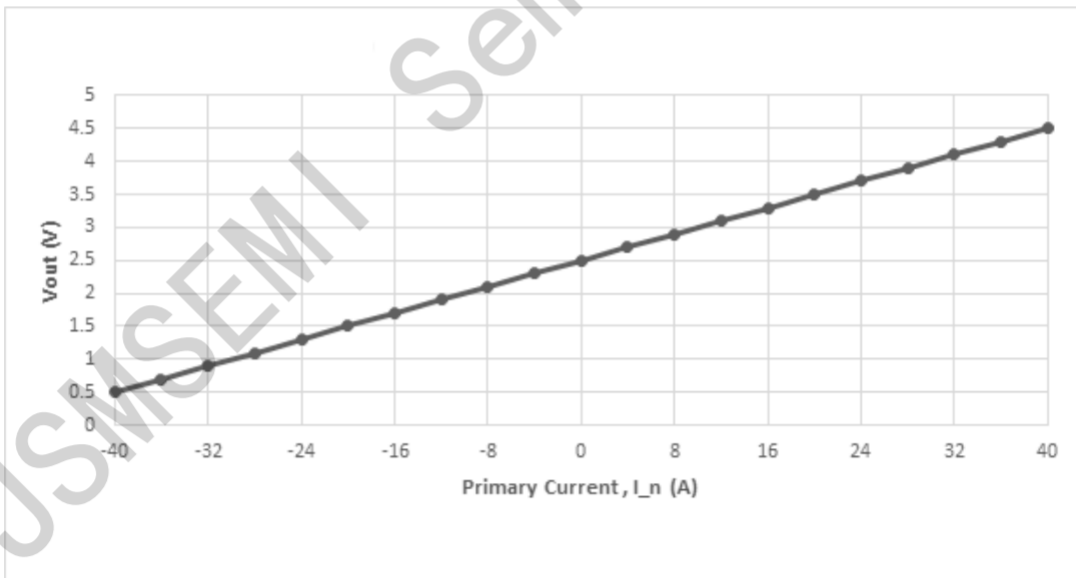


Figure 6. SHLSR 16-P output characteristic curve

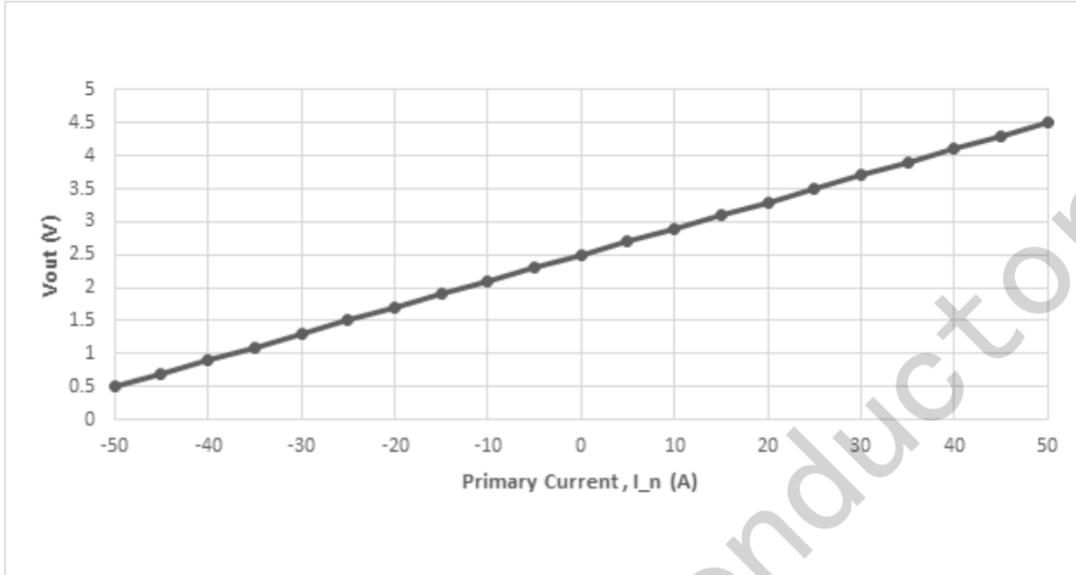


Figure 7. SHLSR 20-P output characteristic curve

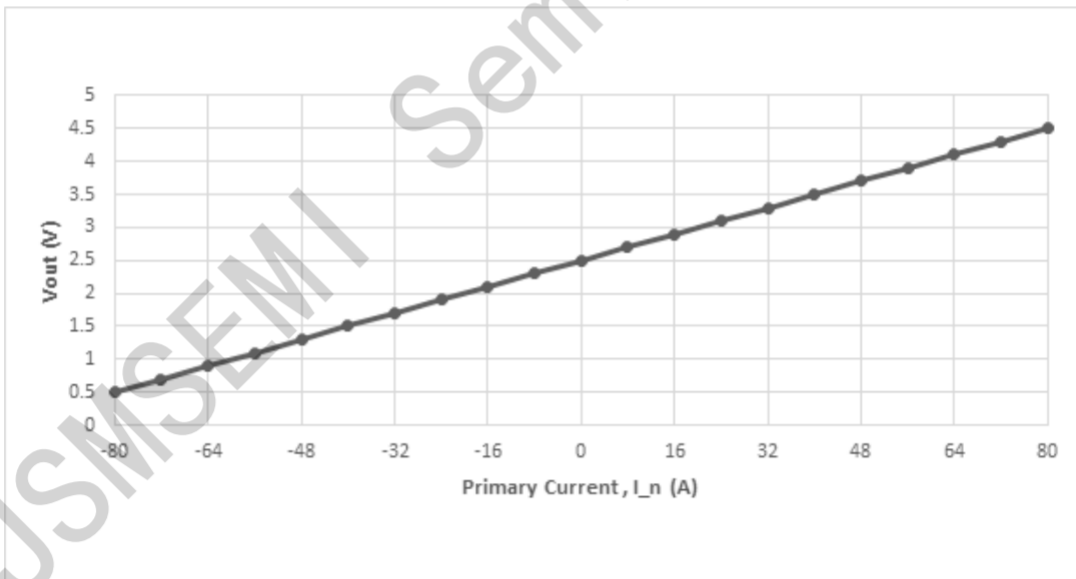


Figure 8. SHLSR 32-P output characteristic curve

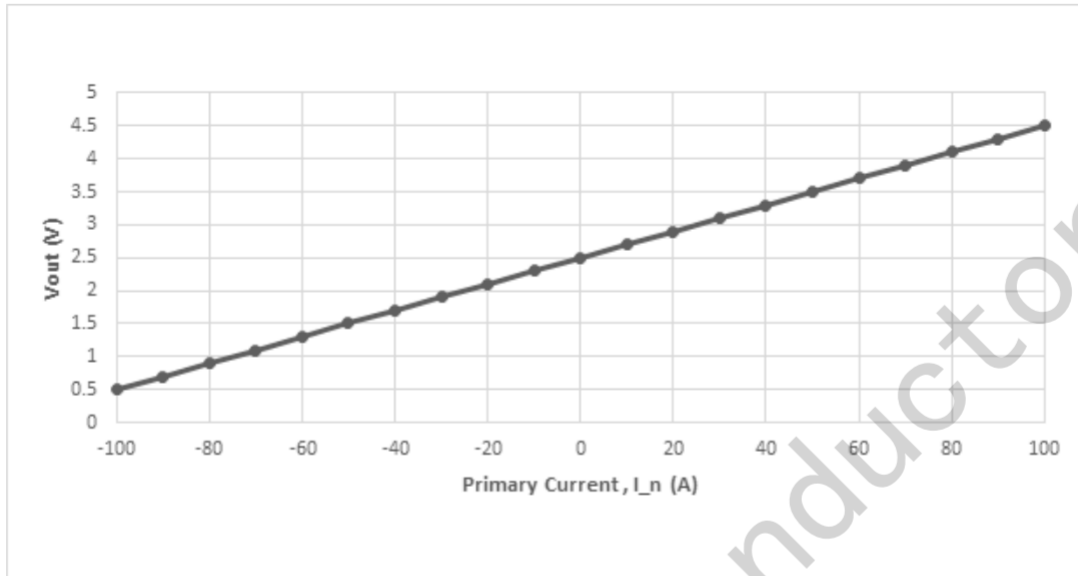


Figure 9. SHLSR 40-P output characteristic curve

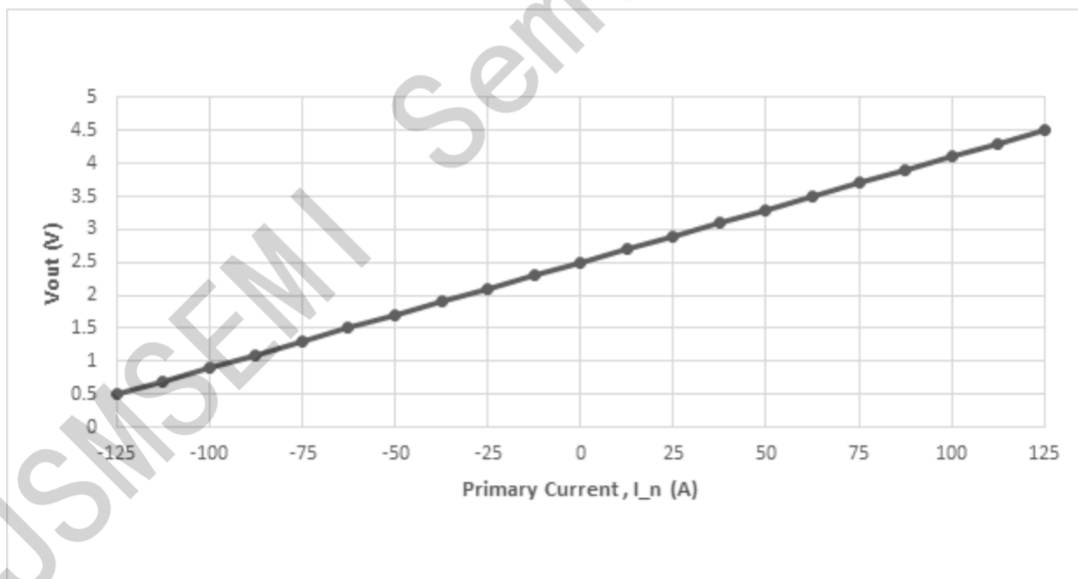


Figure 10. SHLSR 50-P output characteristic curve

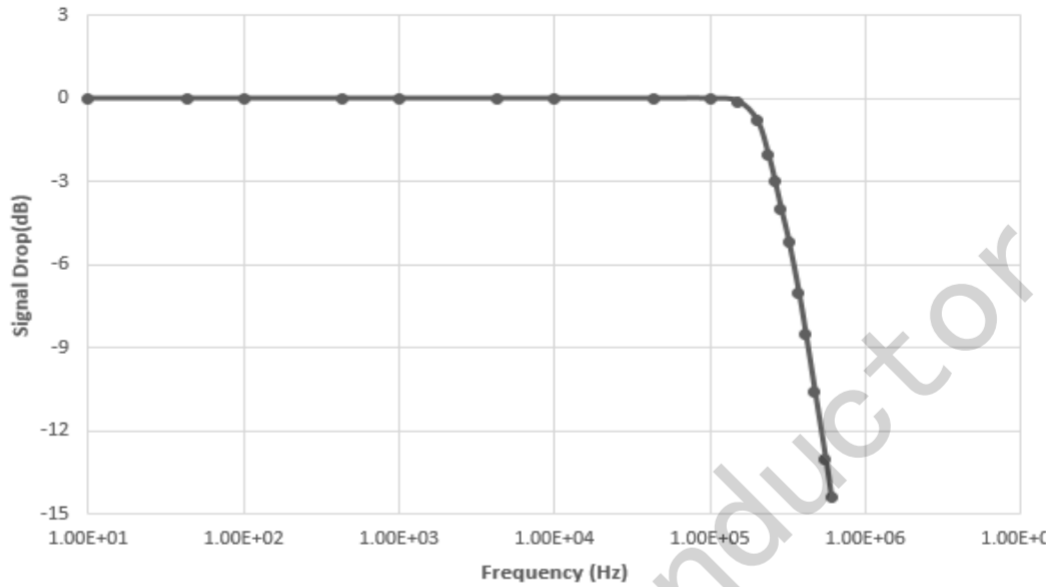


Figure 11. Amplitude frequency characteristic curve

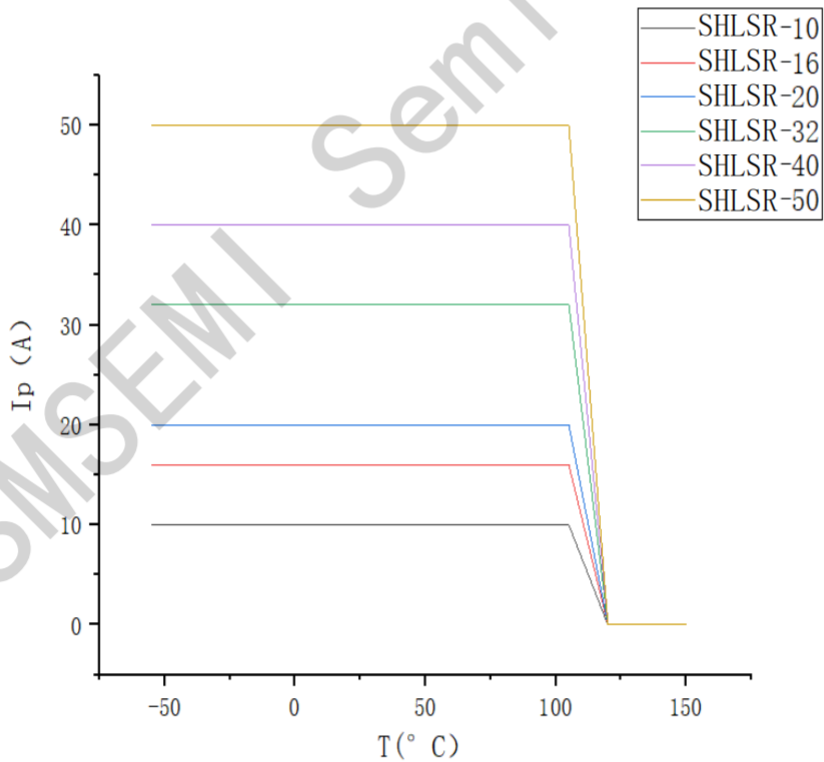


Figure 12. Maximum Continuous DC Test Current

8. Packaging outline drawing

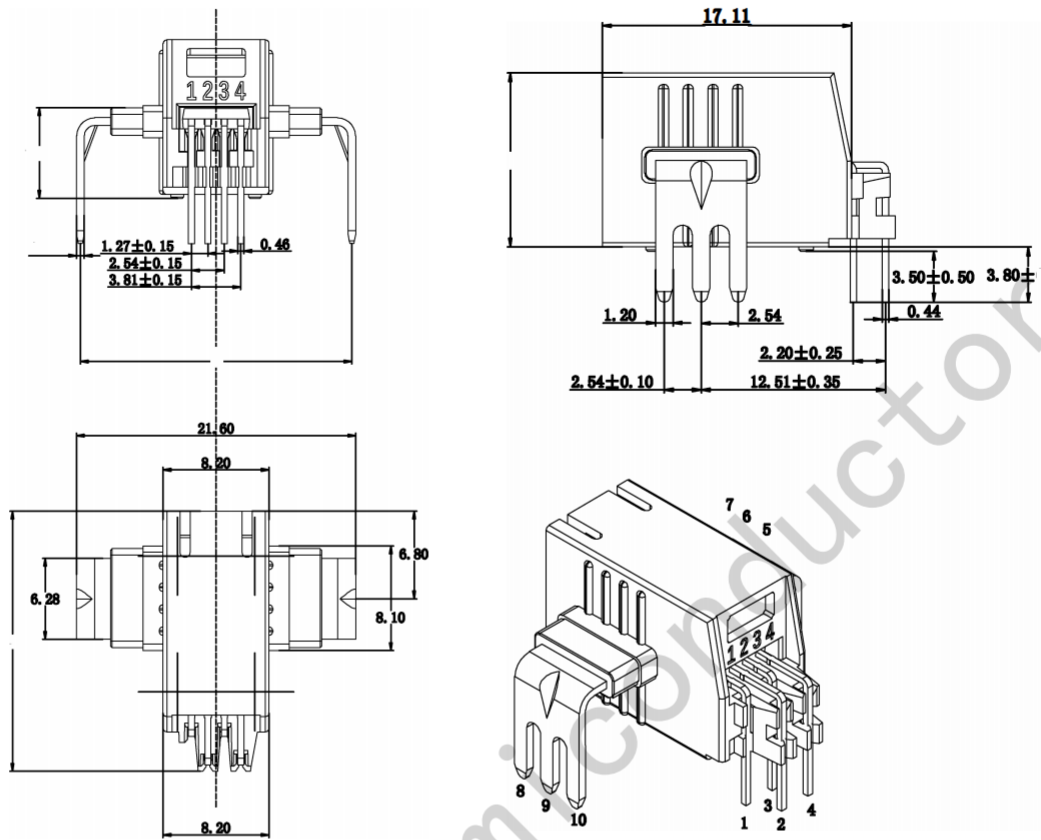


Figure 13. Appearance dimension diagram

Installation of view: overlooking (unit: mm)

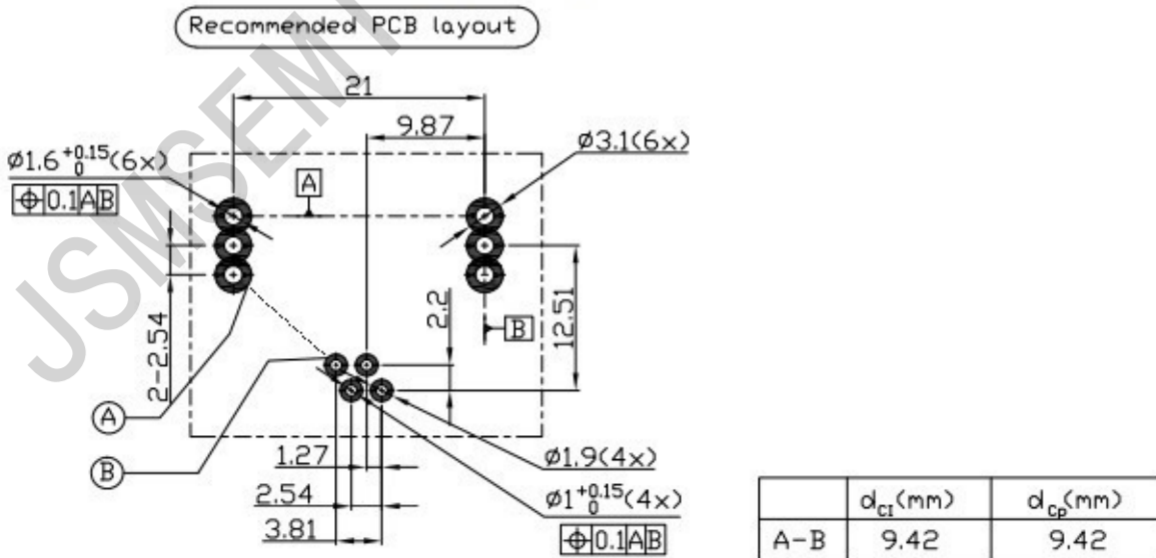


Figure 14. PCB Dimensional Diagram

9. Packaging and storage

9.1 Packaging specifications

Palletizing

9.2 Storage method

9.2.1 The product should be stored in an appropriate temperature and humidity environment (5 to 35 °C, 40% to 85% RH), and kept away from chlorine and corrosive gases.

9.2.2 Even under appropriate conditions, long-term storage may lead to a decrease in the weldability and electrical performance of the product. For products stored for a long time, their weldability should be checked before use.

9.2.3 If stored for more than 2 years, it is recommended to store in a nitrogen environment. The oxygen in the atmosphere will oxidize the leads of the product, resulting in a decrease in the weldability of the leads.

10. Safety warning

10.1 This product is sensitive to ESD (electrostatic discharge). When in contact with Hall elements marked with ESD Caution, the environmental requirements are as follows:

10.1.1 Static charges are unlikely to occur in the environment (e.g. relative humidity exceeding 40% RH).

10.1.2 When in contact with products, anti-static clothing and wristbands should be worn.

10.1.3 Implement anti-static measures for equipment or containers that come into direct contact with the product.

10.2 Do not turn the product into gas, powder, or liquid through combustion, crushing, or chemical treatment.

10.3 When discarding this product, please comply with laws and company regulations.

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