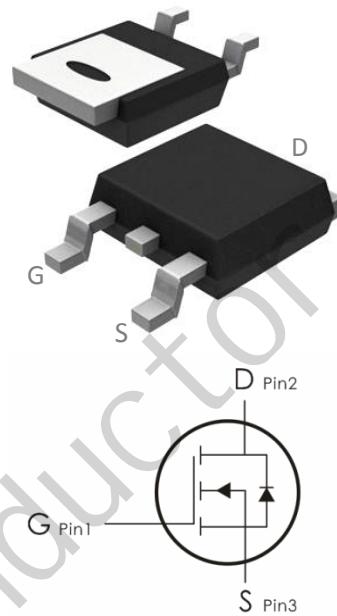


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=60V, I_D=20A, R_{DS(ON)}<36m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	20	A
	Continuous Drain Current- $T_C=100^\circ C$	13	
I_{DM}	Pulsed Drain Current ^{note1}	80	
E_{AS}	Single Pulse Avalanche Energy ^{note2}	40	mJ
P_D	Power Dissipation, $T_C=25^\circ C$	31	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	4	$^\circ C/W$

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

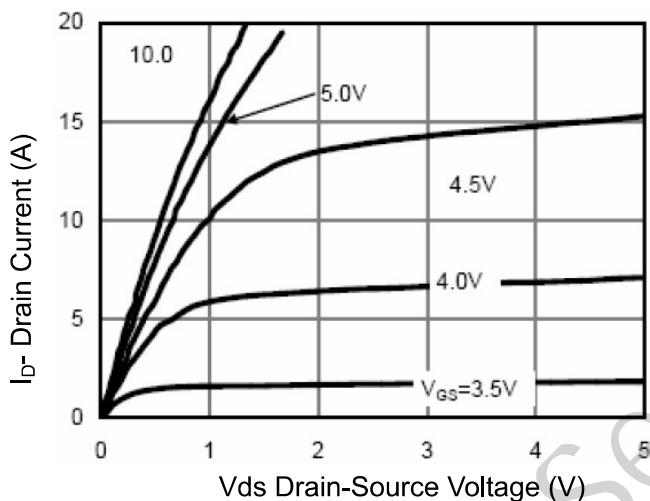
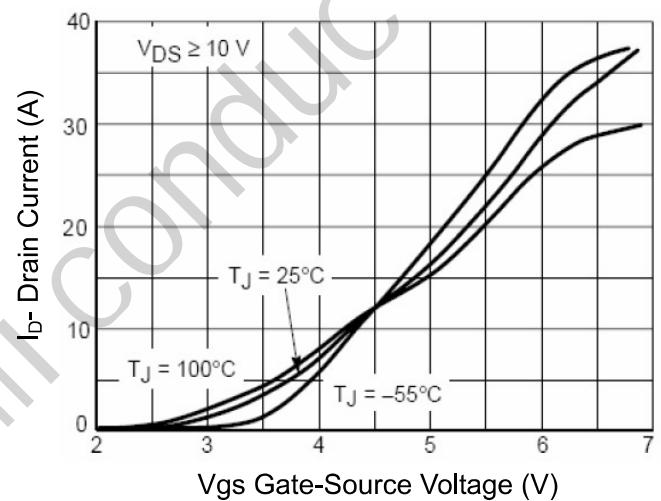
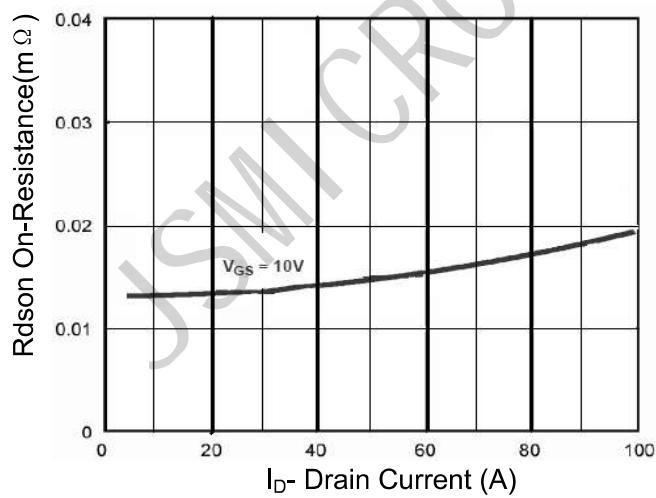
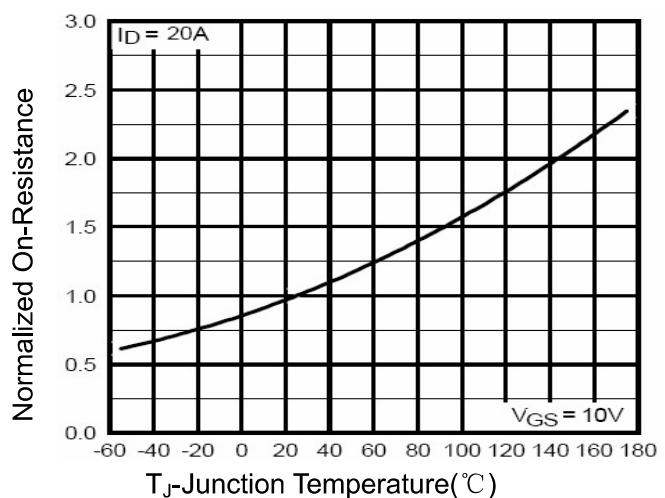
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
$\mathbf{BV_{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}, I_D=250\ \mu\text{A}$	60	---	---	V
$\mathbf{I_{DSS}}$	Zero Gate Voltage Drain Current	$V_{GS}=0\text{V}, V_{DS}=60\text{V}$	---	---	1	μA
$\mathbf{I_{GSS}}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{A}$	---	---	± 100	nA
On Characteristics						
$\mathbf{V_{GS(th)}}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	1	1.6	3	V
$\mathbf{R_{DS(on)}}$	Drain-Source On Resistance ^{note3}	$V_{GS}=10\text{V}, I_D=10\text{A}$	---	26	36	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=5\text{A}$	---	36	45	$\text{m}\Omega$
Dynamic Characteristics						
$\mathbf{C_{iss}}$	Input Capacitance	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	---	1150	---	pF
$\mathbf{C_{oss}}$	Output Capacitance		---	55	---	
$\mathbf{C_{rss}}$	Reverse Transfer Capacitance		---	45.3	---	
Switching Characteristics						
$\mathbf{t_{d(on)}}$	Turn-On Delay Time	$V_{DS}=30\text{V}, I_D=15\text{A}, V_{GS}=10\text{V}, R_{GEN}=1.8\Omega$	---	7.6	---	ns
$\mathbf{t_r}$	Rise Time		---	20	---	ns
$\mathbf{t_{d(off)}}$	Turn-Off Delay Time		---	15	---	ns
$\mathbf{t_f}$	Fall Time		---	24	---	ns
$\mathbf{Q_g}$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=30\text{V}, I_D=10\text{A}$	---	20.3	---	nC
$\mathbf{Q_{gs}}$	Gate-Source Charge		---	3.7	---	nC
$\mathbf{Q_{gd}}$	Gate-Drain "Miller" Charge		---	5.3	---	nC
Drain-Source Diode Characteristics						
$\mathbf{V_{SD}}$	Source-Drain Diode Forward Voltage	$V_{GS}=0\text{V}, I_S=20\text{A}$	---	---	1.2	V
$\mathbf{I_S}$	Maximum Continuous Drain to Source Diode Forward Current	---	---	20	A	
$\mathbf{I_{SM}}$	Maximum Pulsed Drain to Source Diode Forward Current	---	---	80	A	

trr	Reverse Recovery Time	$I_F = 10A$, $di/dt = 100A/\mu s$	---	29	---	Ns
qrr	Reverse Recovery Charge		---	43	---	nc

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition : $T_J=25^\circ C$, $V_{DD}=30V$, $V_G=10V$, $L=0.5mH$, $R_g=25\Omega$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_c=25^\circ C$ unless otherwise noted)


Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 Rdson - Drain Current

Figure 4 Rdson - Junction Temperature

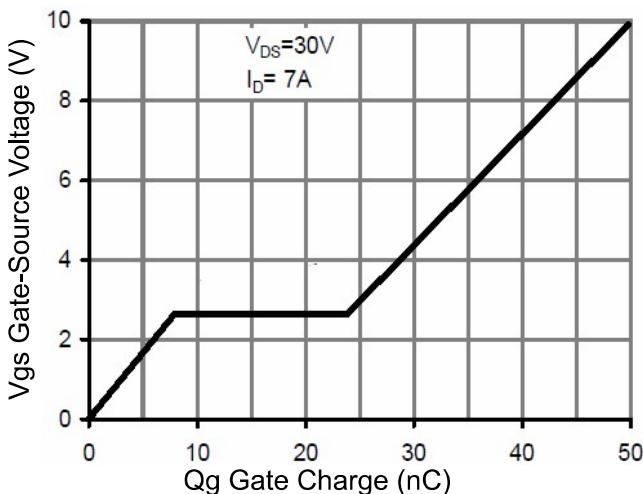


Figure 5 Gate Charge

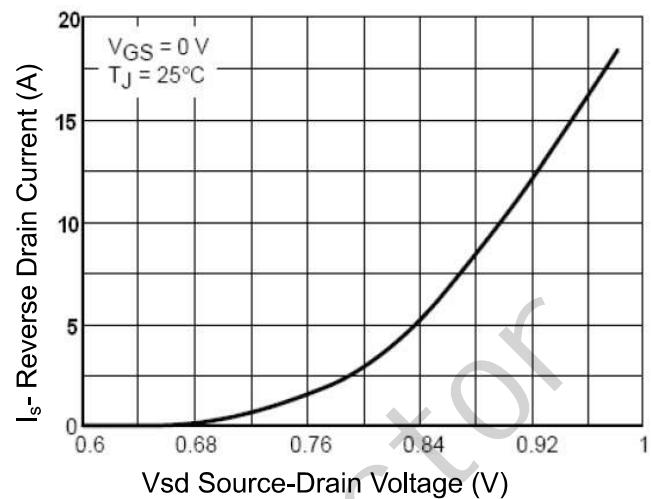


Figure 6 Source-Drain Diode Forward

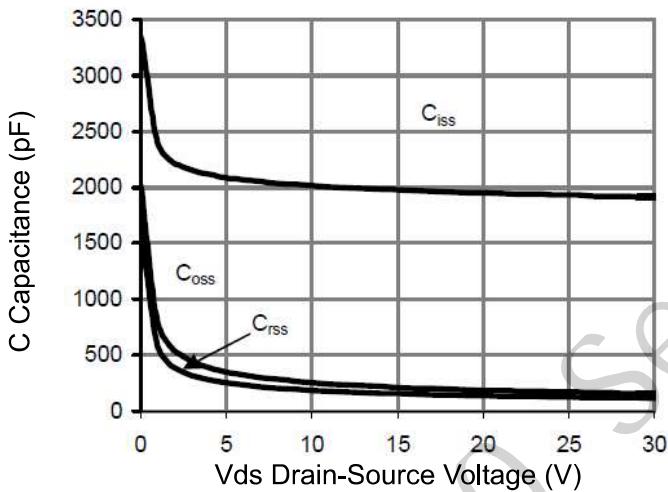


Figure 7 Capacitance vs Vds

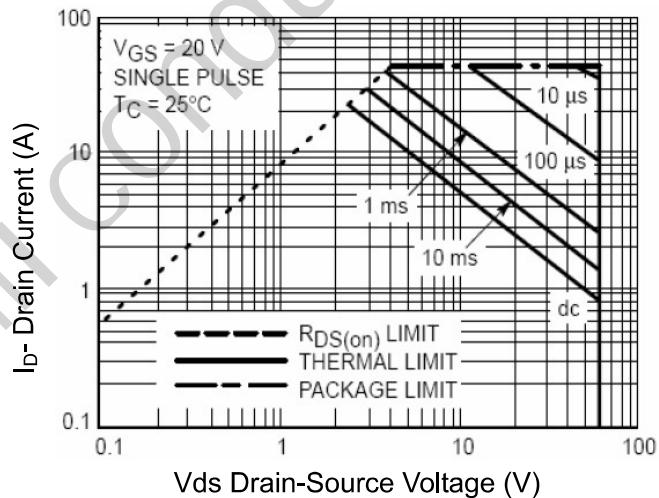


Figure 8 Safe Operation Area

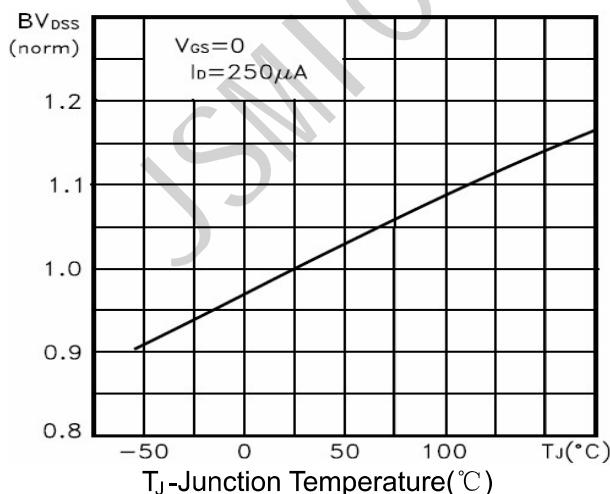


Figure 9 BV vs Junction Temperature

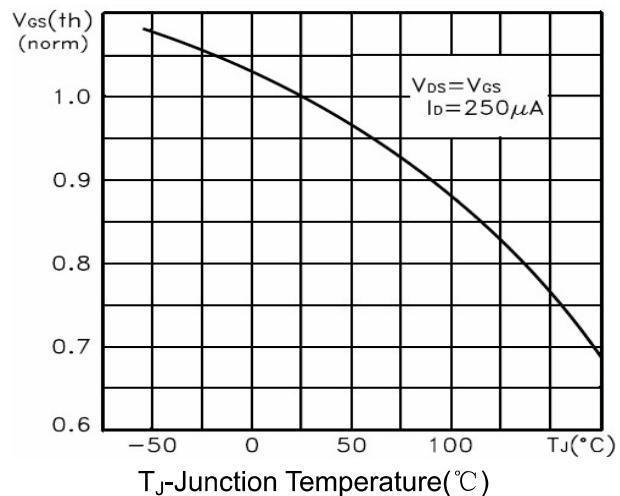


Figure 10 $V_{GS(th)}$ vs Junction Temperature

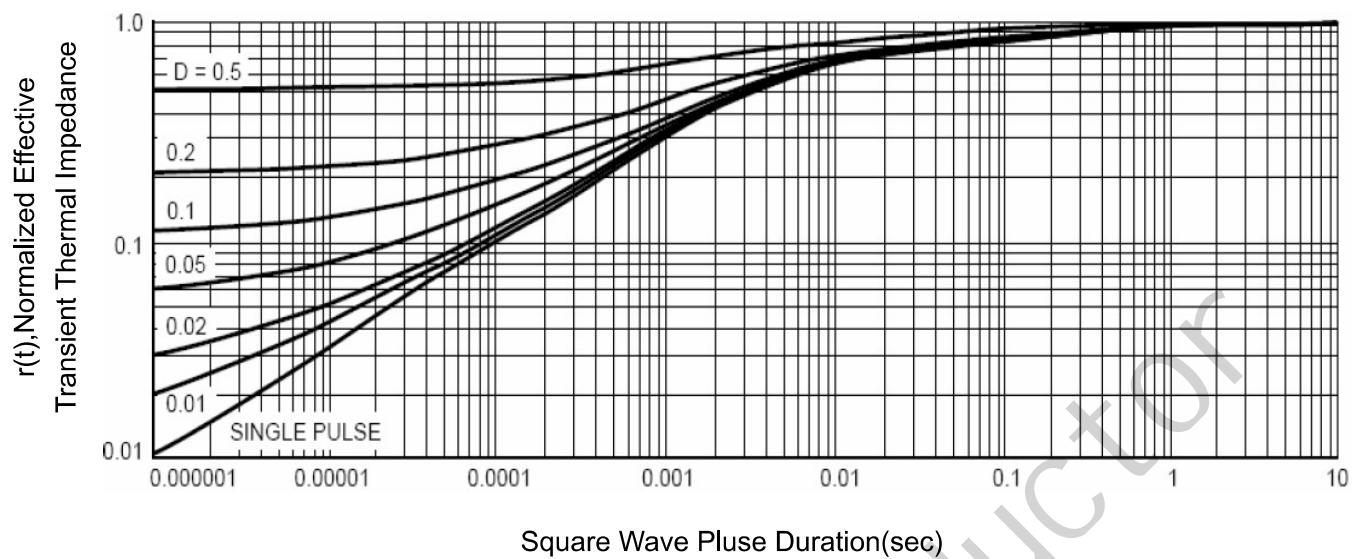
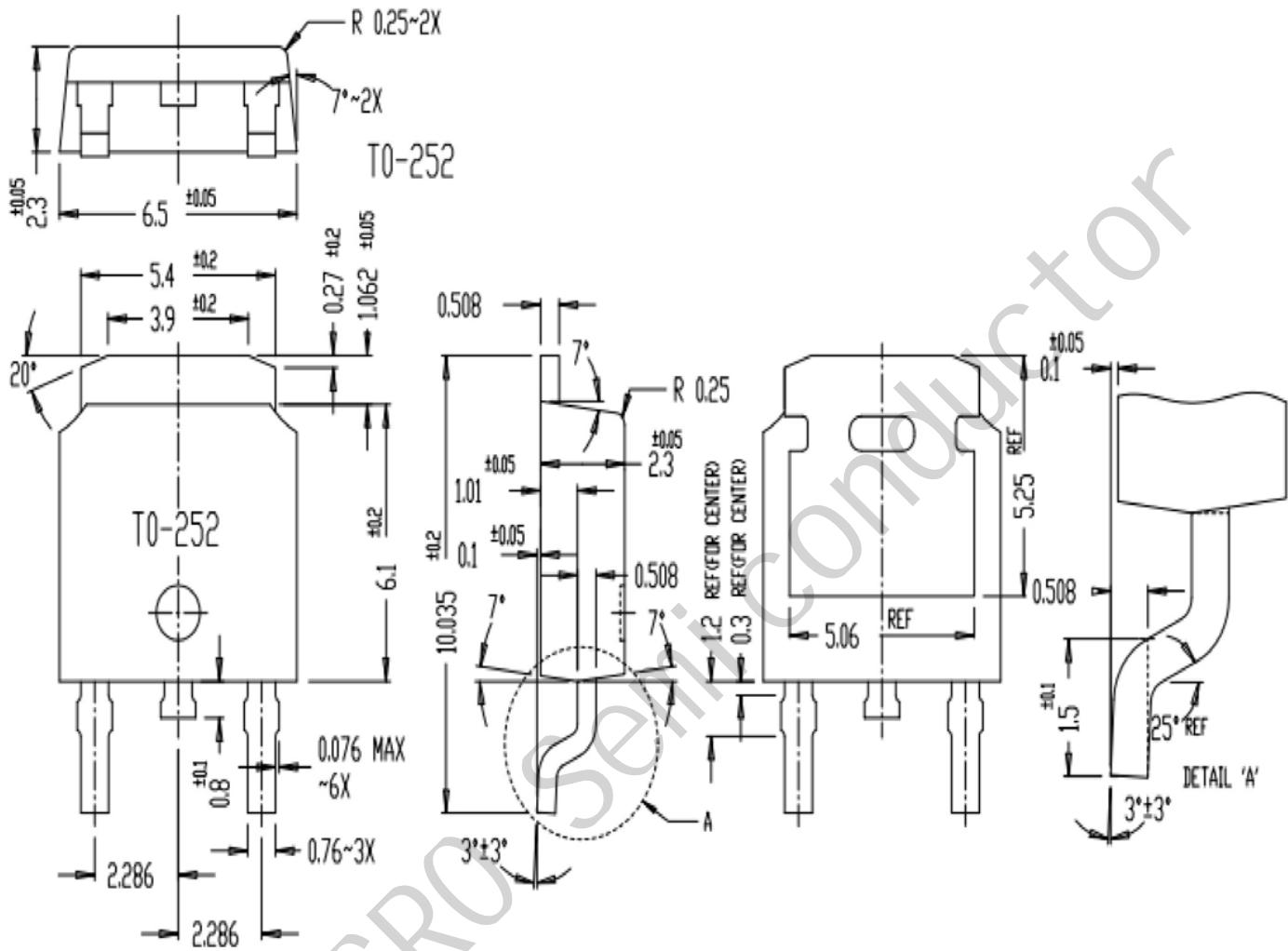


Figure 11 Normalized Maximum Transient Thermal Impedance

Package Mechanical Data: TO-252-3L



单击下面可查看定价，库存，交付和生命周期等信息

[>>JSMSEMI\(杰盛微\)](#)