

Click here for the 3D model.

Dimensions Footprint

L

W

Н

S

F

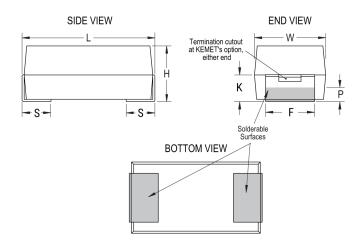
Κ

Ρ

## T497A225K010AT6110

General Information

T497 HRA, Tantalum, MnO2 Tantalum, HRA, 2.2 uF, 10%, 10 VDC, SMD, MnO2, Molded, High Reliability, Medical, N/A, 12 Ohms, 2513, Height Max = 1.65mm



Series T497 HRA MnO2 Tantalum Dielectric Style SMD Chip Description SMD, MnO2, Molded, High Reliability, Medical Features High Reliability, Medical RoHS Yes Termination Tin Qualifications CWR09/19/29 Style AEC-Q200 No Component 39.91 mg Weight Note: When solder coated terminations are Notes required, add an additional 0.38mm (0.015inch) to the tolerances for "L", "W", "H", "K", "F" and "S". MSL 1

2513	Specifications	
2.54mm +/-0.38mm	Capacitance	2.2 uF
1.27mm +/-0.38mm	Capacitance Tolerance	10%
1.27mm +/-0.38mm	Voltage DC	10 VD0
0.76mm +0.25/-0.13mm	Temperature Range	-55/+1
1.27mm +/-0.13mm	Rated Temperature	85°C
0.76mm MIN	Humidity	85C, 8
0.38mm MIN	Dissipation Factor	6% 120
	Failure Rate	N/A
	Resistance	12 Ohn

Packaging Specifications	
Packaging	T&R, 178mm
Packaging Quantity	2500

Specifications	
Capacitance	2.2 uF
Capacitance Tolerance	10%
Voltage DC	10 VDC (85C), 6.7 VDC (125C)
Temperature Range	-55/+125°C
Rated Temperature	85°C
Humidity	85C, 85% RH, 1000 Hours, No Load
Dissipation Factor	6% 120Hz 25C
Failure Rate	N/A
Resistance	12 Ohms (100kHz 25C)
Leakage Current	0.5 uA (5min 25°C)
Testing and Reliability	Standard Testing Only

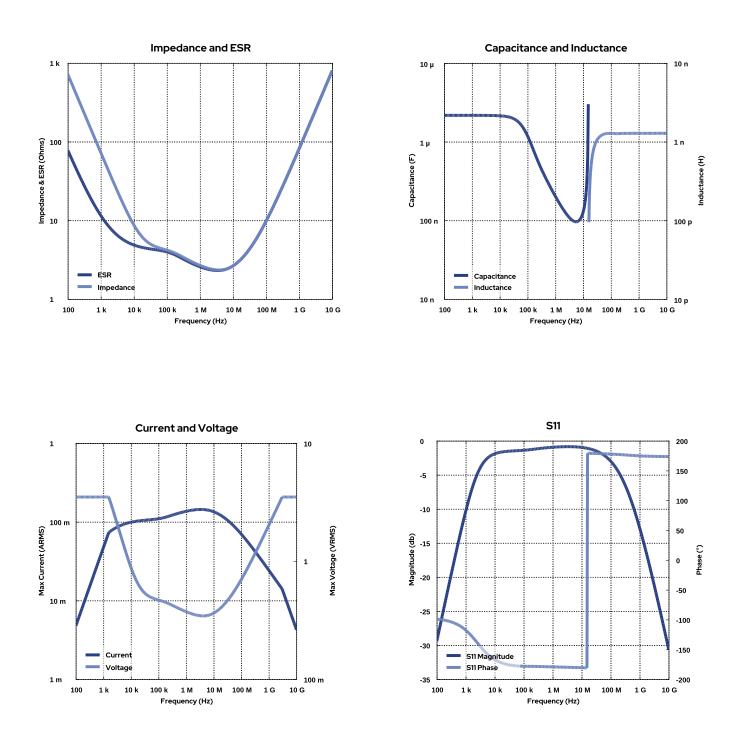
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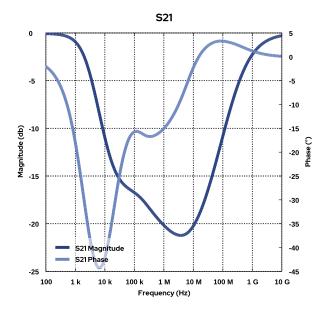
## Simulations

For the complete simulation environment please visit K-SIM.





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## These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.

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