

SMD COTS COG, Ceramic, 180 pF, 10%, 50 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 0603



| Dimensions | |
|------------|------------------|
| Chip Size | 0603 |
| L | 1.6mm +/-0.15mm |
| W | 0.8mm +/-0.15mm |
| T | 0.8mm +/-0.07mm |
| S | 0.7mm MIN |
| B | 0.35mm +/-0.15mm |

| Packaging Specifications | |
|--------------------------|-----------|
| Packaging: | Bulk, Bag |
| Packaging Quantity: | 1 |

| General Information | |
|---------------------|---|
| Series: | SMD COTS COG |
| Style: | SMD Chip |
| Description: | SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I |
| Features: | Ultra-Stable, Low Loss, Class I |
| RoHS: | No |
| Prop 65: | WARNING: Cancer and reproductive harm - www.p65warnings.ca.gov . |
| SCIP Number: | 2d771165-5336-48a3-96fa-3663929fd828 |
| Termination: | Lead (SnPb) |
| Marking: | No |
| Failure Rate: | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469 |
| AEC-Q200: | No |
| Component Weight: | 3700 ug |
| Shelf Life: | 78 Weeks |
| MSL: | 1 |

| Specifications | |
|---|---------------------------|
| Capacitance: | 180 pF |
| Measurement Condition: | 1 MHz 1.0Vrms |
| Capacitance Tolerance: | 10% |
| Voltage DC: | 50 VDC |
| Dielectric Withstanding Voltage: | 125 VDC |
| Temperature Range: | -55/+125°C |
| Temperature Coefficient: | COG |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC): | 30 ppm/C, 1MegaHz 1.0Vrms |
| Dissipation Factor: | 0.1% 1 MHz 1.0Vrms |
| Aging Rate: | 0% Loss/Decade Hour |
| Insulation Resistance: | 100 GOhms |

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