

## C1812C106K3PAC

SMD Comm X5R, Ceramic, 10 uF, 10%, 25 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1812



Click here for the 3D model.

| Dimensions |                 |  |
|------------|-----------------|--|
| Chip Size  | 1812            |  |
| L          | 4.5mm +/-0.3mm  |  |
| W          | 3.2mm +/-0.3mm  |  |
| Т          | 1.6mm +/-0.20mm |  |
| В          | 0.6mm +/-0.35mm |  |

| Packaging Specifications |           |
|--------------------------|-----------|
| Packaging                | Bulk, Bag |
| Packaging Quantity       | 1         |

| General Information |   |
|---------------------|---|
| Series              | SMD Comm X5R                            |
| Style               | SMD Chip                                |
| Description         | SMD, MLCC, Temperature Stable, Class II |
| Features            | Temperature Stable, Class II            |
| RoHS                | Yes                                     |
| Termination         | Tin                                     |
| Marking             | No                                      |
| AEC-Q200            | No                                      |
| Component Weight    | 185 mg                                  |
| Shelf Life          | 78 Weeks                                |
| MSL                 | 1                                       |

| Specifications   |  |
|--|--|
| Capacitance  | 10 uF  |
| Measurement Condition  | 1 kHz 1.0Vrms                                      |
| Capacitance Tolerance  | 10%  |
| Voltage DC   | 25 VDC   |
| Dielectric Withstanding Voltage                                    | 62.5 VDC   |
| Temperature Range  | -55/+85°C  |
| Temperature Coefficient  | X5R  |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms                                  |
| Dissipation Factor   | 10% 1 kHz 1.0Vrms                                  |
| Aging Rate   | 5% Loss/Decade Hour:<br>Referee Time is 1000 Hours |
| Insulation Resistance  | 50 MOhms   |

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