

SMD COTS COG, Ceramic, 0.15 uF, 1%, 100 VDC, COG, SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I, 1812



Dimensions	
Chip Size	1812
L	4.5mm +/-0.3mm
W	3.2mm +/-0.3mm
T	1.7mm +/-0.20mm
B	0.6mm +/-0.35mm

Packaging Specifications	
Packaging:	Bulk, Bag
Packaging Quantity:	1

General Information	
Series:	SMD COTS COG
Style:	SMD Chip
Description:	SMD, MLCC, COTS, Ultra-Stable, Low Loss, Class I
Features:	Ultra-Stable, Low Loss, Class I
RoHS:	No
Prop 65:	<b>WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">www.p65warnings.ca.gov</a> .
SCIP Number:	2d771165-5336-48a3-96fa-3663929fd828
Termination:	Lead (SnPb)
Marking:	No
Failure Rate:	Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A
AEC-Q200:	No
Component Weight:	87 mg
Shelf Life:	78 Weeks
MSL:	1

Specifications	
Capacitance:	0.15 uF
Measurement Condition:	1 kHz 1.0Vrms
Capacitance Tolerance:	1%
Voltage DC:	100 VDC
Dielectric Withstanding Voltage:	250 VDC
Temperature Range:	-55/+125°C
Temperature Coefficient:	COG
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	30 ppm/C, 1kHz 1.0Vrms
Dissipation Factor:	0.1% 1 kHz 1.0Vrms
Aging Rate:	0% Loss/Decade Hour
Insulation Resistance:	6.6667 GOhms

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