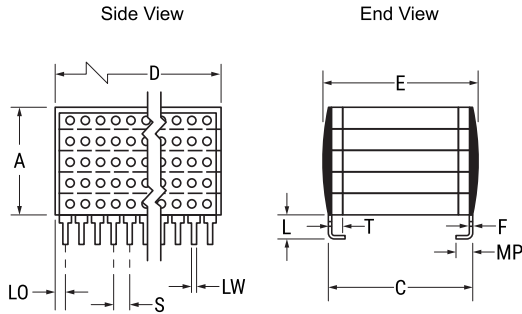


**KEMET Part Number: M49470Q01824KEJ**  
(L1QJ40C824KB36)

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KPS LDD Mil SMPS PRF49470, Ceramic, 0.82 uF, 10%, 500 VDC, BQ, N/A



**Dimensions**

D	9.845mm +/-0.955mm
L	1.78mm +/-0.25mm
T	1.397mm MAX
S	2.54mm TYP
F	0.254mm +/-0.051mm
A	9.144mm MAX
C	10.16mm +/-0.635mm
E	11.18mm MAX
LO	1.586mm MAX
LW	0.508mm +/-0.051mm
MP	1.27mm MIN

**Packaging Specifications**

<b>Packaging:</b>	Waffle, Box
<b>Packaging Quantity:</b>	36

**General Information**

<b>Series:</b>	KPS LDD Mil SMPS PRF49470
<b>Style:</b>	Leaded Stacked Chip
<b>Description:</b>	Low ESR, High Current Stacked Ceramic Chips
<b>Features:</b>	Low ESR, High Current
<b>RoHS:</b>	No
<b>Prop 65:</b>	<b>WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">www.p65warnings.ca.gov</a> .
<b>Termination:</b>	60/40 Solder Coated
<b>Lead:</b>	J Leads
<b>Failure Rate:</b>	N/A
<b>Testing and Reliability:</b>	Level B
<b>Qualifications:</b>	MIL-PRF-49470
<b>AEC-Q200:</b>	No
<b>Notes:</b>	Note: Number of chips in stack depends on design. Note: Turn Radius For Lead Extension Is 0.1 Radians (Typical). Note: Lead alignment within pin rows shall be within ±0.13 mm.

**Specifications**

<b>Capacitance:</b>	0.82 uF
<b>Capacitance Tolerance:</b>	10%
<b>Voltage DC:</b>	500 VDC
<b>Dielectric Withstanding Voltage:</b>	750 VDC
<b>Temperature Range:</b>	-55/+125°C
<b>Temperature Coefficient:</b>	BQ
<b>Insulation Resistance:</b>	1.219512 GOhms

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