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### Dimensions

D	10.7mm +/-0.5mm
L	8.5mm MAX
W	10.8mm +/-0.2mm
S	5mm NOM
G	0mm +0.3/-0.1mm
LT1	1.2mm +/-0.1mm
LT2	3.9mm +0.5mm

### Packaging Specifications

Lead	V-Chip
Packaging	T&R, 380mm

### General Information

Series	FCS
Description	Surface Mount Double Layer Capacitor
Features	Ultra Low ESR, High Power Pulse
RoHS	Yes
AEC-Q200	No
Component Weight	1.4 g

### Specifications

Capacitance	0.47 F
Capacitance Tolerance	-20/+80%
Voltage DC	3.5 VDC
Temperature Range	-25/+70°C
Rated Temperature	70°C
Resistance	50000 mOhms (1kHz)
Discharge Current	0.42 mAmps (30 Minutes After Charging)

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