



Click [here](#) for the 3D model.

Dimensions

| | |
|-----------|--|
| Chip Size | 2220 |
| L | 5.69mm +1.05/-0.41mm (0.224 in +0.041/-0.016 in) |
| W | 5mm +0.79/-0.41mm (0.197 in +0.031/-0.016 in) |
| T | 2.79mm MAX (0.11 in MAX) |
| B | 0.64mm +/-0.46mm (0.025 in +/-0.018 in) |

Packaging Specifications

| | |
|--------------------|--------|
| Packaging | Waffle |
| Packaging Quantity | 20 |

General Information

| | |
|----------------|--|
| Series | SMD MIL BP PRF32535 |
| Style | SMD Chip |
| Description | SMD, Low ESR, MIL-PRF-32535 |
| RoHS | No |
| Prop 65 | ⚠ WARNING: Cancer and reproductive harm - http://www.p65warnings.ca.gov |
| Termination | Solder Dipped |
| Failure Rate | N/A |
| Qualifications | MIL-PRF-32535 M-Level |
| AEC-Q200 | No |
| Shelf Life | 78 Weeks |
| MSL | 1 |

Specifications

| | |
|---------------------------------|---------------------|
| Capacitance | 0.1 uF |
| Capacitance Tolerance | 5% |
| Voltage DC | 100 VDC |
| Dielectric Withstanding Voltage | 250 VDC |
| Temperature Range | -55/+125°C |
| Temperature Coefficient | BP |
| Dissipation Factor | 0.25% 1 MHz 25C |
| Aging Rate | 0% Loss/Decade Hour |
| Insulation Resistance | 10 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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