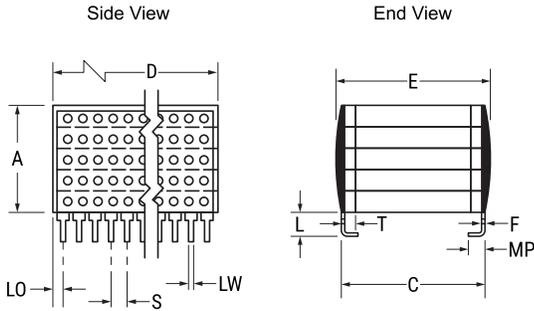


**KEMET Part Number: M49470X01105MAJ**  
(L1XJ505105MB12)

KPS LDD Mil SMPS PRF49470, Ceramic, 1 uF, 20%, 50 VDC, BX, N/A



**Dimensions**

<b>D</b>	6.335mm +/-0.645mm
<b>L</b>	1.78mm +/-0.25mm
<b>T</b>	1.397mm MAX
<b>S</b>	2.54mm TYP
<b>F</b>	0.254mm +/-0.051mm
<b>A</b>	3.048mm MAX
<b>C</b>	6.35mm +/-0.635mm
<b>E</b>	7.62mm MAX
<b>LO</b>	1.586mm MAX
<b>LW</b>	0.508mm +/-0.051mm
<b>MP</b>	1.27mm MIN

**Packaging Specifications**

<b>Packaging:</b>	Waffle, Box
<b>Packaging Quantity:</b>	64

**General Information**

<b>Series:</b>	KPS LDD Mil SMPS PRF49470
<b>Style:</b>	Leaded Stacked Chip
<b>Description:</b>	Low ESR, High Current Stacked Ceramic Chips
<b>Features:</b>	Low ESR, High Current
<b>RoHS:</b>	No
<b>Prop 65:</b>	<b>WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">www.p65warnings.ca.gov</a> .
<b>SCIP Number:</b>	2499890a-0e07-42ff-98a1-bd02d3b7c2ec
<b>Termination:</b>	60/40 Solder Coated
<b>Lead:</b>	J Leads
<b>Failure Rate:</b>	N/A
<b>Testing and Reliability:</b>	Level B
<b>Qualifications:</b>	MIL-PRF-49470
<b>AEC-Q200:</b>	No
<b>Notes:</b>	Note: Number of chips in stack depends on design. Note: Turn Radius For Lead Extension Is 0.1 Radians (Typical). Note: Lead alignment within pin rows shall be within ±0.13 mm.

**Specifications**

<b>Capacitance:</b>	1 uF
<b>Capacitance Tolerance:</b>	20%
<b>Voltage DC:</b>	50 VDC
<b>Dielectric Withstanding Voltage:</b>	125 VDC
<b>Temperature Range:</b>	-55/+125°C
<b>Temperature Coefficient:</b>	BX
<b>Dissipation Factor:</b>	2.5%
<b>Insulation Resistance:</b>	1 GOhms

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