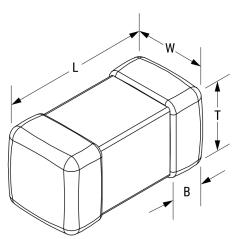


## L1007C6R8MPWST

Aliases (L1007C6R8MPWST)

KEMET, L-PWS, Power, 6.8 uH, 20%



Click here for the 3D model.

| Dimensions |                |
|------------|----------------|
| Chip Size  | 1007           |
| L          | 2.5mm +/-0.2mm |
| W          | 1.8mm +/-0.2mm |
| Т          | 1.8mm +/-0.2mm |
| LL         | 0.5mm +/-0.2mm |
| В          | 0.5mm +/-0.2mm |

| Packaging Specifications |      |
|--------------------------|------|
| Packaging                | T&R  |
| Packaging Quantity       | 2000 |

| General Information |                        |
|---------------------|------------------------|
| Series              | L-PWS                  |
| Style               | SMD Chip               |
| Description         | Surface Mount Inductor |
| Features            | Standard Type          |
| RoHS                | Yes                    |

| Specifications          |                   |
|-------------------------|-------------------|
| Inductance              | 6.8 uH (7.96 MHz) |
| Inductance Tolerance    | 20%               |
| Temperature Range       | -40/+105°C        |
| Current                 | 195 mAmps (MAX)   |
| DC Resistance           | 0.15 Ohms         |
| Self-Resonant Frequency | 38 MHz            |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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## >>KEMET(基美)