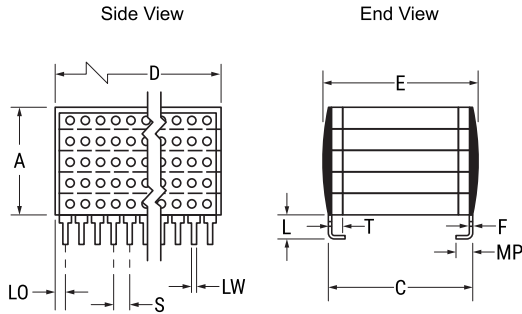


KEMET Part Number: T49470Q01824KEJ
(L1QJ40C824KT36)

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KPS LDD Mil SMPS PRF49470, Ceramic, 0.82 uF, 10%, 500 VDC, BQ, N/A



Dimensions

D	9.845mm +/-0.955mm
L	1.78mm +/-0.25mm
T	1.397mm MAX
S	2.54mm TYP
F	0.254mm +/-0.051mm
A	9.144mm MAX
C	10.16mm +/-0.635mm
E	11.18mm MAX
LO	1.586mm MAX
LW	0.508mm +/-0.051mm
MP	1.27mm MIN

Packaging Specifications

Packaging:	Waffle, Box
Packaging Quantity:	36

General Information

Series:	KPS LDD Mil SMPS PRF49470
Style:	Leaded Stacked Chip
Description:	Low ESR, High Current Stacked Ceramic Chips
Features:	Low ESR, High Current
RoHS:	No
Prop 65:	WARNING: Cancer and reproductive harm - www.p65warnings.ca.gov .
Termination:	60/40 Solder Coated
Lead:	J Leads
Failure Rate:	N/A
Testing and Reliability:	Level T
Qualifications:	MIL-PRF-49470
AEC-Q200:	No
Notes:	Note: Number of chips in stack depends on design. Note: Turn Radius For Lead Extension Is 0.1 Radians (Typical). Note: Lead alignment within pin rows shall be within ±0.13 mm.

Specifications

Capacitance:	0.82 uF
Capacitance Tolerance:	10%
Voltage DC:	500 VDC
Dielectric Withstanding Voltage:	750 VDC
Temperature Range:	-55/+125°C
Temperature Coefficient:	BQ
Insulation Resistance:	1.219512 GOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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单击下面可查看定价，库存，交付和生命周期等信息

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