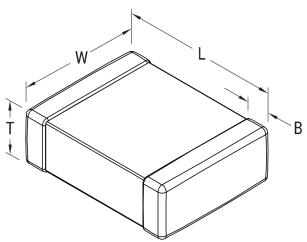


C1206C474K3PACTU

Aliases (C1206C474K3PAC7800)

SMD Comm X5R, Ceramic, 0.47 uF, 10%, 25 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1206



Click here for the 3D model.

| Dimensions | |
|------------|-----------------|
| Chip Size | 1206 |
| L | 3.2mm +/-0.2mm |
| W | 1.6mm +/-0.2mm |
| T | 0.9mm +/-0.10mm |
| В | 0.5mm +/-0.25mm |

| Packaging Specifications | |
|--------------------------|--------------------------|
| Packaging | T&R, 180mm, Plastic Tape |
| Packaging Quantity | 4000 |

| General Information | |
|---------------------|---|
| Series | SMD Comm X5R |
| Style | SMD Chip |
| Description | SMD, MLCC, Temperature Stable, Class II |
| Features | Temperature Stable, Class II |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| AEC-Q200 | No |
| Component Weight | 25 mg |
| Shelf Life | 78 Weeks |
| MSL | 1 |

| Specifications | |
|--|--|
| Capacitance | 0.47 uF |
| Measurement Condition | 1 kHz 1.0Vrms |
| Capacitance Tolerance | 10% |
| Voltage DC | 25 VDC |
| Dielectric Withstanding Voltage | 62.5 VDC |
| Temperature Range | -55/+85°C |
| Temperature Coefficient | X5R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC) | 15%, 1kHz 1.0Vrms |
| Dissipation Factor | 5% 1 kHz 1.0Vrms |
| Aging Rate | 5% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance | 1.0638 GOhms |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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