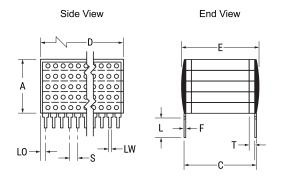
## KEMET Part Number: L1XN401685MC48

(87106080)



## KPS LDD Comm SMPS, Ceramic, 6.8 uF, 20%, 100 VDC, BX, N/A



Dimensions	
D	9.845mm +/-0.955mm
L	6.35mm MIN
Т	1.397mm MAX
S	2.54mm TYP
F	0.254mm +/-0.051mm
Α	12.192mm MAX
С	10.16mm +/-0.635mm
E	11.18mm +/-0.25mm
G	1.4mm +/-0.254mm
LO	1.586mm MAX
LW	0.508mm +/-0.051mm

Packaging Specifications		
Packaging:	Waffle, Box	
Packaging Quantity:	36	

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General Information		
Series:	KPS LDD Comm SMPS	
Style:	Leaded Stacked Chip	
Description:	Low ESR, High Current Stacked Ceramic Chips	
Features:	Low ESR, High Current	
RoHS:	No	
Prop 65:	WARNING: Cancer and reproductive harm - www.p65warnings.ca.gov.	
SCIP Number:	df2a2509-5a11-4b26-944d-42278323e	ecf4
Termination:	60/40 Solder Coated	
Lead:	Wire Leads	
Failure Rate:	N/A	
Testing and Reliability:	DSCC87106	
AEC-Q200:	No	
Notes:	Note: Number of chips in stack depends on design. Number of Chips in this stack = 4. Note: Lead alignment within pin rows shall be within ±.0.13 mm.	

Specifications		
Capacitance:	6.8 uF	
Capacitance Tolerance:	20%	
Voltage DC:	100 VDC	
Dielectric Withstanding Voltage:	250 VDC	
Temperature Range:	-55/+125°C	
Temperature Coefficient:	BX	
Dissipation Factor:	2.5% 1 kHz 25C	
Aging Rate:	1% Loss/Decade Hour	
Insulation Resistance:	14.7 GOhms	

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.



单击下面可查看定价,库存,交付和生命周期等信息

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