

KEMET Part Number: C1825T105K5RCLTU
(C1825T105K5RCL7800)



SMD COTS X7R, Ceramic, 1 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1825




Dimensions

| Chip Size | 1825 |
|-----------|-----------------|
| L | 4.5mm +/-0.3mm |
| W | 6.4mm +/-0.4mm |
| T | 1.5mm +/-0.15mm |
| B | 0.6mm +/-0.35mm |

Packaging Specifications

| Packaging: | T&R, 180mm, Plastic Tape |
|---------------------|--------------------------|
| Packaging Quantity: | 1000 |

General Information

| Series: | SMD COTS X7R |
|-------------------|---|
| Style: | SMD Chip |
| Description: | SMD, MLCC, COTS, Temperature Stable, Class II |
| Features: | Temperature Stable, Class II |
| RoHS: | No |
| Prop 65: |  WARNING: Cancer and reproductive harm - www.p65warnings.ca.gov |
| SCIP Number: | 2d771165-5336-48a3-96fa-3663929fd828 |
| Termination: | Lead (SnPb) |
| Marking: | No |
| Failure Rate: | Testing per MIL-PRF-55681 PDA 8%, DPA per EIA-469, Humidity per MIL-STD-202, Method 103, Condition A |
| AEC-Q200: | No |
| Component Weight: | 240 mg |
| Shelf Life: | 78 Weeks |
| MSL: | 1 |

Specifications

| Capacitance: | 1 uF |
|---|---|
| Measurement Condition: | 1 kHz 1.0Vrms |
| Capacitance Tolerance: | 10% |
| Voltage DC: | 50 VDC |
| Dielectric Withstanding Voltage: | 125 VDC |
| Temperature Range: | -55/+125°C |
| Temperature Coefficient: | X7R |
| Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC): | 15%, 1kHz 1.0Vrms |
| Dissipation Factor: | 2.5% 1 kHz 1.0Vrms |
| Aging Rate: | 3% Loss/Decade Hour: Referee Time is 1000 Hours |
| Insulation Resistance: | 1 GOhms |

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