



Click [here](#) for the 3D model.

| Dimensions |   |
|------------|---|
| Chip Size  | 1812                                    |
| L          | 4.57mm +/-0.38mm (0.18 in +/-0.015 in)  |
| W          | 3.18mm +/-0.38mm (0.125 in +/-0.015 in) |
| T          | 2.03mm MAX (0.08 in MAX)                |
| B          | 0.51mm +/-0.25mm (0.02 in +/-0.01 in)   |

| Packaging Specifications |           |
|--------------------------|-----------|
| Packaging                | Bulk, Bag |

| General Information     |                 |
|-------------------------|-----------------|
| Series                  | GR900 SMD Space |
| Style                   | SMD Chip        |
| RoHS                    | Yes             |
| Termination             | Tin             |
| Failure Rate            | N/A             |
| Testing and Reliability | GR900 CSAM A    |
| AEC-Q200                | No              |
| Shelf Life              | 78 Weeks        |
| MSL                     | 1               |

| Specifications          |            |
|-------------------------|------------|
| Capacitance             | 6200 pF    |
| Capacitance Tolerance   | 1%         |
| Voltage DC              | 100 VDC    |
| Temperature Range       | -55/+125°C |
| Temperature Coefficient | BP         |

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.

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